

Ahmed M Eltawil

List of Publications by Year in descending order

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241
papers

3,292
citations

218677

26
h-index

233421

45
g-index

251
all docs

251
docs citations

251
times ranked

2381
citing authors

#	ARTICLE	IF	CITATIONS
1	All-Digital Self-Interference Cancellation Technique for Full-Duplex Systems. IEEE Transactions on Wireless Communications, 2015, 14, 3519-3532.	9.2	352
2	Optimizations of a MIMO Relay Network. IEEE Transactions on Signal Processing, 2008, 56, 5062-5073.	5.3	234
3	Rate Gain Region and Design Tradeoffs for Full-Duplex Wireless Communications. IEEE Transactions on Wireless Communications, 2013, 12, 3556-3565.	9.2	106
4	A Beam-Steering Reconfigurable Antenna for WLAN Applications. IEEE Transactions on Antennas and Propagation, 2015, 63, 24-32.	5.1	101
5	Self-interference cancellation with nonlinear distortion suppression for full-duplex systems. , 2013, , .		98
6	Neural Coding in Spiking Neural Networks: A Comparative Study for Robust Neuromorphic Systems. Frontiers in Neuroscience, 2021, 15, 638474.	2.8	89
7	Memristor Multiport Readout: A Closed-Form Solution for Sneak Paths. IEEE Nanotechnology Magazine, 2014, 13, 274-282.	2.0	73
8	Design and Implementation of a Sort-Free K-Best Sphere Decoder. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2010, 18, 1497-1501.	3.1	61
9	Linear Decentralized Estimation of Correlated Data for Power-Constrained Wireless Sensor Networks. IEEE Transactions on Signal Processing, 2012, 60, 6003-6016.	5.3	61
10	A Radius Adaptive K-Best Decoder With Early Termination: Algorithm and VLSI Architecture. IEEE Transactions on Circuits and Systems I: Regular Papers, 2010, 57, 2476-2486.	5.4	56
11	Modeling and Analysis of Passive Switching Crossbar Arrays. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 270-282.	5.4	55
12	On Phase Noise Suppression in Full-Duplex Systems. IEEE Transactions on Wireless Communications, 2015, 14, 1237-1251.	9.2	48
13	Design and Implementation of a Scalable Channel Emulator for Wideband MIMO Systems. IEEE Transactions on Vehicular Technology, 2009, 58, 4698-4709.	6.3	39
14	Self-interference cancellation with phase noise induced ICI suppression for full-duplex systems. , 2013, , .		38
15	Hardware- and Interference-Limited Cognitive IoT Relaying NOMA Networks With Imperfect SIC Over Generalized Non-Homogeneous Fading Channels. IEEE Access, 2020, 8, 72942-72956.	4.2	36
16	The Internet of Bodies: A Systematic Survey on Propagation Characterization and Channel Modeling. IEEE Internet of Things Journal, 2022, 9, 321-345.	8.7	36
17	Architectural Optimizations for Low-Power K-Best MIMO Decoders. IEEE Transactions on Vehicular Technology, 2009, 58, 3145-3153.	6.3	35
18	Low-Power Multimedia System Design by Aggressive Voltage Scaling. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2010, 18, 852-856.	3.1	35

#	ARTICLE	IF	CITATIONS
19	Amplify-and-Forward Relay Networks Under Received Power Constraint. IEEE Transactions on Wireless Communications, 2009, 8, 5422-5426.	9.2	34
20	Cross Layer Error Exploitation for Aggressive Voltage Scaling. , 2007, , .		33
21	A flexible capacitive photoreceptor for the biomimetic retina. Light: Science and Applications, 2022, 11, 3.	16.6	33
22	Supercapacitor discharge under constant resistance, constant current and constant power loads. Journal of Power Sources, 2019, 435, 226829.	7.8	31
23	Mask Technique for Fast and Efficient Training of Binary Resistive Crossbar Arrays. IEEE Nanotechnology Magazine, 2019, 18, 704-716.	2.0	31
24	Decentralized Estimation Under Correlated Noise. IEEE Transactions on Signal Processing, 2014, 62, 5603-5614.	5.3	30
25	AS8â€static random access memory (SRAM): asymmetric SRAM architecture for soft error hardening enhancement. IET Circuits, Devices and Systems, 2017, 11, 89-94.	1.4	30
26	A Machine Learning Approach for Structural Health Monitoring Using Noisy Data Sets. IEEE Transactions on Automation Science and Engineering, 2020, 17, 900-908.	5.2	30
27	System-Level SRAM Yield Enhancement. , 0, , .		29
28	Simultaneous transmit and sense for cognitive radios using full-duplex: A first study. , 2012, , .		29
29	E < MC2. , 2010, , .		28
30	Full-Duplex Systems Using Multireconfigurable Antennas. IEEE Transactions on Wireless Communications, 2015, 14, 5971-5983.	9.2	28
31	A Best-First Soft/Hard Decision Tree Searching MIMO Decoder for a 4 \$imes\$ 4 64-QAM System. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2012, 20, 1537-1541.	3.1	27
32	Balancing Spectral Efficiency, Energy Consumption, and Fairness in Future Heterogeneous Wireless Systems with Reconfigurable Devices. IEEE Journal on Selected Areas in Communications, 2013, 31, 969-980.	14.0	26
33	A fault tolerant cache architecture for sub 500mV operation. , 2009, , .		25
34	Reduced Overhead Training for Multi Reconfigurable Antennas with Beam-Tilting Capability. IEEE Transactions on Wireless Communications, 2010, 9, 3810-3821.	9.2	25
35	Learning to Predict IR Drop with Effective Training for ReRAM-based Neural Network Hardware. , 2020, , .		25
36	On Channel Estimation and Capacity for Amplify and Forward Relay Networks. , 2008, , .		24

#	ARTICLE	IF	CITATIONS
37	Deep Learning-Based Frequency-Selective Channel Estimation for Hybrid mmWave MIMO Systems. IEEE Transactions on Wireless Communications, 2022, 21, 3804-3821.	9.2	23
38	A combined channel and hardware noise resilient Viterbi decoder. , 2010, , .		22
39	UAV-Assisted Cooperative & Cognitive NOMA: Deployment, Clustering, and Resource Allocation. IEEE Transactions on Cognitive Communications and Networking, 2022, 8, 263-281.	7.9	22
40	IR-QNN Framework: An IR Drop-Aware Offline Training of Quantized Crossbar Arrays. IEEE Access, 2020, 8, 228392-228408.	4.2	21
41	A Hybrid Approximate Computing Approach for Associative In-Memory Processors. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2018, 8, 758-769.	3.6	20
42	A Low Power JPEG2000 Encoder With Iterative and Fault Tolerant Error Concealment. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2009, 17, 827-837.	3.1	18
43	Process Variation Aware SRAM/Cache for aggressive voltage-frequency scaling. , 2009, , .		18
44	Approximate Memristive In-memory Computing. Transactions on Embedded Computing Systems, 2017, 16, 1-18.	2.9	18
45	On-Chip Error-Triggered Learning of Multi-Layer Memristive Spiking Neural Networks. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2020, 10, 522-535.	3.6	18
46	Performance of NOMA-Enabled Cognitive Satellite-Terrestrial Networks With Non-Ideal System Limitations. IEEE Access, 2021, 9, 35932-35946.	4.2	18
47	Hardware and Interference Limited Cooperative CR-NOMA Networks Under Imperfect SIC and CSI. IEEE Open Journal of the Communications Society, 2021, 2, 1473-1485.	6.9	18
48	Toward the Optimal Design and FPGA Implementation of Spiking Neural Networks. IEEE Transactions on Neural Networks and Learning Systems, 2022, 33, 3988-4002.	11.3	18
49	Cognitive Radio Rides on the Cloud. , 2010, , .		17
50	A Unified Hardware and Channel Noise Model for Communication Systems. , 2010, , .		17
51	Inquisitive Defect Cache: A Means of Combating Manufacturing Induced Process Variation. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2011, 19, 1597-1609.	3.1	17
52	Joint Detection and Decoding for MIMO Systems Using Convolutional Codes: Algorithm and VLSI Architecture. IEEE Transactions on Circuits and Systems I: Regular Papers, 2012, 59, 1919-1931.	5.4	17
53	Accuracy Limits of Embedded Smart Device Accelerometer Sensors. IEEE Transactions on Instrumentation and Measurement, 2020, 69, 5488-5496.	4.7	16
54	Dual antenna UMTS mobile station transceiver ASIC for 2 Mb/s data rate. , 0, , .		15

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55	Design and VLSI Implementation for a WCDMA Multipath Searcher. IEEE Transactions on Vehicular Technology, 2005, 54, 889-902.	6.3	15
56	Fast error aware model for arithmetic and logic circuits. , 2012, , .		15
57	Frequency and Timing Synchronization for In-Band Full-Duplex OFDM System. , 2017, , .		15
58	Error-triggered Three-Factor Learning Dynamics for Crossbar Arrays. , 2020, , .		15
59	A Two-Dimensional Associative Processor. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 1659-1670.	3.1	14
60	An Ultra-Area-Efficient 1024-Point In-Memory FFT Processor. Micromachines, 2019, 10, 509.	2.9	14
61	Independent Component Analysis Using RRAMs. IEEE Nanotechnology Magazine, 2019, 18, 611-615.	2.0	14
62	Power Management for Cognitive Radio Platforms. , 2007, , .		13
63	A Class of Low Power Error Compensation Iterative Decoders. , 2011, , .		13
64	An Adaptive Reduced Complexity K-Best Decoding Algorithm with Early Termination. , 2010, , .		12
65	Overcoming Crossbar Nonidealities in Binary Neural Networks Through Learning. , 2018, , .		12
66	Unsupervised Adaptive Weight Pruning for Energy-Efficient Neuromorphic Systems. Frontiers in Neuroscience, 2020, 14, 598876.	2.8	12
67	Improving effective yield through error tolerant system design. , 2005, , .		11
68	Optimized scheduling algorithm for LTE downlink system. , 2012, , .		11
69	Algorithms and Architectures of Energy-Efficient Error-Resilient MIMO Detectors for Memory-Dominated Wireless Communication Systems. IEEE Transactions on Circuits and Systems I: Regular Papers, 2014, 61, 2159-2171.	5.4	11
70	Physical Multi-Layer Phantoms for Intra-Body Communications. IEEE Access, 2018, 6, 42812-42821.	4.2	11
71	Towards Efficient Neuromorphic Hardware: Unsupervised Adaptive Neuron Pruning. Electronics (Switzerland), 2020, 9, 1059.	3.1	11
72	Spiking neural networks for inference and learning: a memristor-based design perspective. , 2020, , 499-530.		11

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73	Application of ICA on Self-Interference Cancellation of In-Band Full Duplex Systems. IEEE Wireless Communications Letters, 2020, 9, 924-927.	5.0	11
74	Limits on voltage scaling for caches utilizing fault tolerant techniques. , 2007, , .		10
75	Size-Reconfiguration Delay Tradeoffs for a Class of DSP Blocks in Multi-mode Communication Systems. , 2009, , .		10
76	DWT-based watermarking technique for video authentication. , 2015, , .		10
77	Design and analysis of 2T-2M Ternary content addressable memories. , 2017, , .		10
78	Performance Analysis and Enhancements for In-Band Full-Duplex Wireless Local Area Networks. IEEE Access, 2020, 8, 111636-111652.	4.2	10
79	Cost- and Dataset-free Stuck-at Fault Mitigation for ReRAM-based Deep Learning Accelerators. , 2021, , .		10
80	Interpolation based direct digital frequency synthesis for wireless communications. , 0, , .		9
81	Wireless field trial results of a high hopping rate FHSS-FSK testbed. IEEE Journal on Selected Areas in Communications, 2005, 23, 1113-1122.	14.0	9
82	Joint Power Loading of Data and Pilots in OFDM Using Imperfect Channel State Information at the Transmitter. , 2008, , .		9
83	Link Performance Improvement Using Reconfigurable Multiantenna Systems. IEEE Antennas and Wireless Propagation Letters, 2009, 8, 873-876.	4.0	9
84	Using Reconfigurable Devices to Maximize Spectral Efficiency in Future Heterogeneous Wireless Systems. , 2011, , .		9
85	Linear Estimation of Correlated Vector Sources for Wireless Sensor Networks with Fusion Center. IEEE Wireless Communications Letters, 2012, 1, 400-403.	5.0	9
86	Intra-body communication model based on variable biological parameters. , 2015, , .		9
87	Design Exploration of Sensing Techniques in 2T-2R Resistive Ternary CAMs. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 762-766.	3.0	9
88	Plant stem tissue modeling and parameter identification using metaheuristic optimization algorithms. Scientific Reports, 2022, 12, 3992.	3.3	9
89	System Redundancy; A Means of Improving Process Variation Yield Degradation in Memory Arrays. , 2006, , .		8
90	Design and Implementation of a Baseband WCDMA Dual-Antenna Mobile Terminal. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2007, 54, 518-529.	0.1	8

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91	Error-Aware Design. , 2007, , .		8
92	Energy aware task mapping algorithm for heterogeneous MPSoC based architectures. , 2011, , .		8
93	Embedded Memories Fault-Tolerant Pre- and Post-Silicon Optimization. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2011, 19, 1916-1921.	3.1	8
94	Equi-Noise: A Statistical Model That Combines Embedded Memory Failures and Channel Noise. IEEE Transactions on Circuits and Systems I: Regular Papers, 2014, 61, 407-419.	5.4	8
95	Reliability Enhancement of Low-Power Sequential Circuits Using Reconfigurable Pulsed Latches. IEEE Transactions on Circuits and Systems I: Regular Papers, 2017, 64, 1803-1814.	5.4	8
96	Post-Disaster Structural Health Monitoring System Using Personal Mobile-Phones. , 2019, , .		8
97	Effect of Sensor Error on the Assessment of Seismic Building Damage. IEEE Transactions on Instrumentation and Measurement, 2020, 69, 573-584.	4.7	8
98	Blind Source Separation For Full-Duplex Systems: Potential and Challenges. IEEE Open Journal of the Communications Society, 2021, 2, 1379-1389.	6.9	8
99	Enabling the Internet of Bodies Through Capacitive Body Channel Access Schemes. IEEE Internet of Things Journal, 2022, 9, 15901-15914.	8.7	8
100	A low-power DS-CDMA RAKE receiver utilizing resource allocation techniques. IEEE Journal of Solid-State Circuits, 2004, 39, 1321-1330.	5.4	7
101	On Signal Processing Methods for MIMO Relay Architectures. , 2007, , .		7
102	Fault Tolerant Approaches Targeting Ultra Low Power Communications System Design. IEEE Vehicular Technology Conference, 2007, , .	0.4	7
103	EVALUATION FRAMEWORK FOR K-BEST SPHERE DECODERS. Journal of Circuits, Systems and Computers, 2010, 19, 975-995.	1.5	7
104	Reconfigurable filter implementation of a matched-filter based spectrum sensor for Cognitive Radio systems. , 2011, , .		7
105	Variation Trained Drowsy Cache (VTD-Cache): A History Trained Variation Aware Drowsy Cache for Fine Grain Voltage Scaling. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2012, 20, 630-642.	3.1	7
106	A Simple Full-Duplex MAC Protocol Exploiting Asymmetric Traffic Loads in WiFi Systems. , 2017, , .		7
107	Dynamic behavior of a smart device on a surface subjected to earthquake motion. Earthquake Engineering and Structural Dynamics, 2018, 47, 1905-1920.	4.4	7
108	Non-Stationary Polar Codes for Resistive Memories. , 2019, , .		7

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109	NOMA/OMA Mode Selection and Resource Allocation for Beyond 5G Networks. , 2020, , .		7
110	IMCA: An Efficient In-Memory Convolution Accelerator. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2021, 29, 447-460.	3.1	7
111	Community-Based Multi-Sensory Structural Health Monitoring System: A Smartphone Accelerometer and Camera Fusion Approach. IEEE Sensors Journal, 2021, 21, 20539-20551.	4.7	7
112	A Scalable Wireless Channel Emulator for Broadband MIMO Systems. , 2007, , .		6
113	Cross-layer co-exploration of exploiting error resilience for video over wireless applications. , 2008, , .		6
114	Spectral efficiency and energy consumption tradeoffs for reconfigurable devices in heterogeneous wireless systems. , 2012, , .		6
115	Error resilient MIMO detector for memory-dominated wireless communication systems. , 2012, , .		6
116	A survey of cross-layer power-reliability tradeoffs in multi and many core systems-on-chip. Microprocessors and Microsystems, 2013, 37, 760-771.	2.8	6
117	The Feasibility of Using Smart Devices for Quantifying Seismic Damage to Buildings. , 2017, , .		6
118	Rapid in-memory matrix multiplication using associative processor. , 2018, , .		6
119	Efficient Acceleration of Stencil Applications through In-Memory Computing. Micromachines, 2020, 11, 622.	2.9	6
120	Automated Assessment of Building Damage from Seismic Events Using Smartphones. Journal of Structural Engineering, 2020, 146, 04020076.	3.4	6
121	Wireless Communication Technologies in Omnidirectional CubeSat Crosslink: Feasibility Study and Performance Analysis. IEEE Journal on Miniaturization for Air and Space Systems, 2021, 2, 157-166.	2.7	6
122	Performance of NOMA-Based mmWave D2D Networks Under Practical System Conditions. IEEE Access, 2021, 9, 160958-160974.	4.2	6
123	Energy Efficient Capacitive Body Channel Access Schemes for Internet of Bodies. , 2021, , .		6
124	In-Memory Associative Processors: Tutorial, Potential, and Challenges. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 2641-2647.	3.0	6
125	Exploiting Fault Tolerance Towards Power Efficient Wireless Multimedia Applications. , 2007, , .		5
126	A partial memory protection scheme for higher effective yield of embedded memory for video data. , 2008, , .		5

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127	A best-first tree-searching approach for ML decoding in MIMO system. , 2010, , .		5
128	Multiuser Sum MSE Minimization Relaying Strategy. , 2011, , .		5
129	Heterogeneous memory management for 3D-DRAM and external DRAM with QoS. , 2013, , .		5
130	High SNR Linear Estimation of Vector Sources. IEEE Wireless Communications Letters, 2014, 3, 581-584.	5.0	5
131	Collision Tolerance and Throughput Gain in Full-Duplex IEEE 802.11 DCF. , 2018, , .		5
132	Power Performance Tradeoffs Using Adaptive Bit Width Adjustments on Resistive Associative Processors. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 302-312.	5.4	5
133	Effect of Asymmetric Nonlinearity Dynamics in RRAMs on Spiking Neural Network Performance. , 2019, , .		5
134	Hybrid pyramid-DWT-SVD dual data hiding technique for videos ownership protection. Multimedia Tools and Applications, 2019, 78, 14511-14547.	3.9	5
135	A hardware/software co-design methodology for in-memory processors. Journal of Parallel and Distributed Computing, 2022, 161, 63-71.	4.1	5
136	Piece-wise parabolic interpolation for direct digital frequency synthesis. , 0, , .		4
137	FFT processing through faulty memories in OFDM based systems. , 2010, , .		4
138	Process variation aware transcoding for low power H.264 decoding. , 2010, , .		4
139	Reliability-aware placement in SRAM-based FPGA for voltage scaling realization in the presence of process variations. , 2011, , .		4
140	Error-Aware Algorithm/Architecture Coexploration for Video Over Wireless Applications. Transactions on Embedded Computing Systems, 2012, 11S, 1-23.	2.9	4
141	History & Variation Trained Cache (HVT-Cache): A process variation aware and fine grain voltage scalable cache with active access history monitoring. , 2012, , .		4
142	Joint Power Management and Adaptive Modulation and Coding for Wireless Communications Systems With Unreliable Buffering Memories. IEEE Transactions on Circuits and Systems I: Regular Papers, 2014, 61, 2456-2465.	5.4	4
143	Process variations-aware resistive associative processor design. , 2016, , .		4
144	Practical Framework for Downlink MU-MIMO for LTE Systems. IEEE Wireless Communications Letters, 2017, 6, 314-317.	5.0	4

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145	Active Cancellation of Self-Interference for Full-Duplex Amplify and Forward Wi-Fi Relay. IEEE Wireless Communications Letters, 2018, 7, 1050-1053.	5.0	4
146	Power optimization techniques for associative processors. Journal of Systems Architecture, 2018, 90, 44-53.	4.3	4
147	Feasibility of Utilizing Smart-phone Cameras for Seismic Structural Damage Detection. , 2020, , .		4
148	Practical Considerations for Full Duplex Enabled 5G Integrated Access and Backhaul. Journal of Signal Processing Systems, 2020, 92, 465-474.	2.1	4
149	Cognitive Non-ideal NOMA Satellite-Terrestrial Networks with Channel and Hardware Imperfections. , 2021, , .		4
150	Capacity Analysis of Wireless Powered Cooperative NOMA Networks over Generalized Fading. , 2021, , .		4
151	Stick-Slip Classification Based on Machine Learning Techniques for Building Damage Assessment. Journal of Earthquake Engineering, 2022, 26, 5848-5865.	2.5	4
152	Coverage Analysis of CR-based Satellite-Terrestrial NOMA Networks with Practical System Impairments. , 2021, , .		4
153	Design and Analysis of Low Power Image Filters Toward Defect-Resilient Embedded Memories for Multimedia SoCs. Lecture Notes in Computer Science, 2006, , 295-308.	1.3	4
154	Electrode Impedance Modeling for Channel Characterization for Intra-body Communication. Internet of Things, 2019, , 253-266.	1.7	4
155	An Alternative Organization of Defect Map for Defect-Resilient Embedded On-Chip Memories. Lecture Notes in Computer Science, 2007, , 102-113.	1.3	4
156	Full-Duplex Self Cancellation Techniques Using Independent Component Analysis. , 2020, , .		4
157	Performance limits of wireless powered cooperative NOMA over generalized fading. Transactions on Emerging Telecommunications Technologies, 0, , .	3.9	4
158	A low-power ASIC implementation of 2Mbps antenna-rake combiner for WCDMA with MRC and LMS capabilities. , 0, , .		3
159	Demonstration of highly programmable downlink OFDMA (WiMax) transceivers for SDR systems. , 2009, , .		3
160	Low overhead correction scheme for unreliable LDPC buffering. , 2013, , .		3
161	An interference cancellation strategy for broadcast in hierarchical cell structure. , 2014, , .		3
162	State dependent statistical timing model for voltage scaled circuits. , 2014, , .		3

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163	Link adaptation for wireless systems. <i>Wireless Communications and Mobile Computing</i> , 2014, 14, 1509-1521.	1.2	3
164	Multicopy Cache. <i>Transactions on Embedded Computing Systems</i> , 2014, 13, 1-27.	2.9	3
165	Efficient pulsed-latch implementation for multiport register files. , 2017, , .		3
166	On one step row readout technique of selector-less resistive arrays. , 2017, , .		3
167	Simple MOS Transistor-Based Realization of Fractional-Order Capacitors. , 2019, , .		3
168	Identifying Stick-Slip Characteristics of a Smart Device on a Seismically Excited Surface Using On-Board Sensors. <i>Journal of Earthquake Engineering</i> , 2019, , 1-19.	2.5	3
169	Throughput Characterization for Bluetooth Low Energy with Applications in Body Area Networks. , 2020, , .		3
170	Programmable constant phase element realization with crossbar arrays. <i>Journal of Advanced Research</i> , 2021, 29, 137-145.	9.5	3
171	Outage Analysis of EH-based Cooperative NOMA Networks over Generalized Statistical Models. , 2021, , .		3
172	Parameter Estimation of Two Spiking Neuron Models With Meta-Heuristic Optimization Algorithms. <i>Frontiers in Neuroinformatics</i> , 2022, 16, 771730.	2.5	3
173	Diversity processing WCDMA cell searcher implementation. , 0, , .		2
174	Managing leakage power and reliability in hot chips using system floorplanning and SRAM design. , 2008, , .		2
175	TRAM: A tool for Temperature and Reliability Aware Memory Design. , 2009, , .		2
176	Placement-aware partial reconfiguration for a class of FIR-like structures. , 2010, , .		2
177	Distributed detection for wireless sensor networks with fusion center under correlated noise. , 2014, , .		2
178	Energy Aware Mapping for Reconfigurable Wireless MPSoCs. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2015, 23, 392-396.	3.1	2
179	A System-Level Exploration of Power Delivery Architectures for Near-Threshold Manycores Considering Performance Constraints. , 2016, , .		2
180	Performance analysis of full-duplex multiuser decode-and-forward relay networks with interference management. , 2016, , .		2

#	ARTICLE	IF	CITATIONS
181	Poster Abstract: Unifying Modeling Substrate for Irrigation Cyber-Physical Systems. , 2016, , .		2
182	Low Latency Approximate Adder for Highly Correlated Input Streams. , 2017, , .		2
183	Low-Power Resistive Associative Processor Implementation Through the Multi-Compare. , 2018, , .		2
184	Scheduling and power adaptation for wireless local area networks with full-duplex capability. Transactions on Emerging Telecommunications Technologies, 2018, 29, e3451.	3.9	2
185	IBCFAP: Intra-Body Communications Five-Layers Arm Phantom Model. IEEE Access, 2019, 7, 93701-93710.	4.2	2
186	Design and Implementation of an End-to-End Amplify and Forward Full-Duplex Relay Network. IEEE Access, 2020, 8, 163594-163607.	4.2	2
187	Networking research for the Arab world. Communications of the ACM, 2021, 64, 114-119.	4.5	2
188	A Comparative Study of Different Human Skin Impedance Models. , 2021, , .		2
189	Independent Component Analysis with Nonlinearity Mitigation for MIMO Full-Duplex Systems. , 2021, , .		2
190	CMOS Programmable Imager Implementing Pre-Processing Operations. Analog Integrated Circuits and Signal Processing, 1999, 19, 279-293.	1.4	1
191	Modified all digital timing tracking loop for wireless applications. , 0, , .		1
192	A novel multipath searcher implementation for WCDMA receivers. , 0, , .		1
193	Implementation of a digital timing recovery circuit for CDMA applications. , 0, , .		1
194	A Real-Time Wireless Channel Emulator for MIMO Systems. , 2006, , .		1
195	On the VLSI Implementation of low complexity K-best MIMO decoders. , 2008, , .		1
196	Architectural and algorithm level fault tolerant techniques for low power high yield multimedia devices. , 2008, , .		1
197	Cross-Layer Approaches to Designing Reliable Systems Using Unreliable Chips. , 2008, , .		1
198	Effect of body biasing on embedded SRAM failure. , 2010, , .		1

#	ARTICLE	IF	CITATIONS
199	Adjustable supply voltages and refresh cycle for process variations, temperature changes, and device degradation adaptation in 1T1C embedded DRAM. , 2011, , .		1
200	Amplify-and-Forward Relay Networks under Received Power Constraint with Imperfect CSI. , 2011, , .		1
201	Linear decentralized estimation of correlated data for wireless sensor networks. , 2011, , .		1
202	Area, reconfiguration delay and reliability trade-offs in designing reliable multi-mode FIR filters. , 2011, , .		1
203	Adjustable supply voltages and refresh cycle for process variations and temperature changing adaptation in DRAM to minimize power consumption. , 2011, , .		1
204	Reliable low power Distributed Arithmetic filters via N-Modular Redundancy. , 2012, , .		1
205	On optimizing the performance of interference-limited cellular systems. , 2014, , .		1
206	On the performance of Massive MIMO cellular systems with power amplifiers. , 2014, , .		1
207	A cortical activity localization approach for decoding finger movements from human electrocorticogram signal. , 2015, , .		1
208	Extracting the Cole-Cole Model Parameters of Tissue-mimicking Materials. , 2018, , .		1
209	Robust Frame Boundary Synchronization for In-Band Full-Duplex OFDM System. , 2018, , .		1
210	Circuit Inspired Modeling Method for Irrigation. , 2018, , .		1
211	Testing Topology Adaptive Irrigation IoT with Circuits. , 2019, , .		1
212	Biometric Identity Based on Intra-Body Communication Channel Characteristics and Machine Learning. Sensors, 2020, 20, 1421.	3.8	1
213	Comment on "FPGA realization of fractional order neuron" [Appl. Math. Model. 81 (2020) 372-385]. Applied Mathematical Modelling, 2021, 92, 951-954.	4.2	1
214	On Coding and Decoding Reconfigurable Radiation Pattern Modulation Symbols. Electronics (Switzerland), 2021, 10, 614.	3.1	1
215	Cognitive Route Selection and Frequency Allocation for CubeSat Swarm. , 2021, , .		1
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