

Anderson Luiz Sartor

List of Publications by Year in descending order

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Version: 2024-02-01

14
papers

79
citations

1937685

4
h-index

1588992

8
g-index

15
all docs

15
docs citations

15
times ranked

47
citing authors

#	ARTICLE	IF	CITATIONS
1	Runtime Task Scheduling Using Imitation Learning for Heterogeneous Many-Core Systems. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2020, 39, 4064-4077.	2.7	20
2	Adaptive ILP control to increase fault tolerance for VLIW processors. , 2016, , .		11
3	Exploiting Idle Hardware to Provide Low Overhead Fault Tolerance for VLIW Processors. ACM Journal on Emerging Technologies in Computing Systems, 2017, 13, 1-21.	2.3	11
4	HiLITE: Hierarchical and Lightweight Mitigation Learning for Power Management of Embedded SoCs. IEEE Computer Architecture Letters, 2020, 19, 63-67.	1.5	9
5	A Novel Phase-Based Low Overhead Fault Tolerance Approach for VLIW Processors. , 2015, , .		7
6	Simbah-FI: Simulation-Based Hybrid Fault Injector. , 2017, , .		5
7	A fast and accurate hybrid fault injection platform for transient and permanent faults. Design Automation for Embedded Systems, 2019, 23, 3-19.	1.0	5
8	Dynamic Trade-off among Fault Tolerance, Energy Consumption, and Performance on a Multiple-Issue VLIW Processor. IEEE Transactions on Multi-Scale Computing Systems, 2018, 4, 327-339.	2.4	4
9	Adaptive and polymorphic VLIW processor to optimize fault tolerance, energy consumption, and performance. , 2018, , .		3
10	Machine Learning-Based Processor Adaptability Targeting Energy, Performance, and Reliability. , 2019, , .		1
11	ISA-DTMR: Selective Protection in Configurable Heterogeneous Multicores. Lecture Notes in Computer Science, 2018, , 231-242.	1.3	1
12	Low-Cost BRAM-Based Function Reuse for Configurable Soft-Core Processors in FPGAs. Lecture Notes in Computer Science, 2018, , 499-510.	1.3	1
13	BRAM-based function reuse for multi-core architectures in FPGAs. Microprocessors and Microsystems, 2018, 63, 237-248.	2.8	0
14	Multi-architecture profiler for Android. International Journal of High Performance Systems Architecture, 2017, 7, 41.	0.3	0