## Girish Kumar

## List of Publications by Year in descending order

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1478280 1474057 14 169 6 9 citations h-index g-index papers 14 14 14 77 citing authors docs citations times ranked all docs

#	Article	IF	CITATIONS
1	Comprehensive Model for High-Speed Current-Mode Signaling in Next Generation MWCNT Bundle Interconnect Using FDTD Technique. IEEE Nanotechnology Magazine, 2016, 15, 590-598.	1.1	37
2	A Novel Unified Model for Copper and MLGNR Interconnects Using Voltage- and Current-Mode Signaling Schemes. IEEE Transactions on Electromagnetic Compatibility, 2017, 59, 217-227.	1.4	37
3	An Efficient Crosstalk Model For Coupled Multiwalled Carbon Nanotube Interconnects. IEEE Transactions on Electromagnetic Compatibility, 2018, 60, 487-496.	1.4	28
4	Carbon Nanotube Interconnects â^' A Promising Solution for VLSI Circuits. IETE Journal of Education Online, 2016, 57, 46-64.	0.7	27
5	Structure Fortification of Mixed CNT Bundle Interconnects for Nano Integrated Circuits Using Constraint-Based Particle Swarm Optimization. IEEE Nanotechnology Magazine, 2021, 20, 194-204.	1.1	17
6	A Unified Delay, Power and Crosstalk Model for Current Mode Signaling Multiwall Carbon Nanotube Interconnects. Circuits, Systems, and Signal Processing, 2018, 37, 1359-1382.	1.2	8
7	Timing and Stability Analysis of Carbon Nanotube Interconnects. , 2015, , .		5
8	A prominent unified crosstalk model for linear and sub-threshold regions in mixed CNT bundle interconnects. Microelectronics Journal, 2021, 118, 105294.	1.1	4
9	Performance Analysis of Multilayer Graphene Nano-Ribbon in Current-Mode Signaling Interconnect System. , 2015, , .		1
10	Stability Analysis of Carbon Nanotube Interconnects. Advances in Intelligent Systems and Computing, 2017, , 11-19.	0.5	1
11	Transient and Crosstalk Analysis of Doped and Dielectric Inserted MLGNR Interconnects., 2018,,.		1
12	Prospective Incorporation of Booster in Carbon Interconnects for High-Speed Integrated Circuits. Lecture Notes in Electrical Engineering, 2021, , 273-288.	0.3	1
13	Variability Analysis of On-chip Graphene Interconnects at Subthreshold Regime. , 2020, , .		1
14	CNTFET Based Low Power Repeaters for On-Chip Interconnect System. , 2021, , .		1