

Vaibhav Vyas

List of Publications by Year in descending order

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7
papers

48
citations

2682572

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2550090

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8
all docs

8
docs citations

8
times ranked

14
citing authors

#	ARTICLE	IF	CITATIONS
1	Karnaugh Map Method for Memristive and Spintronic Asymmetric Basis Logic Functions. IEEE Transactions on Computers, 2021, 70, 128-138.	3.4	5
2	Energy efficiency challenges for all-spin logic. Microelectronics Journal, 2021, 110, 105008.	2.0	1
3	Framework and Smart Contract for Blockchain Enabled Certificate Verification System Using Robotics. Studies in Computational Intelligence, 2021, , 125-138.	0.9	34
4	A comparative analysis of different load balancing algorithms on different parameters in cloud computing. , 2019, , .		5
5	Proactive Fault Tolerance using Heartbeat Strategy for Fault Detection. International Journal of Engineering and Advanced Technology, 2019, 9, 4927-4932.	0.3	0
6	Sequential Circuit Design with Bilayer Avalanche Spin Diode Logic. , 2018, , .		1
7	Integrate aspects with UML: Aspect oriented use case model. , 2016, , .		2