

# Shibnath Pathak

## List of Publications by Year in descending order

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30  
papers

1,204  
citations

516710

16  
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677142

22  
g-index

30  
all docs

30  
docs citations

30  
times ranked

1323  
citing authors

#	ARTICLE	IF	CITATIONS
1	Waveguide Bends for Suppressed Mode Coupling. IEEE Journal of Quantum Electronics, 2020, 56, 1-10.	1.9	2
2	Photonics Integrated Circuits. , 2019, , 219-270.		5
3	Low-Loss Compact Silicon Nitride Arrayed Waveguide Gratings for Photonic Integrated Circuits. IEEE Photonics Journal, 2017, 9, 1-5.	2.0	30
4	Rapidly reconfigurable high-fidelity optical arbitrary waveform generation in heterogeneous photonic integrated circuits. Optics Express, 2017, 25, 8872.	3.4	14
5	Silicon nitride tri-layer vertical Y-junction and 3D couplers with arbitrary splitting ratio for photonic integrated circuits. Optics Express, 2017, 25, 10474.	3.4	25
6	1Å–256 Multi-layer, low-loss, Si <sub>3</sub> N <sub>4</sub> waveguide optical phased arrays with 0.050° Instantaneous-Field-of-View. , 2017, , .		7
7	Tri-layer, Vertical Y-junction, Si <sub>3</sub> N <sub>4</sub> /SiO <sub>2</sub> 3D Photonic Integrated Circuits with Arbitrary Splitting Ratio. , 2016, , .		3
8	Silicon and silicon nitride photonic circuits for spectroscopic sensing on-a-chip [Invited]. Photonics Research, 2015, 3, B47.	7.0	173
9	Experimental Demonstration of Compact 16 channels-50 GHz Si <sub>3</sub> N <sub>4</sub> Arrayed Waveguide Grating. , 2015, , .		2
10	Silicon photonics non-resonant wavelength filters: comparison between AWGs, echelle gratings, and cascaded Mach-Zehnder filters. Proceedings of SPIE, 2015, , .	0.8	6
11	Simultaneous Interrogation of Multiple Fiber Bragg Grating Sensors Using an Arrayed Waveguide Grating Filter Fabricated in SOI Platform. IEEE Photonics Journal, 2015, 7, 1-11.	2.0	31
12	Compact Silicon Nitride Arrayed Waveguide Gratings for Very Near-Infrared Wavelengths. IEEE Photonics Technology Letters, 2015, 27, 137-140.	2.5	74
13	Low-loss compact multilayer silicon nitride platform for 3D photonic integrated circuits. Optics Express, 2015, 23, 21334.	3.4	108
14	Si <sub>3</sub> N <sub>4</sub> Multilayer Platform for Photonic Integrated Circuits. , 2015, , .		3
15	193nm immersion lithography for high-performance silicon photonic circuits. Proceedings of SPIE, 2014, , .	0.8	11
16	Integrated grating coupler/power splitter for on-chip optical power distribution. , 2014, , .		1
17	Silicon-on-insulator shortwave infrared wavelength meter with integrated photodiodes for on-chip laser monitoring. Optics Express, 2014, 22, 27300.	3.4	26
18	Effect of Mask Discretization on Performance of Silicon Arrayed Waveguide Gratings. IEEE Photonics Technology Letters, 2014, 26, 718-721.	2.5	40

#	ARTICLE	IF	CITATIONS
19	A fast 4-channel silicon switch using an AWG with 12 carrier depletion modulators. , 2014, , .		0
20	Improving the design cycle for nanophotonic components. Journal of Computational Science, 2013, 4, 313-324.	2.9	16
21	Germanium-on-Silicon Mid-Infrared Arrayed Waveguide Grating Multiplexers. IEEE Photonics Technology Letters, 2013, 25, 1805-1808.	2.5	127
22	Integrated design for integrated photonics: from the physical to the circuit level and back. Proceedings of SPIE, 2013, , .	0.8	12
23	Silicon-on-insulator spectrometers with integrated GaInAsSb photodiodes for wide-band spectroscopy from 1510 to 2300 nm. Optics Express, 2013, 21, 6101.	3.4	82
24	Demonstration of Silicon-on-insulator mid-infrared spectrometers operating at 38 $\mu$ m. Optics Express, 2013, 21, 11659.	3.4	111
25	Design trade-offs for silicon-on-insulator-based AWGs for (de)multiplexer applications. Optics Letters, 2013, 38, 2961.	3.3	89
26	III-V-on-silicon multi-frequency lasers. Optics Express, 2013, 21, 13675.	3.4	32
27	Optimized Silicon AWG With Flattened Spectral Response Using an MMI Aperture. Journal of Lightwave Technology, 2013, 31, 87-93.	4.6	105
28	Athermal arrayed waveguide gratings in silicon-on-insulator by overlaying a polymer cladding on narrowed arrayed waveguides. Applied Optics, 2012, 51, 1251.	1.8	26
29	Compact SOI-based polarization diversity wavelength de-multiplexer circuit using two symmetric AWGs. Optics Express, 2012, 20, B493.	3.4	36
30	IPKISS: A parametric design and simulation framework for silicon photonics. , 2012, , .		7