

Pi-Feng Chiu

List of Publications by Year in descending order

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10
papers

207
citations

1937685

4
h-index

1872680

6
g-index

10
all docs

10
docs citations

10
times ranked

238
citing authors

#	ARTICLE	IF	CITATIONS
1	A Dual-Core RISC-V Vector Processor With On-Chip Fine-Grain Power Management in 28-nm FD-SOI. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2020, 28, 2721-2725.	3.1	7
2	Cache Resiliency Techniques for a Low-Voltage RISC-V Out-of-Order Processor in 28-nm CMOS. IEEE Solid-State Circuits Letters, 2018, 1, 229-232.	2.0	2
3	An Out-of-Order RISC-V Processor with Resilient Low-Voltage Operation in 28NM CMOS. , 2018, , .		5
4	A RISC-V Processor SoC With Integrated Power Management at Submicrosecond Timescales in 28 nm FD-SOI. IEEE Journal of Solid-State Circuits, 2017, 52, 1863-1875.	5.4	32
5	Reprogrammable Redundancy for SRAM-Based Cache V_{\min} Reduction in a 28-nm RISC-V Processor. IEEE Journal of Solid-State Circuits, 2017, 52, 2589-2600.	5.4	4
6	Reprogrammable redundancy for cache V_{\min} reduction in a 28nm RISC-V processor. , 2016, , .		1
7	A double-tail sense amplifier for low-voltage SRAM in 28nm technology. , 2016, , .		6
8	An Agile Approach to Building RISC-V Microprocessors. IEEE Micro, 2016, 36, 8-20.	1.8	79
9	A RISC-V Vector Processor With Simultaneous-Switching Switched-Capacitor DC-DC Converters in 28 nm FDSOI. IEEE Journal of Solid-State Circuits, 2016, 51, 930-942.	5.4	47
10	A RISC-V vector processor with tightly-integrated switched-capacitor DC-DC converters in 28nm FDSOI. , 2015, , .		24