

Chen Liu

List of Publications by Year in descending order

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Version: 2024-02-01

58
papers

254
citations

1684188

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h-index

1372567

10
g-index

59
all docs

59
docs citations

59
times ranked

168
citing authors

| # | ARTICLE | IF | CITATIONS |
|----|---|-----|-----------|
| 1 | Prefetching in Embedded Mobile Systems Can Be Energy-Efficient. IEEE Computer Architecture Letters, 2011, 10, 8-11. | 1.5 | 25 |
| 2 | Can Data-Only Exploits be Detected at Runtime Using Hardware Events?. , 2016, , . | | 18 |
| 3 | Enabling Deep Learning on IoT Edge: Approaches and Evaluation. , 2018, , . | | 17 |
| 4 | Acceleration of XML Parsing through Prefetching. IEEE Transactions on Computers, 2013, 62, 1616-1628. | 3.4 | 14 |
| 5 | IoT Edge Device Based Key Frame Extraction for Face in Video Recognition. , 2018, , . | | 12 |
| 6 | Energy Efficient Architecture Using Hardware Acceleration for Software Defined Radio Components. Journal of Information Processing Systems, 2012, 8, 133-144. | 0.9 | 12 |
| 7 | Pinned OS/Services: A Case Study of XML Parsing on Intel SCC. Journal of Computer Science and Technology, 2013, 28, 3-13. | 1.5 | 11 |
| 8 | GPU-Accelerated Key Frame Analysis for Face Detection in Video. , 2015, , . | | 9 |
| 9 | Memory-Side Acceleration for XML Parsing. Lecture Notes in Computer Science, 2011, , 277-292. | 1.3 | 8 |
| 10 | Workload Characterization of Cryptography Algorithms for Hardware Acceleration. , 2011, , . | | 7 |
| 11 | Scheduling optimization in multicore multithreaded microprocessors through dynamic modeling. , 2013, , . | | 7 |
| 12 | The Impact of Speculative Execution on SMT Processors. International Journal of Parallel Programming, 2008, 36, 361-385. | 1.5 | 6 |
| 13 | Hardware-assisted security mechanism: The acceleration of cryptographic operations with low hardware cost. , 2010, , . | | 6 |
| 14 | Detecting Data Exploits Using Low-level Hardware Information. , 2018, , . | | 6 |
| 15 | Detecting Non-Control-Flow Hijacking Attacks Using Contextual Execution Information. , 2019, , . | | 6 |
| 16 | On Better Performance from Scheduling Threads According to Resource Demands in MMMP. , 2010, , . | | 5 |
| 17 | Power and energy consumption analysis on intel SCC many-core system. , 2011, , . | | 5 |
| 18 | Power-Efficient Schemes via Workload Characterization on the Intel's Single-Chip Cloud Computer. , 2012, , . | | 5 |

| # | ARTICLE | IF | CITATIONS |
|----|--|-----|-----------|
| 19 | Iris matching algorithm on many-core platforms. , 2015, , . | | 5 |
| 20 | An Auto-tuning Assisted Power-Aware Study of Iris Matching Algorithm on Intel™s SCC. Journal of Signal Processing Systems, 2015, 80, 261-276. | 2.1 | 5 |
| 21 | Adaptive Virtual Machine Management in the Cloud. International Journal of Systems and Service-Oriented Engineering, 2014, 4, 28-43. | 0.6 | 5 |
| 22 | Application-level voltage and frequency tuning of multi-phase program on the SCC. , 2013, , . | | 5 |
| 23 | Auto-tuning multi-programmed workload on the SCC. , 2013, , . | | 4 |
| 24 | The Impact on the Performance of Co-running Virtual Machines in a Virtualized Environment. , 2016, , . | | 4 |
| 25 | The Impact of Resource Sharing Control on the Design of Multicore Processors. Lecture Notes in Computer Science, 2009, , 315-326. | 1.3 | 4 |
| 26 | PCOUNT: A power aware fetch policy in Simultaneous Multithreading processors. , 2011, , . | | 3 |
| 27 | How many cores do we need to run a parallel workload: A test drive of the Intel SCC platform?. Journal of Parallel and Distributed Computing, 2014, 74, 2582-2595. | 4.1 | 3 |
| 28 | Key-Frame Analysis for Face Related Video on GPU-Accelerated Embedded Platform. , 2016, , . | | 3 |
| 29 | Energy-Aware Automatic Tuning on Many-Core Platform via Differential Evolution. , 2016, , . | | 3 |
| 30 | Case Study on a Software Communications Architecture Component for Hardware Acceleration. , 2011, , . | | 2 |
| 31 | Power and energy analysis on intel Single-Chip Cloud Computer system. , 2012, , . | | 2 |
| 32 | A manual approach and analysis of Voltage and Frequency Scaling using SCC. , 2012, , . | | 2 |
| 33 | EHA: The Extremely Heterogeneous Architecture. , 2012, , . | | 2 |
| 34 | A Power-Aware Study of Iris Matching Algorithms on Intel's SCC. , 2013, , . | | 2 |
| 35 | Workload characteristics for iris matching algorithm: A case study. , 2013, , . | | 2 |
| 36 | Multi-core Approach towards Efficient Biometric Cryptosystems. , 2015, , . | | 2 |

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| 37 | A resource utilization based instruction fetch policy for SMT processors. <i>Microprocessors and Microsystems</i> , 2015, 39, 1-10. | 2.8 | 2 |
| 38 | Energy-Aware Automatic Tuning of Many-Core Platform via Gradient Descent. , 2018, , . | | 2 |
| 39 | An Autoencoder Based Approach to Defend Against Adversarial Attacks for Autonomous Vehicles. , 2020, , . | | 2 |
| 40 | Static Partitioning vs Dynamic Sharing of Resources in Simultaneous MultiThreading Microarchitectures. <i>Lecture Notes in Computer Science</i> , 2005, , 81-90. | 1.3 | 2 |
| 41 | Hardware Acceleration for Cryptography Algorithms by Hotspot Detection. <i>Lecture Notes in Computer Science</i> , 2013, , 472-481. | 1.3 | 2 |
| 42 | Resource sharing control in Simultaneous MultiThreading microarchitectures. , 2008, , . | | 1 |
| 43 | Practical models for energy-efficient prefetching in mobile embedded systems. <i>Microprocessors and Microsystems</i> , 2013, 37, 1173-1182. | 2.8 | 1 |
| 44 | Parallel BP Neural Network on Single-chip Cloud Computer. , 2015, , . | | 1 |
| 45 | Implementation and optimization of a biometric cryptosystem using iris recognition. <i>Proceedings of SPIE</i> , 2015, , . | 0.8 | 1 |
| 46 | GreedyTalents: An Energy-Aware Auto-Tuning Method for Many-Core Processor. , 2018, , . | | 1 |
| 47 | 2D Map Estimation via Teacher-Forcing Unsupervised Learning. , 2020, , . | | 1 |
| 48 | The Performance Analysis and Hardware Acceleration of Crypto-computations for Enhanced Security. , 2010, , . | | 0 |
| 49 | Validation of scheduling techniques to reduce peak temperature on an architectural level platform set-up. , 2011, , . | | 0 |
| 50 | INTRODUCING THE EXTREMELY HETEROGENEOUS ARCHITECTURE. <i>Journal of Interconnection Networks</i> , 2012, 13, 1250010. | 1.0 | 0 |
| 51 | OCP: Offload Co-Processor for energy efficiency in embedded mobile systems. , 2013, , . | | 0 |
| 52 | Many-core computing for space-based stereoscopic imaging. , 2013, , . | | 0 |
| 53 | Enhancement for Potential Target in Cryptography Algorithms by Applying Processor-in-Memory Architecture. , 2013, , . | | 0 |
| 54 | How can Garbage Collection be energy efficient by dynamic offloading?. , 2015, , . | | 0 |

| # | ARTICLE | IF | CITATIONS |
|----|--|-----|-----------|
| 55 | Parallel Gaussian Elimination on Single-chip Cloud Computer. , 2015, , . | | 0 |
| 56 | Feature Creation Towards the Detection of Non-control-Flow Hijacking Attacks. Lecture Notes in Computer Science, 2021, , 153-164. | 1.3 | 0 |
| 57 | Simultaneous MultiThreading Microarchitecture. , 2010, , 552-582. | | 0 |
| 58 | Using Hardware Acceleration to Improve the Security of Wi-Fi Client Devices. Lecture Notes in Electrical Engineering, 2014, , 557-562. | 0.4 | 0 |