

Emmanuel M Drakakis

List of Publications by Year in descending order

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Version: 2024-02-01

13
papers

110
citations

1307594

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1372567

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all docs

13
docs citations

13
times ranked

141
citing authors

#	ARTICLE	IF	CITATIONS
1	On memristor ideality and reciprocity. <i>Microelectronics Journal</i> , 2014, 45, 1363-1371.	2.0	19
2	A High-Performance Application Specific Integrated Circuit for Electrical and Neurochemical Traumatic Brain Injury Monitoring. <i>ChemPhysChem</i> , 2018, 19, 1215-1225.	2.1	11
3	A high-performance 8 μ nV/ μ sHz 8-channel wearable and wireless system for real-time monitoring of bioelectrical signals. <i>Journal of NeuroEngineering and Rehabilitation</i> , 2019, 16, 156.	4.6	11
4	Systematic Computation of Nonlinear Cellular and Molecular Dynamics with Low-Power CytoMimetic Circuits: A Simulation Study. <i>PLoS ONE</i> , 2013, 8, e53591.	2.5	9
5	Organic Electrochemical Transistor Common-Source Amplifier for Electrophysiological Measurements. <i>Advanced Functional Materials</i> , 2021, 31, 2103385.	14.9	9
6	A 19nW analogue CMOS log-domain 6th-order Bessel filter without E-minus cells. <i>Microelectronics Journal</i> , 2009, 40, 1170-1174.	2.0	8
7	A CMOS image sensor with light-controlled oscillating pixels for an investigative optobionic retinal prosthesis system. <i>Microelectronics Journal</i> , 2009, 40, 1202-1211.	2.0	8
8	An implantable mixed-signal CMOS die for battery-powered in vivo blowfly neural recordings. <i>Microelectronics Journal</i> , 2018, 74, 34-42.	2.0	8
9	CMOS weak-inversion log-domain glycolytic oscillator: a cytomimetic circuit example. <i>International Journal of Circuit Theory and Applications</i> , 2014, 42, 173-194.	2.0	7
10	Current-input current-output analog half center oscillator and central pattern generator circuits with memristors. <i>International Journal of Circuit Theory and Applications</i> , 2018, 46, 1294-1310.	2.0	6
11	Traumatic brain injury neuroelectrochemical monitoring: behind-the-ear micro-instrument and cloud application. <i>Journal of NeuroEngineering and Rehabilitation</i> , 2020, 17, 114.	4.6	6
12	Preliminary results from an analog implementation of first-order TDCNN dynamics. <i>International Journal of Circuit Theory and Applications</i> , 2011, 39, 665-678.	2.0	5
13	Analytical study, performance optimisation and design rules for customary static and dynamic subthreshold MOS translinear topologies. <i>Microelectronics Journal</i> , 2016, 53, 177-193.	2.0	3