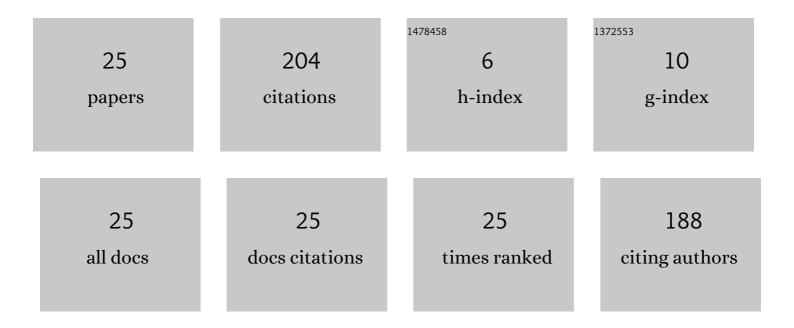
Hiwa Mahmoudi

List of Publications by Year in descending order

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1 Inglection logic gates using gpin transfer torque operated magnetic tunnel junctions for intrinsic 1.4 64 2 Reliability Analysis and Comparison of Implication and Reprogrammable Logic Cates in Magnetic Tunnel 2.1 74 3 MRAM-based logic array for large-scale non-volatile logic-in-memory applications., 2013, 12 4 Rigorous simulation study of a novel non-volatile logic circuits., 2013, 10 6 Design and applications of magnetic tunnel junction based logic circuits., 2013, 2.6 10 6 Novel bias-field-free spin transfer oscillator. Journal of Applied Physics, 2014, 115, 17C901. 2.5 10 7 Modeling and Analysis of BER Performance in a SPAD-Based Integrated Fiber Optical Receiver. IEEE 3.0 9 8 Statistical Study of Intrinsic Parasitics In an SPAD-Based Integrated Fiber Optical Receiver. IEEE 3.0 9 9 Spite-Rhoton Avalanche Dodes. IEEE Sensors Journal, 2021, 21, 7572-7580. 4.7 9 10 Novel MII-based shift register for non-volatile logic applications, 2013, 5 5 11 MT-based miplication logic gates and circuit architecture for large-scale spintronic stateful logic 5.4 5 12 Reliability-Based Optimization of Spin-Transfer Torque Magnetic Tunnel Junc	#	Article	IF	CITATIONS
2 junction Logic Circuits. IEEE Transactions on Magnetics, 2013, 49, 5620-5628. 2.1 24 3 MRAM-based logic array for large-scale non-volatile logic-in-memory applications., 2013, 12 4 Rigorous simulation study of a novel non-volatile logic-in-memory applications., 2013, 10 5 Design and applications of magnetic tunnel junction based logic circuits., 2013, 10 6 Novel blas-field free spin transfer oscillator. Journal of Applied Physics, 2014, 115, 17C901. 2.5 10 7 Photoiners Journal, 2018, 10, 1-11. 2.0 10 8 Statistical Study of Intrinsic Parasities in an SPAD-Based Integrated Fiber Optical Receiver. IEEE 3.0 9 9 Optical and Electrical Characterization and Modeling of Photon Detection Probability in CMOS 4.7 9 10 Novel MIJ-based shift register for non-volatile logic applications., 2013, 6 6 11 MTJ based Implication logic gates and circuit architecture for large-scale spintronic stateful logic 5 5 12 Reliability-Based Optimization of Spin-Transfer Torque Magnetic Tunnel Junction Implication Logic 6.3 5 13 Photon detection probability enhancement using an anti-reflection coating in CMOS-based SPADs. 1.8 5	1	Implication logic gates using spin-transfer-torque-operated magnetic tunnel junctions for intrinsic logic-in-memory. Solid-State Electronics, 2013, 84, 191-197.	1.4	65
4 Rigorous simulation study of a novel non-volatile magnetic flip-flop., 2013,, 10 6 Design and applications of magnetic tunnel junction based logic circuits., 2013, 10 6 Novel bias-field-free spin transfer oscillator. Journal of Applied Physics, 2014, 115, 17C901. 2.5 10 7 Modeling and Analysis of BER Performance in a SPAD-Based Integrated Fiber Optical Receiver. IEEE 2.0 10 8 Statistical Study of Intrinsic Parasities in an SPAD-Based Integrated Fiber Optical Receiver. IEEE 3.0 9 9 Statistical Study of Intrinsic Parasities in an SPAD-Based Integrated Fiber Optical Receiver. IEEE 3.0 9 10 Novel MIJ-based Integrated Chiper Optical Receiver. IEEE 3.0 9 11 Novel MIJ-based shift register for non-volatile logic applications., 2013, 6 12 Reliability-Based Optimization of Spin-Transfer Torque Magnetic Tunnel Junction Implication Logic systems. 2021, 21, 554, 89-95. 1.8 5 13 Photon detection probability enhancement using an anti-reflection coating in CMOS-based SPADs. 1.8 5 14 Noise and Breakdown Characterization of SPAD Detectors with Time-Gated Photon-Counting 3.8 4 15 Performance analysis and comparison of two 1T/IMT/Hosed logic gates., 2013,	2	Reliability Analysis and Comparison of Implication and Reprogrammable Logic Gates in Magnetic Tunnel Junction Logic Circuits. IEEE Transactions on Magnetics, 2013, 49, 5620-5628.	2.1	24
3 Design and applications of magnetic tunnel junction based logic circuits., 2013,, 10 6 Novel bias-field-free spin transfer oscillator. Journal of Applied Physics, 2014, 115, 17C901. 2.5 10 7 Modeling and Analysis of BER Performance in a SPAD-Based Integrated Fiber Optical Receiver. IEEE 2.0 10 8 Statistical Study of Intrinsic Parasities in an SPAD-Based Integrated Fiber Optical Receiver. IEEE 3.0 9 9 Statistical Study of Intrinsic Parasities in an SPAD-Based Integrated Fiber Optical Receiver. IEEE 3.0 9 10 Novel MIJ-based Integrated Fiber Optical Receiver. IEEE 3.0 9 10 Novel MIJ-based shift register for non-volatile logic applications., 2013, 6 11 MTI-based implication logic gates and circuit architecture for large-scale spintronic stateful logic systems., 2012, 5 12 Reliability-Based Optimization of Spin-Transfer Torque Magnetic Tunnel Junction Implication Logic Gates. Advanced Materials Research, 0, 854, 89-95. 1.8 6 13 Photon detection probability enhancement using an anti-reflection coating in CMOS-based SPADs. 1.8 6 14 Noise and Breakdown Characterization of SPAD Detectors with Time-Gated Photon-Counting Operation. Sensors, 2021, 21, 5287. 3 3 15	3	MRAM-based logic array for large-scale non-volatile logic-in-memory applications. , 2013, , .		12
6 Novel bias-field-free spin transfer oscillator. Journal of Applied Physics, 2014, 115, 17C901. 2.5 10 7 Modeling and Analysis of BER Performance in a SPAD-Based Integrated Fiber Optical Receiver. IEEE 2.0 10 8 Statistical Study of Intrinsic Parasitics in an SPAD-Based Integrated Fiber Optical Receiver. IEEE 3.0 9 9 Optical and Electrical Characterization and Modeling of Photon Detection Probability in CMOS 4.7 9 10 Novel MTJ-based shift register for non-volatile logic applications., 2013, 6 11 MTJ-based implication logic gates and circuit architecture for large-scale spintronic stateful logic actes. Advanced Materials Research, 0, 854, 89-95. 0.3 5 12 Reliability-Based Optimization of Spin-Transfer Torque Magnetic Tunnel Junction Implication Logic Cates. Advanced Materials Research, 0, 854, 89-95. 1.8 5 13 Photon detection probability enhancement using an anti-reflection coating in CMOS-based SPADs. Applied Optics, 2021, 21, 5287. 3.8 4 14 Noise and Breakdown Characterization of SPAD Detectors with Time-Gated Photon-Counting Operation. Sensors, 2021, 21, 5287. 3 15 Performance analysis and comparison of two 1T/IMTJ-based logic gates., 2013, 3 3	4	Rigorous simulation study of a novel non-volatile magnetic flip-flop. , 2013, , .		10
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Photonics journal, 2018, 10, 1-11. 2.0 10 8 Transactions on Electron Devices, 2019, 66, 497-504. 3.0 9 9 Optical and Electrical Characterization and Modeling of Photon Detection Probability in CMOS Single-Photon Avalanche Diodes. IEEE Sensors Journal, 2021, 21, 7572-7580. 4.7 9 10 Novel MTJ-based shift register for non-volatile logic applications., 2013, , . 6 11 MTJ-based implication logic gates and circuit architecture for large-scale spintronic stateful logic systems., 2012, , . 5 12 Reliability-Based Optimization of Spin-Transfer Torque Magnetic Tunnel Junction Implication Logic Cates. Advanced Materials Research, 0, 854, 89-95. 0.3 5 13 Photon detection probability enhancement using an anti-reflection coating in CMOS-based SPADs. 1.8 5 14 Noise and Breakdown Characterization of SPAD Detectors with Time-Gated Photon-Counting Operation. Sensors, 2021, 21, 5287. 3 3	6	Novel bias-field-free spin transfer oscillator. Journal of Applied Physics, 2014, 115, 17C901.	2.5	10
3Transactions on Electron Devices, 2019, 66, 497-504.3.099Optical and Electrical Characterization and Modeling of Photon Detection Probability in CMOS Single-Photon Avalanche Diodes. IEEE Sensors Journal, 2021, 21, 7572-7580.4.7910Novel MTJ-based shift register for non-volatile logic applications. , 2013, , .611MTJ-based implication logic gates and circuit architecture for large-scale spintronic stateful logic systems. , 2012, , .512Reliability-Based Optimization of Spin-Transfer Torque Magnetic Tunnel Junction Implication Logic Cates. Advanced Materials Research, 0, 854, 89-95.0.3513Photon detection probability enhancement using an anti-reflection coating in CMOS-based SPADs. 	7	Modeling and Analysis of BER Performance in a SPAD-Based Integrated Fiber Optical Receiver. IEEE Photonics Journal, 2018, 10, 1-11.	2.0	10
9 Single-Photon Avalanche Diodes. IEEE Sensors Journal, 2021, 21, 7572-7580. 4.7 9 10 Novel MTJ-based shift register for non-volatile logic applications. , 2013, , . 6 11 MTJ-based implication logic gates and circuit architecture for large-scale spintronic stateful logic systems. , 2012, 5 12 Reliability-Based Optimization of Spin-Transfer Torque Magnetic Tunnel Junction Implication Logic Cates. Advanced Materials Research, 0, 854, 89-95. 0.3 5 13 Photon detection probability enhancement using an anti-reflection coating in CMOS-based SPADs. 1.8 5 14 Noise and Breakdown Characterization of SPAD Detectors with Time-Gated Photon-Counting Operation. Sensors, 2021, 21, 5287. 3 4 15 Performance analysis and comparison of two 1T/1MTJ-based logic gates. , 2013, , . 3 3	8		3.0	9
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12 Gates. Advanced Materials Research, 0, 854, 89-95. 0.3 5 13 Photon detection probability enhancement using an anti-reflection coating in CMOS-based SPADs. 1.8 5 14 Noise and Breakdown Characterization of SPAD Detectors with Time-Gated Photon-Counting 3.8 4 15 Performance analysis and comparison of two 1T/1MTJ-based logic gates. , 2013, , . 3	11	MTJ-based implication logic gates and circuit architecture for large-scale spintronic stateful logic systems. , 2012, , .		5
13 Applied Optics, 2021, 60, 7815. 1.8 5 14 Noise and Breakdown Characterization of SPAD Detectors with Time-Gated Photon-Counting Operation. Sensors, 2021, 21, 5287. 3.8 4 15 Performance analysis and comparison of two 1T/1MTJ-based logic gates. , 2013, , . 3	12	Reliability-Based Optimization of Spin-Transfer Torque Magnetic Tunnel Junction Implication Logic Gates. Advanced Materials Research, 0, 854, 89-95.	0.3	5
14 Operation. Sensors, 2021, 21, 5287. 3.8 4 15 Performance analysis and comparison of two 1T/1MTJ-based logic gates. , 2013, , . 3	13	Photon detection probability enhancement using an anti-reflection coating in CMOS-based SPADs. Applied Optics, 2021, 60, 7815.	1.8	5
	14		3.8	4
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	16	High performance MRAM-based stateful logic. , 2014, , .		3
17 On Optimal Latin Hypercube Design for Yield Analysis of Analog Circuits. , 2015, , . 3	17	On Optimal Latin Hypercube Design for Yield Analysis of Analog Circuits. , 2015, , .		3

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20	Influence of device geometry on the non-volatile magnetic flip flop characteristics. , 2014, , .		2
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