Hiwa Mahmoudi

List of Publications by Year
in descending order

25 papers	204 citations	1478458 6 h-index	1372553 10 g-index
25 all docs	25 docs citations	25 times ranked	188 citing authors

#	Article	IF	Citations
1	Implication logic gates using spin-transfer-torque-operated magnetic tunnel junctions for intrinsic logic-in-memory. Solid-State Electronics, 2013, 84, 191-197.	1.4	65
2	Reliability Analysis and Comparison of Implication and Reprogrammable Logic Gates in Magnetic Tunnel Junction Logic Circuits. IEEE Transactions on Magnetics, 2013, 49, 5620-5628.	2.1	24
3	MRAM-based logic array for large-scale non-volatile logic-in-memory applications. , 2013, , .		12
4	Rigorous simulation study of a novel non-volatile magnetic flip-flop. , 2013, , .		10
5	Design and applications of magnetic tunnel junction based logic circuits. , 2013, , .		10
6	Novel bias-field-free spin transfer oscillator. Journal of Applied Physics, 2014, 115, 17C901.	2.5	10
7	Modeling and Analysis of BER Performance in a SPAD-Based Integrated Fiber Optical Receiver. IEEE Photonics Journal, 2018, 10, 1-11.	2.0	10
8	Statistical Study of Intrinsic Parasitics in an SPAD-Based Integrated Fiber Optical Receiver. IEEE Transactions on Electron Devices, 2019, 66, 497-504.	3.0	9
9	Optical and Electrical Characterization and Modeling of Photon Detection Probability in CMOS Single-Photon Avalanche Diodes. IEEE Sensors Journal, 2021, 21, 7572-7580.	4.7	9
10	Novel MTJ-based shift register for non-volatile logic applications. , 2013, , .		6
11	MTJ-based implication logic gates and circuit architecture for large-scale spintronic stateful logic systems. , 2012, , .		5
12	Reliability-Based Optimization of Spin-Transfer Torque Magnetic Tunnel Junction Implication Logic Gates. Advanced Materials Research, 0, 854, 89-95.	0.3	5
13	Photon detection probability enhancement using an anti-reflection coating in CMOS-based SPADs. Applied Optics, 2021, 60, 7815.	1.8	5
14	Noise and Breakdown Characterization of SPAD Detectors with Time-Gated Photon-Counting Operation. Sensors, 2021, 21, 5287.	3.8	4
15	Performance analysis and comparison of two 1T/1MTJ-based logic gates. , 2013, , .		3
16	High performance MRAM-based stateful logic. , 2014, , .		3
17	On Optimal Latin Hypercube Design for Yield Analysis of Analog Circuits. , 2015, , .		3
18	A new sampling technique for Monte Carlo-based statistical circuit analysis. , 2017, , .		3

#	Article	IF	Citations
19	Experimental and simulation study of fill-factor enhancement using a virtual guard ring in n+/p-well CMOS single-photon avalanche diodes. Optical Engineering, 2021, 60, .	1.0	3
20	Influence of device geometry on the non-volatile magnetic flip flop characteristics. , 2014, , .		2
21	Influence of magnetization variations in the free layer on a non-volatile magnetic flip flop. , 2014, , .		2
22	Bit Error Performance of APD and SPAD Receivers in Optical Wireless Communication. Electronics (Switzerland), 2021, 10, 2731.	3.1	1
23	Modeling of spin-based silicon technology. , 2014, , .		0
24	Compact modeling of memristive IMP gates for reliable stateful logic design. , 2014, , .		0
25	Multiple purpose spin transfer torque operated devices. Facta Universitatis - Series Electronics and Energetics, 2013, 26, 227-238.	0.9	0