

# Sudhakar Pamarti

## List of Publications by Year in Descending Order

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

76  
papers

1,022  
citations

17  
h-index

29  
g-index

98  
ext. papers

1,272  
ext. citations

4.2  
avg, IF

4.51  
L-index

#	Paper	IF	Citations
76	Periodically Time-Varying Noise Cancellation for Filtering-by-Aliasing Receiver Front Ends. <i>IEEE Journal of Solid-State Circuits</i> , <b>2021</b> , 56, 928-939	5.5	5
75	A Dual-Channel High-Linearity Filtering-by-Aliasing Receiver Front-End Supporting Carrier Aggregation. <i>IEEE Journal of Solid-State Circuits</i> , <b>2021</b> , 1-1	5.5	1
74	ACOUSTIC: Accelerating Convolutional Neural Networks through Or-Unipolar Skipped Stochastic Computing <b>2020</b> ,		3
73	Introduction to the Special Issue on the 2020 IEEE International Solid-State Circuits Conference (ISSCC). <i>IEEE Journal of Solid-State Circuits</i> , <b>2020</b> , 55, 3127-3130	5.5	
72	6.4 A 0.5-to-2.5GHz Multi-Output Fractional Frequency Synthesizer with 90fs Jitter and -106dBc Spurious Tones Based on Digital Spur Cancellation <b>2019</b> ,		3
71	18.4 A 0.55nW/0.5V 32kHz Crystal Oscillator Based on a DC-Only Sustaining Amplifier for IoT <b>2019</b> ,		5
70	A Sub-nW 32-kHz Crystal Oscillator Architecture Based on a DC-Only Sustaining Amplifier. <i>IEEE Journal of Solid-State Circuits</i> , <b>2019</b> , 54, 3247-3256	5.5	4
69	An LPTV Noise Cancellation Technique for a 0.9-V Filtering-by-Aliasing Receiver Front-End with >67-dB Stopband Rejection <b>2019</b> ,		2
68	Design and Analysis of a Programmable Receiver Front End Based on Baseband Analog-FIR Filtering Using an LPTV Resistor. <i>IEEE Journal of Solid-State Circuits</i> , <b>2018</b> , 53, 1592-1606	5.5	11
67	A Broadband Class-AB Power Amplifier With Instantaneous Supply-Switching Efficiency Enhancement for Cable TV Application. <i>IEEE Journal of Solid-State Circuits</i> , <b>2018</b> , 53, 762-771	5.5	0
66	A Quick Startup Technique for High- $Q$ Oscillators Using Precisely Timed Energy Injection. <i>IEEE Journal of Solid-State Circuits</i> , <b>2018</b> , 53, 692-702	5.5	18
65	Theoretical Analysis of Circuit Non-Idealities in a Passive Spectrum Scanner Based on Periodically Time-Varying Circuit Components. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2018</b> , 65, 2403-2410	3.9	
64	Impedance Matching and Reradiation in LPTV Receiver Front-Ends: An Analysis Using Conversion Matrices. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2018</b> , 65, 2842-2855	3.9	6
63	Design and Analysis of a Programmable Receiver Front End With Time-Interleaved Baseband Analog-FIR Filtering. <i>IEEE Journal of Solid-State Circuits</i> , <b>2018</b> , 53, 3197-3207	5.5	8
62	Errata for Design and Analysis of a Programmable Receiver Front End Based on Baseband Analog-FIR Filtering Using an LPTV Resistor [Jun 18 1592-1606]. <i>IEEE Journal of Solid-State Circuits</i> , <b>2018</b> , 53, 2733-2733	5.5	
61	A 36-V 49% Efficient Hybrid Charge Pump in Nanometer-Scale Bulk CMOS Technology. <i>IEEE Journal of Solid-State Circuits</i> , <b>2017</b> , 52, 781-798	5.5	17
60	24.6 A time-interleaved filtering-by-aliasing receiver front-end with >70dB suppression at <b>2017</b> ,		3

59	Design and Analysis of a 1.8-GHz Open-Loop Modulator for Phase Modulation and Frequency Synthesis Using TDC-Based Calibration. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2017</b> , 65, 3975-3988	4.1	3
58	Design and Analysis of an 8 mW, 1 GHz Span, Passive Spectrum Scanner With >+31 dBm Out-of-Band IIP3 Using Periodically Time-Varying Circuit Components. <i>IEEE Journal of Solid-State Circuits</i> , <b>2017</b> , 52, 2009-2025	5.5	9
57	A precisely-timed energy injection technique achieving 58/10/28 start-up in 1.84/10/50MHz crystal oscillators <b>2017</b> ,		2
56	High-Efficiency Millimeter-Wave Energy-Harvesting Systems With Milliwatt-Level Output Power. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , <b>2017</b> , 64, 605-609	3.5	17
55	. <i>IEEE Journal of Solid-State Circuits</i> , <b>2017</b> , 52, 185-197	5.5	23
54	<b>2017</b> ,		13
53	Training of digital predistortion based on signal-to-distortion-ratio measurements <b>2017</b> ,		1
52	Frequency-Domain Analysis of $\pi$ -Path Filters Using Conversion Matrices. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , <b>2016</b> , 63, 74-78	3.5	25
51	Wave digital filter based analog circuit emulation on FPGA <b>2016</b> ,		3
50	A Compact 60-GHz Wireless Power Transfer System. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2016</b> , 64, 2664-2677	4.1	24
49	<b>2016</b> ,		1
48	11.1 Dual-MEMS-resonator temperature-to-digital converter with 40 K resolution and FOM of 0.12pJK <sup>2</sup> <b>2016</b> ,		4
47	Frequency-domain analysis of a mixer-first receiver using conversion matrices <b>2015</b> ,		7
46	A sharp programmable passive filter based on filtering by Aliasing <b>2015</b> ,		6
45	. <i>IEEE Journal of Solid-State Circuits</i> , <b>2015</b> , 50, 291-302	5.5	61
44	A 1.85GHz CMOS power amplifier with zero-voltage-switching contour-based outphasing control to improve back-off efficiency <b>2015</b> ,		2
43	Linearization Through Dithering: A 50 MHz Bandwidth, 10-b ENOB, 8.2 mW VCO-Based ADC. <i>IEEE Journal of Solid-State Circuits</i> , <b>2015</b> , 50, 2012-2024	5.5	27
42	<b>2014</b> ,		16

41	12.9 A 1.550.85mm2 3ppm 1.0A 32.768kHz MEMS-based oscillator <b>2014</b> ,		2
40	A 2.4 GHz 0.1-Fref-Bandwidth All-Digital Phase-Locked Loop With Delay-Cell-Less TDC. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2013</b> , 60, 3145-3151	3.9	10
39	<b>2013</b> ,		7
38	A fully integrated 22.6dBm mm-Wave PA in 40nm CMOS <b>2013</b> ,		8
37	A 50MHz bandwidth, 10-b ENOB, 8.2mW VCO-based ADC enabled by filtered-dithering based linearization <b>2013</b> ,		6
36	. <i>IEEE Journal of Solid-State Circuits</i> , <b>2013</b> , 48, 276-291	5.5	56
35	A compact millimeter-wave energy transmission system for wireless applications <b>2013</b> ,		11
34	Filtering of subtractive discrete dither in quantizers: Some new results <b>2013</b> ,		2
33	Filtering by Aliasing. <i>IEEE Transactions on Signal Processing</i> , <b>2013</b> , 61, 2319-2327	4.8	15
32	A Zero-Voltage-Switching Contour-Based Outphasing Power Amplifier. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2012</b> , 60, 1896-1906	4.1	11
31	Worst-Case Estimation for Data-Dependent Timing Jitter and Amplitude Noise in High-Speed Differential Link. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , <b>2012</b> , 20, 89-97	2.6	1
30	A temperature-to-digital converter for a MEMS-based programmable oscillator with better than $\pm 0.5$ ppm frequency stability <b>2012</b> ,		6
29	Clock/Frequency Generation Circuits and Systems. <i>Journal of Electrical and Computer Engineering</i> , <b>2012</b> , 2012, 1-2	1.9	
28	A programmable baseband anti-alias filter for a passive-mixer-based, SAW-less, multi-band, multi-mode WEDGE transmitter <b>2011</b> ,		1
27	A 2.4 GHz Wideband Open-Loop GFSK Transmitter With Phase Quantization Noise Cancellation. <i>IEEE Journal of Solid-State Circuits</i> , <b>2011</b> , 46, 615-626	5.5	16
26	A novel quantization noise-cancellation scheme in wideband D/A converters <b>2011</b> ,		1
25	. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , <b>2011</b> , 58, 585-589	3.5	5
24	A Zero-Voltage-Switching Contour-Based Power Amplifier With Minimal Efficiency Degradation Under Back-Off. <i>IEEE Transactions on Microwave Theory and Techniques</i> , <b>2011</b> , 59, 1589-1598	4.1	17

23	A novel reconfigurable alias interference cancellation technique for A-to-D conversion <b>2011</b> ,		1
22	Open-Loop Wide-Bandwidth Phase Modulation Techniques. <i>Journal of Electrical and Computer Engineering</i> , <b>2011</b> , 2011, 1-12	1.9	1
21	A power amplifier with minimal efficiency degradation under back-off <b>2010</b> ,		7
20	A 2-MHz bandwidth fractional-N synthesizer based on a fractional frequency divider with digital spur suppression <b>2010</b> ,		2
19	A Digital Envelope Combiner for Switching Power Amplifier Linearization. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , <b>2010</b> , 57, 270-274	3.5	6
18	Mismatch Shaping Techniques to Linearize Charge Pump Errors in Fractional-N PLLs. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2010</b> , 57, 1221-1230	3.9	13
17	A 3.8 Gb/s Four-Wire High Speed I/O Link Based on CDMA-Like Crosstalk Cancellation. <i>IEEE Journal of Solid-State Circuits</i> , <b>2010</b> , 45, 1522-1532	5.5	3
16	A Low Area, Switched-Resistor Based Fractional-N Synthesizer Applied to a MEMS-Based Programmable Oscillator. <i>IEEE Journal of Solid-State Circuits</i> , <b>2010</b> , 45, 2566-2581	5.5	13
15	A low-area switched-resistor loop-filter technique for fractional-N synthesizers applied to a MEMS-based programmable oscillator <b>2010</b> ,		9
14	Fractional-N Phase-Locked-Loop-Based Frequency Synthesis: A Tutorial. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , <b>2009</b> , 56, 881-885	3.5	38
13	Worst case timing jitter and amplitude noise in differential signaling <b>2009</b> ,		1
12	Digital techniques for integrated frequency synthesizers: A tutorial <b>2009</b> , 47, 126-133		5
11	A Theoretical Study of the Quantization Noise in Split Delta-Sigma ADCs. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2008</b> , 55, 1267-1278	3.9	4
10	A Spur Elimination Technique for Phase Interpolation-Based Fractional-N PLLs. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , <b>2008</b> , 55, 1639-1647	3.9	19
9	The Effect of Noise Cross-Coupling on Time-Interleaved Delta-Sigma ADCs. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , <b>2008</b> , 55, 532-536	3.5	4
8	Towards neuromote: a single-chip, 100-channel, neural-signal acquisition, processing, and telemetry device. <i>Annual International Conference of the IEEE Engineering in Medicine and Biology Society</i> , <b>2007</b> , 2007, 437-40		8
7	LSB Dithering in MASH Delta-Sigma D/A Converters. <i>IEEE Transactions on Circuits and Systems Part 1: Regular Papers</i> , <b>2007</b> , 54, 779-790		65
6	Statistics of the Quantization Noise in 1-Bit Dithered Single-Quantizer Digital Delta-Sigma Modulators. <i>IEEE Transactions on Circuits and Systems Part 1: Regular Papers</i> , <b>2007</b> , 54, 492-503		63

5	Power-efficient pulse width modulation DC/DC converters with zero voltage switching control <b>2006,</b>		2
4	Replica compensated linear regulators for supply-regulated phase-locked loops. <i>IEEE Journal of Solid-State Circuits</i> , <b>2006</b> , 41, 413-424	5.5	84
3	Addition to <a href="#">A Wideband 2.4-GHz Delta-Sigma Fractional-N PLL With 1-Mb/s In-Loop Modulation</a> <i>IEEE Journal of Solid-State Circuits</i> , <b>2005</b> , 40, 559-559	5.5	1
2	A wideband 2.4-GHz delta-sigma fractional-N PLL with 1-Mb/s in-loop modulation. <i>IEEE Journal of Solid-State Circuits</i> , <b>2004</b> , 39, 49-62	5.5	148
1	. <i>IEEE Transactions on Circuits and Systems Part 2: Express Briefs</i> , <b>2003</b> , 50, 829-838		17