

Sudhakar Pamarti

List of Publications by Citations

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76
papers

1,022
citations

17
h-index

29
g-index

98
ext. papers

1,272
ext. citations

4.2
avg, IF

4.51
L-index

#	Paper	IF	Citations
76	A wideband 2.4-GHz delta-sigma fractional-NPLL with 1-Mb/s in-loop modulation. <i>IEEE Journal of Solid-State Circuits</i> , 2004 , 39, 49-62	5.5	148
75	Replica compensated linear regulators for supply-regulated phase-locked loops. <i>IEEE Journal of Solid-State Circuits</i> , 2006 , 41, 413-424	5.5	84
74	LSB Dithering in MASH DeltaSigma D/A Converters. <i>IEEE Transactions on Circuits and Systems Part 1: Regular Papers</i> , 2007 , 54, 779-790		65
73	Statistics of the Quantization Noise in 1-Bit Dithered Single-Quantizer Digital DeltaSigma Modulators. <i>IEEE Transactions on Circuits and Systems Part 1: Regular Papers</i> , 2007 , 54, 492-503		63
72	. <i>IEEE Journal of Solid-State Circuits</i> , 2015 , 50, 291-302	5.5	61
71	. <i>IEEE Journal of Solid-State Circuits</i> , 2013 , 48, 276-291	5.5	56
70	Fractional- N Phase-Locked-Loop-Based Frequency Synthesis: A Tutorial. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2009 , 56, 881-885	3.5	38
69	Linearization Through Dithering: A 50 MHz Bandwidth, 10-b ENOB, 8.2 mW VCO-Based ADC. <i>IEEE Journal of Solid-State Circuits</i> , 2015 , 50, 2012-2024	5.5	27
68	Frequency-Domain Analysis of N -Path Filters Using Conversion Matrices. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2016 , 63, 74-78	3.5	25
67	A Compact 60-GHz Wireless Power Transfer System. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2016 , 64, 2664-2677	4.1	24
66	. <i>IEEE Journal of Solid-State Circuits</i> , 2017 , 52, 185-197	5.5	23
65	A Spur Elimination Technique for Phase Interpolation-Based Fractional- N PLLs. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2008 , 55, 1639-1647	3.9	19
64	A Quick Startup Technique for High- Q Oscillators Using Precisely Timed Energy Injection. <i>IEEE Journal of Solid-State Circuits</i> , 2018 , 53, 692-702	5.5	18
63	A 36-V 49% Efficient Hybrid Charge Pump in Nanometer-Scale Bulk CMOS Technology. <i>IEEE Journal of Solid-State Circuits</i> , 2017 , 52, 781-798	5.5	17
62	High-Efficiency Millimeter-Wave Energy-Harvesting Systems With Milliwatt-Level Output Power. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2017 , 64, 605-609	3.5	17
61	A Zero-Voltage-Switching Contour-Based Power Amplifier With Minimal Efficiency Degradation Under Back-Off. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2011 , 59, 1589-1598	4.1	17
60	. <i>IEEE Transactions on Circuits and Systems Part 2: Express Briefs</i> , 2003 , 50, 829-838		17

59	2014,			16
58	A 2.4 GHz Wideband Open-Loop GFSK Transmitter With Phase Quantization Noise Cancellation. <i>IEEE Journal of Solid-State Circuits</i> , 2011 , 46, 615-626	5.5		16
57	Filtering by Aliasing. <i>IEEE Transactions on Signal Processing</i> , 2013 , 61, 2319-2327	4.8		15
56	2017,			13
55	Mismatch Shaping Techniques to Linearize Charge Pump Errors in Fractional-N PLLs. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2010 , 57, 1221-1230	3.9		13
54	A Low Area, Switched-Resistor Based Fractional-N Synthesizer Applied to a MEMS-Based Programmable Oscillator. <i>IEEE Journal of Solid-State Circuits</i> , 2010 , 45, 2566-2581	5.5		13
53	Design and Analysis of a Programmable Receiver Front End Based on Baseband Analog-FIR Filtering Using an LPTV Resistor. <i>IEEE Journal of Solid-State Circuits</i> , 2018 , 53, 1592-1606	5.5		11
52	A Zero-Voltage-Switching Contour-Based Outphasing Power Amplifier. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2012 , 60, 1896-1906	4.1		11
51	A compact millimeter-wave energy transmission system for wireless applications 2013,			11
50	A 2.4 GHz 0.1-Freq-Bandwidth All-Digital Phase-Locked Loop With Delay-Cell-Less TDC. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2013 , 60, 3145-3151	3.9		10
49	Design and Analysis of an 8 mW, 1 GHz Span, Passive Spectrum Scanner With >+31 dBm Out-of-Band IIP3 Using Periodically Time-Varying Circuit Components. <i>IEEE Journal of Solid-State Circuits</i> , 2017 , 52, 2009-2025	5.5		9
48	A low-area switched-resistor loop-filter technique for fractional-N synthesizers applied to a MEMS-based programmable oscillator 2010,			9
47	Design and Analysis of a Programmable Receiver Front End With Time-Interleaved Baseband Analog-FIR Filtering. <i>IEEE Journal of Solid-State Circuits</i> , 2018 , 53, 3197-3207	5.5		8
46	A fully integrated 22.6dBm mm-Wave PA in 40nm CMOS 2013,			8
45	Towards neuromote: a single-chip, 100-channel, neural-signal acquisition, processing, and telemetry device. <i>Annual International Conference of the IEEE Engineering in Medicine and Biology Society</i> , 2007 , 2007, 437-40			8
44	Frequency-domain analysis of a mixer-first receiver using conversion matrices 2015,			7
43	2013,			7
42	A power amplifier with minimal efficiency degradation under back-off 2010,			7

41	A sharp programmable passive filter based on filtering by Aliasing 2015 ,		6
40	Impedance Matching and Reradiation in LPTV Receiver Front-Ends: An Analysis Using Conversion Matrices. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2018 , 65, 2842-2855	3.9	6
39	A 50MHz bandwidth, 10-b ENOB, 8.2mW VCO-based ADC enabled by filtered-dithering based linearization 2013 ,		6
38	A temperature-to-digital converter for a MEMS-based programmable oscillator with better than ± 0.5 ppm frequency stability 2012 ,		6
37	A Digital Envelope Combiner for Switching Power Amplifier Linearization. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2010 , 57, 270-274	3.5	6
36	18.4 A 0.55nW/0.5V 32kHz Crystal Oscillator Based on a DC-Only Sustaining Amplifier for IoT 2019 ,		5
35	. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2011 , 58, 585-589	3.5	5
34	Digital techniques for integrated frequency synthesizers: A tutorial 2009 , 47, 126-133		5
33	Periodically Time-Varying Noise Cancellation for Filtering-by-Aliasing Receiver Front Ends. <i>IEEE Journal of Solid-State Circuits</i> , 2021 , 56, 928-939	5.5	5
32	11.1 Dual-MEMS-resonator temperature-to-digital converter with 40 K resolution and FOM of 0.12pJK ² 2016 ,		4
31	A Sub-nW 32-kHz Crystal Oscillator Architecture Based on a DC-Only Sustaining Amplifier. <i>IEEE Journal of Solid-State Circuits</i> , 2019 , 54, 3247-3256	5.5	4
30	A Theoretical Study of the Quantization Noise in Split Delta-Sigma ADCs. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2008 , 55, 1267-1278	3.9	4
29	The Effect of Noise Cross-Coupling on Time-Interleaved Delta-Sigma ADCs. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2008 , 55, 532-536	3.5	4
28	24.6 A time-interleaved filtering-by-aliasing receiver front-end with >70dB suppression at 2017 ,		3
27	Design and Analysis of a 1.8-GHz Open-Loop Modulator for Phase Modulation and Frequency Synthesis Using TDC-Based Calibration. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2017 , 65, 3975-3988	4.1	3
26	6.4 A 0.5-to-2.5GHz Multi-Output Fractional Frequency Synthesizer with 90fs Jitter and -106dBc Spurious Tones Based on Digital Spur Cancellation 2019 ,		3
25	Wave digital filter based analog circuit emulation on FPGA 2016 ,		3
24	A 3\$,times,\$ 3.8 Gb/s Four-Wire High Speed I/O Link Based on CDMA-Like Crosstalk Cancellation. <i>IEEE Journal of Solid-State Circuits</i> , 2010 , 45, 1522-1532	5.5	3

23	ACOUSTIC: Accelerating Convolutional Neural Networks through Or-Unipolar Skipped Stochastic Computing 2020,		3
22	An LPTV Noise Cancellation Technique for a 0.9-V Filtering-by-Aliasing Receiver Front-End with >67-dB Stopband Rejection 2019,		2
21	12.9 A 1.55 \times 0.85mm ² 3ppm 1.0 \times 32.768kHz MEMS-based oscillator 2014,		2
20	A precisely-timed energy injection technique achieving 58/10/2 μ s start-up in 1.84/10/50MHz crystal oscillators 2017,		2
19	A 1.85GHz CMOS power amplifier with zero-voltage-switching contour-based outphasing control to improve back-off efficiency 2015,		2
18	Filtering of subtractive discrete dither in quantizers: Some new results 2013,		2
17	A 2-MHz bandwidth Σ fractional-N synthesizer based on a fractional frequency divider with digital spur suppression 2010,		2
16	Power-efficient pulse width modulation DC/DC converters with zero voltage switching control 2006,		2
15	2016,		1
14	Training of digital predistortion based on signal-to-distortion-ratio measurements 2017,		1
13	Worst-Case Estimation for Data-Dependent Timing Jitter and Amplitude Noise in High-Speed Differential Link. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2012 , 20, 89-97	2.6	1
12	A programmable baseband anti-alias filter for a passive-mixer-based, SAW-less, multi-band, multi-mode WEDGE transmitter 2011,		1
11	A novel quantization noise-cancellation scheme in wideband D/A converters 2011,		1
10	A novel reconfigurable alias interference cancellation technique for A-to-D conversion 2011,		1
9	Open-Loop Wide-Bandwidth Phase Modulation Techniques. <i>Journal of Electrical and Computer Engineering</i> , 2011 , 2011, 1-12	1.9	1
8	Worst case timing jitter and amplitude noise in differential signaling 2009,		1
7	Addition to Σ Wideband 2.4-GHz Delta-Sigma Fractional- Σ PLL With 1-Mb/s In-Loop Modulation \square <i>IEEE Journal of Solid-State Circuits</i> , 2005 , 40, 559-559	5.5	1
6	A Dual-Channel High-Linearity Filtering-by-Aliasing Receiver Front-End Supporting Carrier Aggregation. <i>IEEE Journal of Solid-State Circuits</i> , 2021 , 1-1	5.5	1

5	A Broadband Class-AB Power Amplifier With Instantaneous Supply-Switching Efficiency Enhancement for Cable TV Application. <i>IEEE Journal of Solid-State Circuits</i> , 2018 , 53, 762-771	5.5	o
4	Theoretical Analysis of Circuit Non-Idealities in a Passive Spectrum Scanner Based on Periodically Time-Varying Circuit Components. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2018 , 65, 2403-2410	3.9	
3	Clock/Frequency Generation Circuits and Systems. <i>Journal of Electrical and Computer Engineering</i> , 2012 , 2012, 1-2	1.9	
2	Introduction to the Special Issue on the 2020 IEEE International Solid-State Circuits Conference (ISSCC). <i>IEEE Journal of Solid-State Circuits</i> , 2020 , 55, 3127-3130	5.5	
1	Errata for Design and Analysis of a Programmable Receiver Front End Based on Baseband Analog-FIR Filtering Using an LPTV Resistor[Jun 18 1592-1606]. <i>IEEE Journal of Solid-State Circuits</i> , 2018 , 53, 2733-2733	5.5	