

Khoa Le

List of Publications by Year in descending order

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Version: 2024-02-01

10
papers

138
citations

1684188

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h-index

2053705

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g-index

10
all docs

10
docs citations

10
times ranked

90
citing authors

#	ARTICLE	IF	CITATIONS
1	Design of High-Performance and Area-Efficient Decoder for 5G LDPC Codes. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 879-891.	5.4	17
2	An Enhanced Check-Node Architecture for 5G New Radio LDPC Decoders. , 2021, , .		0
3	A Probabilistic Parallel Bit-Flipping Decoder for Low-Density Parity-Check Codes. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 403-416.	5.4	13
4	A Decomposition Mapping based Quantized Belief Propagation Decoding for 5G LDPC Codes. , 2019, , .		0
5	Analysis and Design of Cost-Effective, High-Throughput LDPC Decoders. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 508-521.	3.1	31
6	Variable-Node-Shift Based Architecture for Probabilistic Gradient Descent Bit Flipping on QC-LDPC Codes. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 2183-2195.	5.4	14
7	The Probabilistic Finite Alphabet Iterative Decoder for Low-Density Parity-Check Codes. , 2018, , .		0
8	Efficient Hardware Implementation of Probabilistic Gradient Descent Bit-Flipping. IEEE Transactions on Circuits and Systems I: Regular Papers, 2017, 64, 906-917.	5.4	27
9	Efficient realization of probabilistic gradient descent bit flipping decoders. , 2015, , .		13
10	FPGA design of high throughput LDPC decoder based on imprecise Offset Min-Sum decoding. , 2015, , .		23