Khoa Le

List of Publications by Year
in descending order

10 papers	**138** citations	1684188 **5** h-index	2053705 **5** g-index
10 all docs	**10** docs citations	**10** times ranked	**90** citing authors

#	Article	IF	Citations
1	Analysis and Design of Cost-Effective, High-Throughput LDPC Decoders. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 508-521.	3.1	31
2	Efficient Hardware Implementation of Probabilistic Gradient Descent Bit-Flipping. IEEE Transactions on Circuits and Systems I: Regular Papers, 2017, 64, 906-917.	5.4	27
3	FPGA design of high throughput LDPC decoder based on imprecise Offset Min-Sum decoding. , 2015, , .		23
4	Design of High-Performance and Area-Efficient Decoder for 5G LDPC Codes. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 879-891.	5.4	17
5	Variable-Node-Shift Based Architecture for Probabilistic Gradient Descent Bit Flipping on QC-LDPC Codes. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 2183-2195.	5.4	14
6	Efficient realization of probabilistic gradient descent bit flipping decoders. , 2015, , .		13
7	A Probabilistic Parallel Bit-Flipping Decoder for Low-Density Parity-Check Codes. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 403-416.	5.4	13
8	The Probabilistic Finite Alphabet Iterative Decoder for Low-Density Parity-Check Codes. , 2018, , .		0
9	A Decomposition Mapping based Quantized Belief Propagation Decoding for 5G LDPC Codes. , 2019, , .		0
10	An Enhanced Check-Node Architecture for 5G New Radio LDPC Decoders. , 2021, , .		0