

Vivek Jangra

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	Low Power XNOR based Single Ended VCO Circuit Design with Dynamic Threshold MOS. , 2021, , .		0
2	A 0.28 GHz to 3.84 GHz low power differential ring oscillator design using cross-coupled transistors for radio frequency identification (RFID). , 2021, , .		0
3	Low power active load and IMOS varactor based VCO designs using differential delay stages in 0.18µm technology. Microelectronics Journal, 2020, 98, 104728.	2.0	3
4	New low power differential VCO circuit designs with active load and IMOS varactor. AEU - International Journal of Electronics and Communications, 2020, 119, 153191.	2.9	11
5	LOW POWER SINGLE ENDED VCO DESIGN WITH IMOS VARACTOR FOR WIRELESS APPLICATIONS. Telecommunications and Radio Engineering (English Translation of Elektrosvyaz and Radiotekhnika), 2020, 79, 1073-1082.	0.4	0
6	A wide tuning range VCO design using multi-pass loop complementary current control with IMOS varactor for low power applications. Engineering Science and Technology, an International Journal, 2019, 22, 1077-1086.	3.2	5
7	A low power VCO design using composite load for delay cell with IMOS varactor for wider tuning range. Journal of Information and Optimization Sciences, 2019, 40, 567-585.	0.3	0