

Stanisław J Piestrak

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	The Study of Monotonic Core Functions and Their Use to Build RNS Number Comparators. Electronics (Switzerland), 2021, 10, 1041.	3.1	1
2	RNS Number Comparator Based on a Modified Diagonal Function. Electronics (Switzerland), 2020, 9, 1784.	3.1	13
3	Design of RNS Reverse Converters with Constant Shifting to Residue Datapath Channels. Journal of Signal Processing Systems, 2018, 90, 323-339.	2.1	4
4	Design of Reverse Converters for a New Flexible RNS Five-Moduli Set $\{2^k, 2^{n-1}, 2^{n+1}, 2^{n+1}-1, 2^{n-1}-1\}$ (n Even). Circuits, Systems, and Signal Processing, 2017, 36, 4593-4614.	2.0	6
5	Hardware/Software Approach to Designing Low-Power RNS-Enhanced Arithmetic Units. IEEE Transactions on Circuits and Systems I: Regular Papers, 2017, 64, 1031-1039.	5.4	14
6	A note on RNS architectures for the implementation of the diagonal function. Information Processing Letters, 2015, 115, 453-457.	0.6	16
7	Design of Reverse Converters for the New RNS Moduli Set $\{2^k, 2^{n-1}, 2^{n+1}, 2^{n+1}-1, 2^{n-1}-1\}$ (n Even). Transactions on Circuits and Systems I: Regular Papers, 2014, 61, 3436-3449.	5.4	18
8	Private reliability environments for efficient fault-tolerance in CGRAs. Design Automation for Embedded Systems, 2014, 18, 295-327.	1.0	2
9	Design of Reverse Converters for General RNS Moduli Sets $\{2^k, 2^{n-1}, 2^{n+1}, 2^{n+1}-1, 2^{n-1}-1\}$. Transactions on Circuits and Systems I: Regular Papers, 2014, 61, 1687-1700.	5.4	31
10	Energy-Aware Fault-Tolerant CGRAs Addressing Application with Different Reliability Needs. , 2013, , .		12
11	Design of an RNS reverse converter for a new five-moduli special set. , 2012, , .		5
12	Error recovery technique for coarse-grained reconfigurable architectures. , 2011, , .		17
13	Design of multi-residue generators using shared logic. , 2011, , .		13
14	Design of a fault-tolerant coarse-grained. , 2010, , .		17
15	Exploiting residue number system for power-efficient digital signal processing in embedded processors. , 2009, , .		29