

Matthias Hiller

List of Publications by Year in descending order

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Version: 2024-02-01

32
papers

566
citations

1307594

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1372567

10
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33
all docs

33
docs citations

33
times ranked

404
citing authors

#	ARTICLE	IF	CITATIONS
1	A Lockdown Technique to Prevent Machine Learning on PUFs for Lightweight Authentication. IEEE Transactions on Multi-Scale Computing Systems, 2016, 2, 146-159.	2.4	142
2	Complementary IBS: Application specific error correction for PUFs. , 2012, , .		51
3	Efficient Fuzzy Extraction of PUF-Induced Secrets: Theory and Applications. Lecture Notes in Computer Science, 2016, , 412-431.	1.3	48
4	Breaking through fixed PUF block limitations with differential sequence coding and convolutional codes. , 2013, , .		41
5	Cherry-Picking Reliable PUF Bits With Differential Sequence Coding. IEEE Transactions on Information Forensics and Security, 2016, 11, 2065-2076.	6.9	34
6	Review of error correction for PUFs and evaluation on state-of-the-art FPGAs. Journal of Cryptographic Engineering, 2020, 10, 229-247.	1.8	28
7	Absolute blood velocity measured with a modified fundus camera. Journal of Biomedical Optics, 2010, 15, 056014.	2.6	26
8	B-TREPID: Batteryless tamper-resistant envelope with a PUF and integrity detection. , 2018, , .		25
9	A systematic study of lightweight hash functions on FPGAs. , 2014, , .		19
10	Take a moment and have some t: Hypothesis testing on raw PUF data. , 2017, , .		16
11	Maximum-likelihood decoding of device-specific multi-bit symbols for reliable key generation. , 2015, , .		15
12	Hiding Secrecy Leakage in Leaky Helper Data. Lecture Notes in Computer Science, 2017, , 601-619.	1.3	14
13	Systematic Low Leakage Coding for Physical Unclonable Functions. , 2015, , .		13
14	Seesaw: An Area-Optimized FPGA Viterbi Decoder for PUFs. , 2014, , .		11
15	Variable-Length Bit Mapping and Error-Correcting Codes for Higher-Order Alphabet PUFs. Lecture Notes in Computer Science, 2017, , 190-209.	1.3	10
16	Low-Area Reed Decoding in a Generalized Concatenated Code Construction for PUFs. , 2015, , .		9
17	Statistic-based security analysis of ring oscillator PUFs. , 2014, , .		8
18	An embedded key management system for PUF-based security enclosures. , 2018, , .		7

#	ARTICLE	IF	CITATIONS
19	Increasing the efficiency of syndrome coding for PUFs with helper data compression. , 2014, , .		6
20	Advanced performance metrics for Physical Unclonable Functions. , 2014, , .		6
21	A new model for estimating bit error probabilities of Ring-Oscillator PUFs. , 2013, , .		5
22	Online Reliability Testing for PUF Key Derivation. , 2016, , .		5
23	A Measurement System for Capacitive PUF-Based Security Enclosures. , 2018, , .		5
24	Efficient Evaluation of Physical Unclonable Functions Using Entropy Measures. Journal of Circuits, Systems and Computers, 2016, 25, 1640001.	1.5	4
25	Variable-Length Bit Mapping and Error-Correcting Codes for Higher-Order Alphabet PUFsâ€™Extended Version. Journal of Hardware and Systems Security, 2019, 3, 78-93.	1.3	4
26	Increasing the efficiency of syndrome coding for PUFs with helper data compression. , 2014, , .		3
27	A measurement system for capacitive PUF-based security enclosures. , 2018, , .		3
28	Nano Security: From Nano-Electronics to Secure Systems. , 2021, , .		3
29	Reliability bound and channel capacity of IBS-based fuzzy embedders. , 2012, , .		2
30	A Security Architecture for RISC-V based IoT Devices. , 2019, , .		2
31	Secure Code Execution: A Generic PUF-Driven System Architecture. Lecture Notes in Computer Science, 2018, , 25-46.	1.3	1
32	Attacks and Countermeasures for Capacitive PUF-Based Security Enclosures. , 2021, , .		0