## Andre V Fidalgo

List of Publications by Year in descending order

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#	Article	IF	CITATIONS
1	A roadmap for the VISIR remote lab. European Journal of Engineering Education, 2023, 48, 880-898.	1.5	5
2	Using VISIR Remote Lab in the Classroom: Case of Study of the University of Deusto 2009–2019. Advances in Intelligent Systems and Computing, 2021, , 82-102.	0.5	5
3	A comprehensive VISIR bibliographical reference. , 2021, , .		5
4	Students' perception about using VISIR. , 2021, , .		1
5	A framework for interpreting experimental errors in VISIR. , 2019, , .		1
6	e-Engineering: engineering school at home without compromise. , 2019, , .		1
7	A sustainable approach to laboratory experimentation. , 2019, , .		8
8	A sustainable approach to let students do more real experiments with electrical and electronic circuits. , 2018, , .		3
9	Transistor teaching back to Transfer-Resistor : A summary table of definitions and students' perceptions. , 2018, , .		0
10	PILAR: a Federation of VISIR Remote Laboratory Systems for Educational Open Activities. , 2018, , .		10
11	First Practical Steps on the Educational Activities Using VISIR and Remote Laboratories at UNSE in Partnership with UNED inside the VISIR+ Project. , 2018, , .		3
12	Experimenting in PILAR federation: A common path for the future. , 2018, , .		7
13	Impact of a remote lab on teaching practices and student learning. Computers and Education, 2018, 126, 201-216.	5.1	72
14	International Cooperation for Remote Laboratory Use. , 2018, , 1-31.		6
15	The VISIR+ project-helping contextualize math in an engineering course. , 2017, , .		5
16	Sharing educational experiences from in-person classroom to collaborative lab environments. , 2017, ,		3
17	Do Students Really Understand the Difference Between Simulation and Remote Labs?. , 2017, ,		3
18	Using Remote Lab for Enhancing E-Learning on FPAAs. International Journal of Online Engineering, 2016, 12, 58.	0.5	0

#	Article	IF	CITATIONS
19	How to Use Remote Labs for Enhancing E-Learning on PSoCs. International Journal of Online Engineering, 2016, 12, 61.	0.5	1
20	Spreading remote lab usage a system — A community — A Federation. , 2016, , .		23
21	Reshaping digital methodologies to the analog world. , 2016, , .		1
22	Lab sessions in VISIR laboratories. , 2016, , .		7
23	A remote lab to support e-learning on FPAA. , 2015, , .		1
24	Low cost boundary scan controller for didactic applications (IEEE 1149.1). , 2015, , .		1
25	A remote lab to support e-leaning on Programmable System-on-Chip (PSoC). , 2015, , .		0
26	EOLES course the first accredited on-line degree course in electronics and optics for embedded systems. , 2015, , .		7
27	How Remote Labs Impact on Course Outcomes: Various Practices Using VISIR. IEEE Transactions on Education, 2014, 57, 151-159.	2.0	75
28	High order experimental skills' gap identification — The need to reshape electronics teaching. , 2014, , .		0
29	Adapting Remote Labs to Learning Scenarios: Case Studies Using VISIR and RemotElectLab. Revista Iberoamericana De Tecnologias Del Aprendizaje, 2014, 9, 33-39.	0.7	14
30	A demonstration circuit to support e-learning on IEEE 1149.1/4 infrastructures. , 2014, , .		1
31	Application of mobile devices and remote experiments for physics teaching in elementary education. , 2013, , .		3
32	Remote experimentation using mobile devices. , 2013, , .		3
33	A mobile robot platform for open learning based on serious games and remote laboratories. , 2013, , .		12
34	Using remote labs to serve different teacher's needs A case study with VISIR and RemotElectLab. , 2012, ,		14
35	Using Remote Labs to Serve Different Teacherâ??s Needs - A Case Study with VISIR and RemotElectLab. International Journal of Online and Biomedical Engineering, 2012, 8, 36.	0.9	2

36 Ethernet communication platform for synthesized devices in Xilinx FPGA. , 2011, , .

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#	Article	IF	CITATIONS
37	Real-time fault injection using enhanced on-chip debug infrastructures. Microprocessors and Microsystems, 2011, 35, 441-452.	1.8	18
38	Using test infrastructures for (remote) online evaluation of the sensitivity to SEUs of FPGAs. , 2009, , .		0
39	A comparative analysis of fault injection methods via enhanced on-chip debug infrastructures. , 2008, ,		2
40	Real Time Fault Injection Using Enhanced OCD A Performance Analysis. Defect and Fault Tolerance in VLSI Systems, Proceedings of the IEEE International Symposium on, 2006, , .	0.0	7
41	Using NEXUS compliant debuggers for real time fault injection on microprocessors. , 2006, , .		8
42	Real Time Fault Injection Using a Modified Debugging Infrastructure. , 0, , .		13
43	A Modified Debugging Infrastructure to Assist Real Time Fault Injection Campaigns. , 0, , .		6