

Sungkyung Park

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	Spatial Data Dependence Graph Based Pre-RTL Simulator for Convolutional Neural Network Dataflows. IEEE Access, 2022, 10, 11382-11403.	2.6	1
2	Optimization of Multi-Core Accelerator Performance Based on Accurate Performance Estimation. IEEE Access, 2022, 10, 19629-19642.	2.6	2
3	Modeling and Simulation of System Bus and Memory Collisions in Heterogeneous SoCs. IEEE Access, 2022, 10, 25901-25921.	2.6	0
4	Design and Implementation of a Digital Front-End With Digital Compensation for Low-Complexity 4G Radio Transceivers. IEEE Access, 2021, 9, 111432-111455.	2.6	1
5	Design and Implementation of a Farrow-Interpolator-Based Digital Front-End in LTE Receivers for Carrier Aggregation. Electronics (Switzerland), 2021, 10, 231.	1.8	3
6	Implementation of a Fast Link Rate Adaptation Algorithm for WLAN Systems. Electronics (Switzerland), 2021, 10, 91.	1.8	2
7	System-Level Communication Performance Estimation for DMA-Controlled Accelerators. IEEE Access, 2021, 9, 141389-141402.	2.6	2
8	Optimization of Communication Schemes for DMA-Controlled Accelerators. IEEE Access, 2021, 9, 139228-139247.	2.6	4
9	Roofline-Model-Based Design Space Exploration for Dataflow Techniques of CNN Accelerators. IEEE Access, 2020, 8, 172509-172523.	2.6	12
10	Optimizations of Scatter Network for Sparse CNN Accelerators. , 2019, , .		1
11	Spatial Data Dependence Graph Simulator for Convolutional Neural Network Accelerators. , 2019, , .		2
12	Latency-Insensitive Controller for Convolutional Neural Network Accelerators. , 2019, , .		2
13	Fractional-N phase-locked loop for split and direct automatic frequency control in A-GPS. International Journal of Electronics, 2018, 105, 1200-1216.	0.9	0
14	CSD-based CORDIC algorithm and its VLSI implementation. IEICE Electronics Express, 2016, 13, 20160393-20160393.	0.3	1
15	Impact of power amplifier configuration on LTE carrier aggregation performance. IEICE Electronics Express, 2016, 13, 20160183-20160183.	0.3	0
16	Quantization Noise Analysis of Time-to-Digital-Converter-Based All-Digital Phase-Locked Loop and Frequency Discriminators. Journal of Circuits, Systems and Computers, 2016, 25, 1650131.	1.0	0
17	Analysis of RSRP Measurement Accuracy. IEEE Communications Letters, 2016, 20, 430-433.	2.5	14
18	Reconfigurable radio receiver with fractional sample rate converter and multi-rate ADC based on LO-derived sampling clock. International Journal of Electronics, 0, , 1-15.	0.9	0