

# Ragh Kuttappa

## List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	Multiphase Digital Low-Dropout Regulators. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2022, 30, 40-50.	3.1	0
2	Resonant Clock Synchronization With Active Silicon Interposer for Multi-Die Systems. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 1636-1645.	5.4	8
3	Comprehensive Low Power Adiabatic Circuit Design with Resonant Power Clocking. , 2020, , .		2
4	SnackNoC: Processing in the Communication Layer. , 2020, , .		6
5	FOPAC: Flexible On-Chip Power and Clock. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 4628-4636.	5.4	1
6	RotaSYN: Rotary Traveling Wave Oscillator SYNthesizer. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 2685-2698.	5.4	7
7	Distributed Digital Low-Dropout Regulators with Phase Interleaving for On-Chip Voltage Noise Mitigation. , 2019, , .		3
8	Robust Low Power Clock Synchronization for Multi-Die Systems. , 2019, , .		4
9	Low Frequency Rotary Traveling Wave Oscillators. , 2018, , .		5
10	Stability of Rotary Traveling Wave Oscillators under process variations and NBTI. , 2017, , .		4
11	Reliability analysis of spin transfer torque based look up tables under process variations and NBTI aging. Microelectronics Reliability, 2016, 62, 156-166.	1.7	4