Laércio L Pilla

List of Publications by Year
in descending order

50 papers	**593** citations
932766 **10** h-index	839053 **18** g-index
50 all docs	**50** docs citations
50 times ranked	**427** citing authors

#	Article	IF	Citations
1	Evaluation and Mitigation of Radiation-Induced Soft Errors in Graphics Processing Units. IEEE Transactions on Computers, 2016, 65, 791-804.	2.4	73
2	Impact of GPUs Parallelism Management on Safety-Critical and HPC Applications Reliability. , 2014, , .		60
3	Characterizing communication and page usage of parallel applications for thread and data mapping. Performance Evaluation, 2015, 88-89, 18-36.	0.9	49
4	Experimental and analytical study of Xeon Phi reliability. , 2017, , .		46
5	Software-Based Hardening Strategies for Neutron Sensitive FFT Algorithms on GPUs. IEEE Transactions on Nuclear Science, 2014, 61, 1874-1880.	1.2	29
6	A Hierarchical Approach for Load Balancing on Parallel Multi-core Systems. , 2012, , .		26
7	Radiation-Induced Error Criticality in Modern HPC Parallel Accelerators. , 2017, , .		26
8	A topology-aware load balancing algorithm for clustered hierarchical multi-core machines. Future Generation Computer Systems, 2014, 30, 191-201.	4.9	24
9	Performance/energy trade€off in scientific computing: the case of ARM big.LITTLE and Intel Sandy Bridge. IET Computers and Digital Techniques, 2015, 9, 27-35.	0.9	23
10	An Efficient Algorithm for Communication-Based Task Mapping. , 2015, , .		23
11	Evaluating application performance and energy consumption on hybrid CPU+GPU architecture. Cluster Computing, 2013, 16, 511-525.	3.5	18
12	GPGPUs ECC efficiency and efficacy. , 2014, , .		15
13	A Cost Model for IaaS Clouds Based on Virtual Machine Energy Consumption. Journal of Grid Computing, 2018, 16, 493-512.	2.5	15
14	Hardware-Assisted Thread and Data Mapping in Hierarchical Multicore Architectures. Transactions on Architecture and Code Optimization, 2016, 13, 1-28.	1.6	15
15	EagerMap. ACM Transactions on Parallel Computing, 2018, 5, 1-24.	1.2	12
16	Asymptotically Optimal Load Balancing for Hierarchical Multi-Core Systems. , 2012, , .		11
17	LAPT: A locality-aware page table for thread and data mapping. Parallel Computing, 2016, 54, 59-71.	1.3	10
18	Improving Performance on Atmospheric Models through a Hybrid OpenMP/MPI Implementation. , 2011, , .		9

#	Article	IF	Citations
19	Improving the Performance of Seismic Wave Simulations with Dynamic Load Balancing. , 2014, , .		9
20	A branch and bound strategy for Fast Trajectory Similarity Measuring. Data and Knowledge Engineering, 2018, 115, 16-31.	2.1	9
21	OBSERVING THE IMPACT OF MULTIPLE METRICS AND RUNTIME ADAPTATIONS ON BSP PROCESS RESCHEDULING. Parallel Processing Letters, 2010, 20, 123-144.	0.4	8
22	Saving energy by exploiting residual imbalances on iterative applications. , 2014, , .		6
23	Optimizing Memory Locality Using a Locality-Aware Page Table. , 2014, , .		6
24	A Sharing-Aware Memory Management Unit for Online Mapping in Multi-core Architectures. Lecture Notes in Computer Science, 2016, , 490-501.	1.0	6
25	Algorithm Selection Framework for Legalization Using Deep Convolutional Neural Networks and Transfer Learning. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2022, 41, 1481-1494.	1.9	6
26	Radiation Sensitivity of High Performance Computing Applications on Kepler-Based GPGPUs. , 2014, , .		5
27	MigPF: Towards on self-organizing process rescheduling of Bulk-Synchronous Parallel applications. Future Generation Computer Systems, 2018, 78, 272-286.	4.9	5
28	A Batch Task Migration Approach for Decentralized Global Rescheduling. , 2018, , .		5
29	Improving Communication and Load Balancing with Thread Mapping in Manycore Systems. , 2018, , .		5
30	Controlling Processes Reassignment in BSP Applications. , 2008, , .		4
31	Neutron sensitivity and software hardening strategies for matrix multiplication and FFT on graphics processing units. , 2013, , .		4
32	Exploration of Load Balancing Thresholds to Save Energy on Iterative Applications. Communications in Computer and Information Science, 2017, , 76-88.	0.4	4
33	MigBSP: A Novel Migration Model for Bulk-Synchronous Parallel Processes Rescheduling. , 2009, , .		3
34	Memory Access Time and Input Size Effects on Parallel Processors Reliability. IEEE Transactions on Nuclear Science, 2015, 62, 2627-2634.	1.2	3
35	PackStealLB: A scalable distributed load balancer based on work stealing and workload discretization. Journal of Parallel and Distributed Computing, 2021, 150, 34-45.	2.7	3
36	Applying Processes Rescheduling over Irregular BSP Application. Lecture Notes in Computer Science, 2009, , 213-223.	1.0	3

#	Article	IF	Citations
37	ICE: Managing Multiple Clusters Using Web Services. , 2008, , .		2
38	ComprehensiveBench: a Benchmark for the Extensive Evaluation of Global Scheduling Algorithms. Journal of Physics: Conference Series, 2015, 649, 012007.	0.3	2
39	Value Reuse Potential in ARM Architectures. , 2016, , .		2
40	How Game Engines Can Inspire EDA Tools Development. , 2017, , .		2
41	ARTful: A model for userâ€defined schedulers targeting multiple highâ€performance computing runtime systems. Software - Practice and Experience, 2021, 51, 1622.	2.5	2
42	Understanding the Effect of Multiple Factors on a Parallel File System's Performance. , 2015, , .		1
43	The Path to Exascale. , 2015, , .		1
44	Exploiting parallelism to speed up circuit legalization. , 2016, , .		1
45	Distributed Memory Graph Representation for Load Balancing Data: Accelerating Data Structure Generation for Decentralized Scheduling. , 2019, , .		1
46	Adaptive Load Balancing based on Machine Learning for Iterative Parallel Applications. , 2020, , .		1
47	Supporting performance and adaptivity on BSP process rescheduling. , 2010, , .		0
48	Combining Multiple Metrics to Control BSP Process Rescheduling in Response to Resource and Application Dynamics. , 2011, , .		0
49	Online Thread and Data Mapping Using a Sharing-Aware Memory Management Unit. ACM Transactions on Modeling and Performance Evaluation of Computing Systems, 2020, 5, 1-28.	0.8	0
50	Applying Process Migration on a BSP-Based LU Decomposition Application. Lecture Notes in Computer Science, 2011, , 314-326.	1.0	0