

Francisco Tirado

List of Publications by Year in descending order

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87
papers

2,112
citations

430442

18
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243296

44
g-index

89
all docs

89
docs citations

89
times ranked

2747
citing authors

#	ARTICLE	IF	CITATIONS
1	Leveraging knowledge-as-a-service (KaaS) for QoS-aware resource management in multi-user video transcoding. <i>Journal of Supercomputing</i> , 2020, 76, 9388-9403.	2.4	3
2	CEPRAM: Compression for Endurance in PCM RAM. <i>Journal of Circuits, Systems and Computers</i> , 2017, 26, 1750167.	1.0	0
3	Energy Efficiency Optimization of Task-Parallel Codes on Asymmetric Architectures. , 2017, , .		5
4	Non-negative Matrix Factorization on Low-Power Architectures and Accelerators: A Comparative Study. <i>Computers and Electrical Engineering</i> , 2015, 46, 139-156.	3.0	1
5	NMF-mGPU: non-negative matrix factorization on multi-GPU systems. <i>BMC Bioinformatics</i> , 2015, 16, 43.	1.2	45
6	Write-Aware Replacement Policies for PCM-Based Systems. <i>Computer Journal</i> , 2015, 58, 2000-2025.	1.5	5
7	IMPROVING peLIFO CACHE REPLACEMENT POLICY: HARDWARE REDUCTION AND THREAD-AWARE EXTENSION. <i>Journal of Circuits, Systems and Computers</i> , 2014, 23, 1450046.	1.0	0
8	Disasters as Meshworks: Migratory Birds and the Enlivening of DoÃ±ana's Toxic Spill. <i>Sociological Review</i> , 2014, 62, 38-60.	0.9	9
9	Acceleration of block-matching algorithms using a custom instruction-based paradigm on a Nios II microprocessor. <i>Eurasip Journal on Advances in Signal Processing</i> , 2013, 2013, .	1.0	27
10	Robust motion estimation on a low-power multi-core DSP. <i>Eurasip Journal on Advances in Signal Processing</i> , 2013, 2013, .	1.0	9
11	Multi-GPU based on multicriteria optimization for motion estimation system. <i>Eurasip Journal on Advances in Signal Processing</i> , 2013, 2013, .	1.0	15
12	Reducing Writes in Phase-Change Memory Environments by Using Efficient Cache Replacement Policies. , 2013, , .		12
13	Fault Tolerance on NoCs. , 2013, , .		4
14	Low complexity bit-parallel polynomial basis multipliers over binary fields for special irreducible pentanomials. <i>The Integration VLSI Journal</i> , 2013, 46, 197-210.	1.3	10
15	Non-negative matrix factorization on low-power architectures. , 2013, , .		1
16	Offset Printing Plate Quality Sensor on a Low-Cost Processor. <i>Sensors</i> , 2013, 13, 14277-14300.	2.1	7
17	GPU-based acceleration of bio-inspired motion estimation model. <i>Concurrency Computation Practice and Experience</i> , 2013, 25, 1037-1056.	1.4	19
18	A Low Cost Matching Motion Estimation Sensor Based on the NIOS II Microprocessor. <i>Sensors</i> , 2012, 12, 13126-13149.	2.1	30

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19	REDUCING CACHE HIERARCHY ENERGY CONSUMPTION BY PREDICTING FORWARDING AND DISABLING ASSOCIATIVE SETS. <i>Journal of Circuits, Systems and Computers</i> , 2012, 21, 1250057.	1.0	3
20	eHR software, multinational corporations and emerging China: Exploring the role of information through a postcolonial lens. <i>Information and Organization</i> , 2012, 22, 106-124.	3.1	17
21	Wireless Measurement System for Structural Health Monitoring With High Time-Synchronization Accuracy. <i>IEEE Transactions on Instrumentation and Measurement</i> , 2012, 61, 801-810.	2.4	86
22	Hybrid timing-address oriented load-store queue filtering for an x86 architecture. <i>IET Computers and Digital Techniques</i> , 2011, 5, 145.	0.9	1
23	On-Line Multi-Threaded Processing of Web User-Clicks on Multi-Core Processors. <i>Lecture Notes in Computer Science</i> , 2011, , 222-235.	1.0	0
24	Stack filter: Reducing L1 data cache power consumption. <i>Journal of Systems Architecture</i> , 2010, 56, 685-695.	2.5	8
25	MARQ: an online tool to mine GEO for experiments with similar or opposite gene expression signatures. <i>Nucleic Acids Research</i> , 2010, 38, W228-W232.	6.5	23
26	Building efficient multi-threaded search nodes. , 2010, , .		5
27	Not Just Software. <i>International Journal of Technoethics</i> , 2010, 1, 27-36.	0.6	2
28	The Question of Movement in Dwelling. <i>Space and Culture</i> , 2009, 12, 371-382.	0.6	9
29	GeneCodis: interpreting gene lists through enrichment analysis and integration of diverse biological information. <i>Nucleic Acids Research</i> , 2009, 37, W317-W322.	6.5	391
30	SENT: semantic features in text. <i>Nucleic Acids Research</i> , 2009, 37, W153-W159.	6.5	5
31	Using age registers for a simple load-store queue filtering. <i>Journal of Systems Architecture</i> , 2009, 55, 79-89.	2.5	2
32	Endmember extraction from hyperspectral imagery using a parallel ensemble approach with consensus analysis. , 2009, , .		2
33	Replacing Associative Load Queues: A Timing-Centric Approach. <i>IEEE Transactions on Computers</i> , 2009, 58, 496-511.	2.4	1
34	Stack oriented data cache filtering. , 2009, , .		2
35	Parallel Implementation of the 2D Discrete Wavelet Transform on Graphics Processing Units: Filter Bank versus Lifting. <i>IEEE Transactions on Parallel and Distributed Systems</i> , 2008, 19, 299-310.	4.0	85
36	Energy reduction of the fetch mechanism through dynamic adaptation. <i>IET Computers and Digital Techniques</i> , 2008, 2, 94.	0.9	0

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37	Applying speculation techniques to implement functional units. , 2008, , .		4
38	bioNMF: a web-based tool for nonnegative matrix factorization in biology. Nucleic Acids Research, 2008, 36, W523-W528.	6.5	31
39	GPU for Parallel On-Board Hyperspectral Image Processing. International Journal of High Performance Computing Applications, 2008, 22, 424-437.	2.4	42
40	Improving Search Engines Performance on Multithreading Processors. Lecture Notes in Computer Science, 2008, , 201-213.	1.0	5
41	GENECODIS: a web-based tool for finding significant concurrent annotations in gene lists. Genome Biology, 2007, 8, R3.	13.9	554
42	Parallel Morphological Endmember Extraction Using Commodity Graphics Hardware. IEEE Geoscience and Remote Sensing Letters, 2007, 4, 441-445.	1.4	58
43	Real-Time Onboard Hyperspectral Image Processing Using Programmable Graphics Hardware. Chapman & Hall/CRC Computer and Information Science Series, 2007, , 411-451.	0.4	5
44	Low Complexity Bit-Parallel Multipliers Based on a Class of Irreducible Pentanomials. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2006, 14, 1388-1393.	2.1	26
45	Bit-parallel finite field multipliers for irreducible trinomials. IEEE Transactions on Computers, 2006, 55, 520-533.	2.4	45
46	bioNMF: a versatile tool for non-negative matrix factorization in biology. BMC Bioinformatics, 2006, 7, 366.	1.2	77
47	Biclustering of gene expression data by Non-smooth Non-negative Matrix Factorization. BMC Bioinformatics, 2006, 7, 78.	1.2	158
48	LSQ: a power efficient and scalable implementation. IEE Proceedings: Computers and Digital Techniques, 2006, 153, 389.	1.6	1
49	DMDC: Delayed Memory Dependence Checking through Age-Based Filtering. Microarchitecture (MICRO), Proceedings of the Annual International Symposium on, 2006, , .	0.0	7
50	A Load-Store Queue Design Based on Predictive State Filtering. Journal of Low Power Electronics, 2006, 2, 27-36.	0.6	5
51	Improving superword level parallelism support in modern compilers. , 2005, , .		11
52	A Power-Efficient and Scalable Load-Store Queue Design. Lecture Notes in Computer Science, 2005, , 1-9.	1.0	5
53	Energy Characterization of Garbage Collectors for Dynamic Applications on Embedded Systems. Lecture Notes in Computer Science, 2005, , 69-78.	1.0	3
54	Adaptive Tuning of Reserved Space in an Appel Collector. Lecture Notes in Computer Science, 2004, , 542-558.	1.0	4

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55	A parallel multigrid solver for viscous flows on anisotropic structured grids. <i>Parallel Computing</i> , 2003, 29, 907-923.	1.3	5
56	Customizing the branch predictor to reduce complexity and energy consumption. <i>IEEE Micro</i> , 2003, 23, 12-25.	1.8	23
57	Branch prediction on demand. , 2003, , .		30
58	Hybrid parallelization of a compact genetic algorithm. , 2003, , .		10
59	Wavelet Transform for Large Scale Image Processing on Modern Microprocessors. <i>Lecture Notes in Computer Science</i> , 2003, , 549-562.	1.0	6
60	A multigrid solver for the incompressible Navier-Stokes equations on a Beowulf-class system. , 2001, , .		1
61	Analysing value substitution and confidence estimation for value prediction. <i>Journal of Systems Architecture</i> , 2001, 47, 459-475.	2.5	0
62	Parallel Multigrid for Anisotropic Elliptic Equations. <i>Journal of Parallel and Distributed Computing</i> , 2001, 61, 96-114.	2.7	21
63	Value Prediction as a Cost-Effective Solution to Improve Embedded Processors Performance. <i>Lecture Notes in Computer Science</i> , 2001, , 181-195.	1.0	0
64	Data locality exploitation in the decomposition of regular domain problems. <i>IEEE Transactions on Parallel and Distributed Systems</i> , 2000, 11, 1141-1150.	4.0	17
65	Impact of PE Mapping on Cray T3E Message-Passing Performance. <i>Lecture Notes in Computer Science</i> , 2000, , 199-207.	1.0	4
66	An environment to develop parallel code for solving partial differential equations based-problems. <i>Journal of Systems Architecture</i> , 1999, 45, 543-554.	2.5	0
67	Unified data path allocation and BIST intrusion. <i>The Integration VLSI Journal</i> , 1999, 28, 55-99.	1.3	3
68	Implementation of Hybrid Context Based Value Predictors Using Value Sequence Classification. <i>Lecture Notes in Computer Science</i> , 1999, , 1291-1295.	1.0	8
69	Partitioning Regular Domains on Modern Parallel Computers. <i>Lecture Notes in Computer Science</i> , 1999, , 411-424.	1.0	4
70	Relationships between efficiency and execution time of full multigrid methods on parallel computers. <i>IEEE Transactions on Parallel and Distributed Systems</i> , 1997, 8, 562-573.	4.0	21
71	A method for area estimation of data-path in high level synthesis. <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , 1996, 15, 258-265.	1.9	18
72	Some aspects about the scalability of scientific applications on parallel architectures. <i>Parallel Computing</i> , 1996, 22, 1169-1195.	1.3	12

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73	FIDIAS: an integral approach to high-level synthesis. IET Circuits, Devices and Systems, 1995, 142, 227.	0.6	6
74	Guidance for optimization-based synthesis tools. Microprocessing and Microprogramming, 1993, 37, 95-98.	0.3	0
75	Data path structures and heuristics for testable allocation in high level synthesis. Microprocessing and Microprogramming, 1993, 39, 263-266.	0.3	3
76	Design control in a high level synthesis system. Microprocessing and Microprogramming, 1992, 34, 93-96.	0.3	2
77	A hardware allocator guided by cost functions. Microprocessing and Microprogramming, 1991, 32, 185-192.	0.3	4
78	Microprocessor instruction for undergraduate students. Education and Computing, 1988, 4, 265-272.	0.3	1
79	The WIRMI System: A Multimicrocomputer-Based System for Hotel Automation. IEEE Transactions on Industrial Electronics, 1984, IE-31, 317-321.	5.2	1
80	RDBAS: A relational database system for non-experienced users. Microprocessing and Microprogramming, 1983, 12, 99-109.	0.3	0
81	An architectural design for simultaneous microdiagnostic. Microprocessing and Microprogramming, 1982, 9, 27-37.	0.3	0
82	A general purpose computer emulator. Euromicro Newsletter, 1978, 4, 133-140.	0.1	0
83	Power-efficient value speculation for high-performance microprocessors. , 0, , .		2
84	A parallel compact genetic algorithm for multi-FPGA partitioning. , 0, , .		10
85	Garbage collector refinement for new dynamic multimedia applications on embedded systems. , 0, , .		0
86	Dynamic management of nursery space organization in generational collection. , 0, , .		2
87	Load-store queue management: an energy-efficient design based on a state-filtering mechanism. , 0, , .		7