Francisco Tirado

List of Publications by Year
in descending order

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87	2,112	430754	243529
papers	citations	18 h-index	44 g-index

89	89	89	2747
all docs	docs citations	times ranked	citing authors

#	Article	IF	Citations
1	GENECODIS: a web-based tool for finding significant concurrent annotations in gene lists. Genome Biology, 2007, 8, R3.	13.9	554
2	GeneCodis: interpreting gene lists through enrichment analysis and integration of diverse biological information. Nucleic Acids Research, 2009, 37, W317-W322.	6.5	391
3	Biclustering of gene expression data by Non-smooth Non-negative Matrix Factorization. BMC Bioinformatics, 2006, 7, 78.	1.2	158
4	Wireless Measurement System for Structural Health Monitoring With High Time-Synchronization Accuracy. IEEE Transactions on Instrumentation and Measurement, 2012, 61, 801-810.	2.4	86
5	Parallel Implementation of the 2D Discrete Wavelet Transform on Graphics Processing Units: Filter Bank versus Lifting. IEEE Transactions on Parallel and Distributed Systems, 2008, 19, 299-310.	4.0	85
6	bioNMF: a versatile tool for non-negative matrix factorization in biology. BMC Bioinformatics, 2006, 7, 366.	1.2	77
7	Parallel Morphological Endmember Extraction Using Commodity Graphics Hardware. IEEE Geoscience and Remote Sensing Letters, 2007, 4, 441-445.	1.4	58
8	Bit-parallel finite field multipliers for irreducible trinomials. IEEE Transactions on Computers, 2006, 55, 520-533.	2.4	45
9	NMF-mGPU: non-negative matrix factorization on multi-GPU systems. BMC Bioinformatics, 2015, 16, 43.	1.2	45
10	GPU for Parallel On-Board Hyperspectral Image Processing. International Journal of High Performance Computing Applications, 2008, 22, 424-437.	2.4	42
11	bioNMF: a web-based tool for nonnegative matrix factorization in biology. Nucleic Acids Research, 2008, 36, W523-W528.	6.5	31
12	Branch prediction on demand. , 2003, , .		30
13	A Low Cost Matching Motion Estimation Sensor Based on the NIOS II Microprocessor. Sensors, 2012, 12, 13126-13149.	2.1	30
14	Acceleration of block-matching algorithms using a custom instruction-based paradigm on a Nios II microprocessor. Eurasip Journal on Advances in Signal Processing, 2013, 2013, .	1.0	27
15	Low Complexity Bit-Parallel Multipliers Based on a Class of Irreducible Pentanomials. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2006, 14, 1388-1393.	2.1	26
16	Customizing the branch predictor to reduce complexity and energy consumption. IEEE Micro, 2003, 23, 12-25.	1.8	23
17	MARQ: an online tool to mine GEO for experiments with similar or opposite gene expression signatures. Nucleic Acids Research, 2010, 38, W228-W232.	6.5	23
18	Relationships between efficiency and execution time of full multigrid methods on parallel computers. IEEE Transactions on Parallel and Distributed Systems, 1997, 8, 562-573.	4.0	21

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19	Parallel Multigrid for Anisotropic Elliptic Equations. Journal of Parallel and Distributed Computing, 2001, 61, 96-114.	2.7	21
20	GPUâ€based acceleration of bioâ€inspired motion estimation model. Concurrency Computation Practice and Experience, 2013, 25, 1037-1056.	1.4	19
21	A method for area estimation of data-path in high level synthesis. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 1996, 15, 258-265.	1.9	18
22	Data locality exploitation in the decomposition of regular domain problems. IEEE Transactions on Parallel and Distributed Systems, 2000, 11, 1141-1150.	4.0	17
23	eHR software, multinational corporations and emerging China: Exploring the role of information through a postcolonial lens. Information and Organization, 2012, 22, 106-124.	3.1	17
24	Multi-GPU based on multicriteria optimization for motion estimation system. Eurasip Journal on Advances in Signal Processing, 2013, 2013, .	1.0	15
25	Some aspects about the scalability of scientific applications on parallel architectures. Parallel Computing, 1996, 22, 1169-1195.	1.3	12
26	Reducing Writes in Phase-Change Memory Environments by Using Efficient Cache Replacement Policies. , 2013, , .		12
27	Improving superword level parallelism support in modern compilers. , 2005, , .		11
28	A parallel compact genetic algorithm for multi-FPGA partitioning. , 0, , .		10
29	Hybrid parallelization of a compact genetic algorithm. , 2003, , .		10
30	Low complexity bit-parallel polynomial basis multipliers over binary fields for special irreducible pentanomials. The Integration VLSI Journal, 2013, 46, 197-210.	1.3	10
31	The Question of Movement in Dwelling. Space and Culture, 2009, 12, 371-382.	0.6	9
32	Robust motion estimation on a low-power multi-core DSP. Eurasip Journal on Advances in Signal Processing, 2013, 2013, .	1.0	9
33	Disasters as Meshworks: Migratory Birds and the Enlivening of DoÃ±ana's Toxic Spill. Sociological Review, 2014, 62, 38-60.	0.9	9
34	Stack filter: Reducing L1 data cache power consumption. Journal of Systems Architecture, 2010, 56, 685-695.	2.5	8
35	Implementation of Hybrid Context Based Value Predictors Using Value Sequence Classification. Lecture Notes in Computer Science, 1999, , 1291-1295.	1.0	8
36	Load-store queue management: an energy-efficient design based on a state-filtering mechanism. , 0, , .		7

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37	DMDC: Delayed Memory Dependence Checking through Age-Based Filtering. Microarchitecture (MICRO), Proceedings of the Annual International Symposium on, 2006, , .	0.0	7
38	Offset Printing Plate Quality Sensor on a Low-Cost Processor. Sensors, 2013, 13, 14277-14300.	2.1	7
39	FIDIAS: an integral approach to high-level synthesis. IET Circuits, Devices and Systems, 1995, 142, 227.	0.6	6
40	Wavelet Transform for Large Scale Image Processing on Modern Microprocessors. Lecture Notes in Computer Science, 2003, , 549-562.	1.0	6
41	A parallel multigrid solver for viscous flows on anisotropic structured grids. Parallel Computing, 2003, 29, 907-923.	1.3	5
42	SENT: semantic features in text. Nucleic Acids Research, 2009, 37, W153-W159.	6.5	5
43	Building efficient multi-threaded search nodes. , 2010, , .		5
44	Write-Aware Replacement Policies for PCM-Based Systems. Computer Journal, 2015, 58, 2000-2025.	1.5	5
45	Energy Efficiency Optimization of Task-Parallel Codes on Asymmetric Architectures. , 2017, , .		5
46	A Power-Efficient and Scalable Load-Store Queue Design. Lecture Notes in Computer Science, 2005, , 1-9.	1.0	5
47	A Load-Store Queue Design Based on Predictive State Filtering. Journal of Low Power Electronics, 2006, 2, 27-36.	0.6	5
48	Real-Time Onboard Hyperspectral Image Processing Using Programmable Graphics Hardware. Chapman & Hall/CRC Computer and Information Science Series, 2007, , 411-451.	0.4	5
49	Improving Search Engines Performance on Multithreading Processors. Lecture Notes in Computer Science, 2008, , 201-213.	1.0	5
50	A hardware allocator guided by cost functions. Microprocessing and Microprogramming, 1991, 32, 185-192.	0.3	4
51	Applying speculation techniques to implement functional units. , 2008, , .		4
52	Fault Tolerance on NoCs. , 2013, , .		4
53	Impact of PE Mapping on Cray T3E Message-Passing Performance. Lecture Notes in Computer Science, 2000, , 199-207.	1.0	4
54	Adaptive Tuning of Reserved Space in an Appel Collector. Lecture Notes in Computer Science, 2004, , 542-558.	1.0	4

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55	Partitioning Regular Domains on Modern Parallel Computers. Lecture Notes in Computer Science, 1999, , 411-424.	1.0	4
56	Data path structures and heuristics for testable allocation in high level synthesis. Microprocessing and Microprogramming, 1993, 39, 263-266.	0.3	3
57	Unified data path allocation and BIST intrusion. The Integration VLSI Journal, 1999, 28, 55-99.	1.3	3
58	REDUCING CACHE HIERARCHY ENERGY CONSUMPTION BY PREDICTING FORWARDING AND DISABLING ASSOCIATIVE SETS. Journal of Circuits, Systems and Computers, 2012, 21, 1250057.	1.0	3
59	Leveraging knowledge-as-a-service (KaaS) for QoS-aware resource management in multi-user video transcoding. Journal of Supercomputing, 2020, 76, 9388-9403.	2.4	3
60	Energy Characterization of Garbage Collectors for Dynamic Applications on Embedded Systems. Lecture Notes in Computer Science, 2005, , 69-78.	1.0	3
61	Design control in a high level synthesis system. Microprocessing and Microprogramming, 1992, 34, 93-96.	0.3	2
62	Power-efficient value speculation for high-performance microprocessors. , 0, , .		2
63	Dynamic management of nursery space organization in generational collection. , 0, , .		2
64	Using age registers for a simple loadâ€“store queue filtering. Journal of Systems Architecture, 2009, 55, 79-89.	2.5	2
65	Endmember extraction from hyperspectral imagery using a parallel ensemble approach with consensus analysis. , 2009, , .		2
66	Stack oriented data cache filtering. , 2009, , .		2
67	Not Just Software. International Journal of Technoethics, 2010, 1, 27-36.	0.6	2
68	The WIRMI System: A Multimicrocomputer-Based System for Hotel Automation. IEEE Transactions on Industrial Electronics, 1984, IE-31, 317-321.	5.2	1
69	Microprocessor instruction for undergraduate students. Education and Computing, 1988, 4, 265-272.	0.3	1
70	A multigrid solver for the incompressible Navier-Stokes equations on a Beowulf-class system. , 2001, , .		1
71	LSQ: a power efficient and scalable implementation. IEE Proceedings: Computers and Digital Techniques, 2006, 153, 389.	1.6	1
72	Replacing Associative Load Queues: A Timing-Centric Approach. IEEE Transactions on Computers, 2009, 58, 496-511.	2.4	1

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73	Hybrid timing-address oriented load-store queue filtering for an x86 architecture. IET Computers and Digital Techniques, 2011, 5, 145.	0.9	1
74	Non-negative matrix factorization on low-power architectures. , 2013, , .		1
75	Non-negative Matrix Factorization on Low-Power Architectures and Accelerators: A Comparative Study. Computers and Electrical Engineering, 2015, 46, 139-156.	3.0	1
76	A general purpose computer emulator. Euromicro Newsletter, 1978, 4, 133-140.	0.1	0
77	An architectural design for simultaneous microdiagnostic. Microprocessing and Microprogramming, 1982, 9, 27-37.	0.3	0
78	RDBAS: A relational database system for non-experienced users. Microprocessing and Microprogramming, 1983, 12, 99-109.	0.3	0
79	Guidance for optimization-based synthesis tools. Microprocessing and Microprogramming, 1993, 37, 95-98.	0.3	0
80	An environment to develop parallel code for solving partial differential equations based-problems. Journal of Systems Architecture, 1999, 45, 543-554.	2.5	0
81	Analysing value substitution and confidence estimation for value prediction. Journal of Systems Architecture, 2001, 47, 459-475.	2.5	0
82	Garbage collector re .nement for new dynamic multimedia applications on embedded systems. , 0, , .		0
83	Energy reduction of the fetch mechanism through dynamic adaptation. IET Computers and Digital Techniques, 2008, 2, 94.	0.9	0
84	IMPROVING peLIFO CACHE REPLACEMENT POLICY: HARDWARE REDUCTION AND THREAD-AWARE EXTENSION. Journal of Circuits, Systems and Computers, 2014, 23, 1450046.	1.0	0
85	CEPRAM: Compression for Endurance in PCM RAM. Journal of Circuits, Systems and Computers, 2017, 26, 1750167.	1.0	0
86	Value Prediction as a Cost-Effective Solution to Improve Embedded Processors Performance. Lecture Notes in Computer Science, 2001, , 181-195.	1.0	0
87	On-Line Multi-Threaded Processing of Web User-Clicks on Multi-Core Processors. Lecture Notes in Computer Science, 2011, , 222-235.	1.0	0