Daehoon Kim

List of Publications by Year
in descending order

7	18	2682572	2272923
7 papers	**18** citations	**2** h-index	**4** g-index
7 all docs	**7** docs citations	**7** times ranked	**7** citing authors

#	Article	IF	Citations
1	Deep Partitioned Training From Near-Storage Computing to DNN Accelerators. IEEE Computer Architecture Letters, 2021, 20, 70-73.	1.5	3
2	NMAP: Power Management Based on Network Packet Processing Mode Transition for Latency-Critical Workloads. , 2021, , .		5
3	Network Packet Processing Mode-Aware Power Management for Data Center Servers. IEEE Computer Architecture Letters, 2020, 19, 1-4.	1.5	4
4	Defending Against Flush+Reload Attack With DRAM Cache by Bypassing Shared SRAM Cache. IEEE Access, 2020, 8, 179837-179844.	4.2	1
5	Co-Adjusting Voltage/Frequency State and Interrupt Rate for Improving Energy-Efficiency of Latency-Critical Applications. IEEE Access, 2020, 8, 201028-201039.	4.2	1
6	IDIO: Orchestrating Inbound Network Data on Server Processors. IEEE Computer Architecture Letters, 2020, , 1-1.	1.5	1
7	Exploiting OS-Level Memory Offlining for DRAM Power Management. IEEE Computer Architecture Letters, 2019, 18, 141-144.	1.5	3