

# Daehoon Kim

## List of Publications by Year in descending order

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Version: 2024-02-01

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#	ARTICLE	IF	CITATIONS
1	NMAP: Power Management Based on Network Packet Processing Mode Transition for Latency-Critical Workloads. , 2021, , .		5
2	Network Packet Processing Mode-Aware Power Management for Data Center Servers. IEEE Computer Architecture Letters, 2020, 19, 1-4.	1.5	4
3	Exploiting OS-Level Memory Offlining for DRAM Power Management. IEEE Computer Architecture Letters, 2019, 18, 141-144.	1.5	3
4	Deep Partitioned Training From Near-Storage Computing to DNN Accelerators. IEEE Computer Architecture Letters, 2021, 20, 70-73.	1.5	3
5	Defending Against Flush+Reload Attack With DRAM Cache by Bypassing Shared SRAM Cache. IEEE Access, 2020, 8, 179837-179844.	4.2	1
6	Co-Adjusting Voltage/Frequency State and Interrupt Rate for Improving Energy-Efficiency of Latency-Critical Applications. IEEE Access, 2020, 8, 201028-201039.	4.2	1
7	IDIO: Orchestrating Inbound Network Data on Server Processors. IEEE Computer Architecture Letters, 2020, , 1-1.	1.5	1