

# B Vandana

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/7569137/publications.pdf>

Version: 2024-02-01

10  
papers

24  
citations

2258059

3  
h-index

2053705

5  
g-index

10  
all docs

10  
docs citations

10  
times ranked

20  
citing authors

#	ARTICLE	IF	CITATIONS
1	Memoryless nonlinearity in IT JL FinFET with spacer technology: Investigation towards reliability. <i>Microelectronics Reliability</i> , 2021, 119, 114072.	1.7	5
2	Automatic movable road divider using Arduino UNO with Node Micro Controller Unit (MCU). <i>Materials Today: Proceedings</i> , 2021, , .	1.8	2
3	Mole Fraction Dependency Electrical Performances of Extremely Thin SiGe on Insulator Junctionless Channel Transistor (SG-OI JLCT). <i>Lecture Notes in Electrical Engineering</i> , 2019, , 573-581.	0.4	3
4	Inverted $\hat{\text{T}}^{\text{TM}}$ Junctionless FinFET (ITJL FinFET): Performance Estimation through Device Geometry Variation. <i>ECS Journal of Solid State Science and Technology</i> , 2018, 7, Q52-Q59.	1.8	7
5	Impact of channel engineering (Si1-0.25Ge0.25) technique on GM(transconductance) and its higher order derivatives of 3D conventional and wavy Junctionless FinFETs (JLT). <i>Facta Universitatis - Series Electronics and Energetics</i> , 2018, 31, 257-265.	0.9	4
6	Effectiveness of High Permittivity Spacer for Underlap Regions of Wavy-Junctionless FinFET at 22Ånm Node and Scaling Short Channel Effects. <i>Communications in Computer and Information Science</i> , 2017, , 545-556.	0.5	2
7	Impact on gate oxide material of inverted $\hat{\text{T}}^{\text{TM}}$ Junctionless FinFET at 22 nm technology node. , 2017, , .		0
8	Exploration towards electrostatic integrity for SiGe on insulator (SG-OI) on junctionless channel transistor (JLCT). <i>Facta Universitatis - Series Electronics and Energetics</i> , 2017, 30, 383-390.	0.9	1
9	Memory Efficient Fractal-SPIHT Based Hybrid Image Encoder. <i>Communications in Computer and Information Science</i> , 2017, , 376-387.	0.5	0
10	Prospects of 2D Junctionless Channel Transistor (JLCT) Towards Analog and RF Metrics Using Si and SiGe in Device Layer. <i>Journal of Low Power Electronics</i> , 2017, 13, 536-544.	0.6	0