Iñigo Artundo

List of Publications by Year in descending order

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1684188 1199594 24 191 5 12 citations g-index h-index papers 25 25 25 248 docs citations times ranked citing authors all docs

#	Article	IF	Citations
1	How to Develop your product based on Photonic Integrated Circuits Technologies. , 2021, , .		O
2	[INVITED] Silicon nitride photonic integration for visible light applications. Optics and Laser Technology, 2019, 112, 299-306.	4.6	74
3	Monolithic photonic integration for visible and short near-infrared wavelengths: technologies and platforms for bio and life science applications. Advanced Optical Technologies, 2018, 7, 57-65.	1.7	2
4	PIX4life: photonic integrated circuits for bio-photonics. , 2018, , .		2
5	Integrated bio-photonics to revolutionize health care enabled through PIX4life and PIXAPP. , 2018, , .		4
6	Photonic Integration: New Applications Are Visible. Optik & Photonik, 2017, 12, 22-25.	0.2	16
7	EU pilot lines for integrated photonics. , 2017, , .		3
8	Photonic Mach-Zehnder modulators driven by surface acoustic waves in AlGaAs technology. Proceedings of SPIE, 2014, , .	0.8	1
9	Reconfigurable Networks-on-Chip. Embedded Systems, 2013, , 201-240.	0.6	1
10	Evolution of fabless generic photonic integration. , 2013, , .		8
11	Siliziumphotonik - Grundbausteine kommerzieller Anwendungen. Optik & Photonik, 2013, 8, 52-55.	0.2	1
12	Converged Optical Networks for Multimedia Distribution and Data Services in Hospitality Environments. Communications in Computer and Information Science, 2012, , 133-144.	0.5	0
13	On the Conditions That Justify Requiring Dynamic Reconfigurability in WDM–TDMA Optical Access Networks. Journal of Optical Communications and Networking, 2011, 3, 259.	4.8	1
14	Cost forecasting of passive components for optical fiber network deployments. Optical Fiber Technology, 2011, 17, 218-226.	2.7	6
15	Cycle-accurate evaluation of reconfigurable photonic networks-on-chip. Proceedings of SPIE, 2010, , .	0.8	1
16	Architectural study of reconfigurable photonic Networks-on-Chip for multi-core processors. , 2009, ,		4
17	Low-Power Reconfigurable Network Architecture for On-Chip Photonic Interconnects. , 2009, , .		15
18	Predicting the performance of reconfigurable optical interconnects in distributed shared-memory systems. Photonic Network Communications, 2008, 15, 25-40.	2.7	9

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#	Article	IF	CITATIONS
19	Design of a reconfigurable optical interconnect for large-scale multiprocessor networks. Proceedings of SPIE, 2008, , .	0.8	2
20	Predicting reconfigurable interconnect performance in distributed shared-memory systems. The Integration VLSI Journal, 2007, 40, 382-393.	2.1	5
21	Selective optical broadcast component for reconfigurable multiprocessor interconnects. IEEE Journal of Selected Topics in Quantum Electronics, 2006, 12, 828-837.	2.9	26
22	Speeding up multiprocessor machines with reconfigurable optical interconnects. , 2006, , .		3
23	Selective optical broadcasting in reconfigurable multiprocessor interconnects. , 2006, 6185, 145.		3
24	Wavelength tunable reconfigurable optical interconnection network for shared-memory machines. , 2005, , .		3