

# Andrea Domenici

## List of Publications by Year in descending order

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41  
papers

403  
citations

1040056

9  
h-index

839539

18  
g-index

41  
all docs

41  
docs citations

41  
times ranked

257  
citing authors

#	ARTICLE	IF	CITATIONS
1	Design and Safety Verification of a Distributed Charge Equalizer for Modular Li-Ion Batteries. IEEE Transactions on Industrial Informatics, 2014, 10, 1003-1011.	11.3	47
2	SRAM-Based FPGA Systems for Safety-Critical Applications: A Survey on Design Standards and Proposed Methodologies. Journal of Computer Science and Technology, 2015, 30, 373-390.	1.5	39
3	Replica Management in the European DataGrid Project. Journal of Grid Computing, 2004, 2, 341-351.	3.9	37
4	Replica consistency in a Data Grid. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2004, 534, 24-28.	1.6	36
5	A PVS-Simulink Integrated Environment for Model-Based Analysis of Cyber-Physical Systems. IEEE Transactions on Software Engineering, 2018, 44, 512-533.	5.6	32
6	Formal Verification and Co-Simulation in the Design of a Synchronous Motor Control Algorithm. Energies, 2020, 13, 4057.	3.1	27
7	Accurate simulation of SEUs in the configuration memory of SRAM-based FPGAs. , 2012, , .		21
8	ASSESS: A Simulator of Soft Errors in the Configuration Memory of SRAM-Based FPGAs. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2014, 33, 1342-1355.	2.7	17
9	Relaxed data consistency with CONStanza. , 2006, , .		16
10	GABES: A genetic algorithm based environment for SEU testing in SRAM-FPGAs. Journal of Systems Architecture, 2013, 59, 1243-1254.	4.3	13
11	Verifying safety properties of a nonlinear control by interactive theorem proving with the Prototype Verification System. Information Processing Letters, 2016, 116, 409-415.	0.6	13
12	Failure probability of SRAM-FPGA systems with Stochastic Activity Networks. , 2011, , .		10
13	SEU-X: A SEu un-excitability prover for SRAM-FPGAs. , 2012, , .		9
14	Unexcitability analysis of SEUs affecting the routing structure of SRAM-based FPGAs. , 2013, , .		9
15	Integrated Simulation and Formal Verification of a Simple Autonomous Vehicle. Lecture Notes in Computer Science, 2018, , 300-314.	1.3	9
16	Extending a User Interface Prototyping Tool with Automatic MISRA C Code Generation. Electronic Proceedings in Theoretical Computer Science, EPTCS, 0, 240, 53-66.	0.8	9
17	Co-simulation and Verification of a Non-linear Control System for Cogging Torque Reduction in Brushless Motors. Lecture Notes in Computer Science, 2020, , 3-19.	1.3	8
18	A performance study on the synchronisation of heterogeneous Grid databases using CONStanza. Future Generation Computer Systems, 2010, 26, 820-834.	7.5	5

#	ARTICLE	IF	CITATIONS
19	Failure Probability and Fault Observability of SRAM-FPGA Systems. , 2011, , .		5
20	UA2TPG: An untestability analyzer and test pattern generator for SEUs in the configuration memory of SRAM-based FPGAs. The Integration VLSI Journal, 2016, 55, 85-97.	2.1	5
21	Formalization and co-simulation of attacks on cyber-physical systems. Journal of Computer Virology and Hacking Techniques, 2020, 16, 63-77.	2.2	5
22	Static and Dynamic Data Models for the Storage Resource Manager v2.2. Journal of Grid Computing, 2009, 7, 115-133.	3.9	4
23	High availability using virtualization. Journal of Physics: Conference Series, 2010, 219, 052017.	0.4	4
24	HRT-UML: Taking HRT-HOOD onto UML. Lecture Notes in Computer Science, 2003, , 405-416.	1.3	4
25	Modeling communication network requirements for an integrated clinical environment in the Prototype Verification System. , 2016, , .		3
26	Co-simulation and Formal Verification of Co-operative Drone Control With Logic-Based Specifications. Computer Journal, 2023, 66, 295-317.	2.4	3
27	OLT(RE)2: An On-Line On-Demand Testing Approach for Permanent Radiation Effects in Reconfigurable Systems. IEEE Transactions on Emerging Topics in Computing, 2018, 6, 511-523.	4.6	2
28	Cross-level Co-simulation and Verification of an Automatic Transmission Control on Embedded Processor. Lecture Notes in Computer Science, 2021, , 263-279.	1.3	2
29	A Logic Theory Pattern for Linearized Control Systems. Electronic Proceedings in Theoretical Computer Science, EPTCS, 0, 338, 46-52.	0.8	2
30	Integrated Simulation of Implantable Cardiac Pacemaker Software and Heart Models. , 2014, , .		2
31	A protocol for resource locking and deadlock detection in a multi-user environment. Microprocessing and Microprogramming, 1989, 27, 431-437.	0.2	1
32	Petri nets in logic. Microprocessing and Microprogramming, 1990, 30, 193-198.	0.2	1
33	Formal approaches to SEU testing in FPGAs. , 2013, , .		1
34	Mitigation of Single Event Upsets in the control logic of a charge equalizer for Li-ion batteries. , 2013, , .		1
35	Modeling and Simulation of Attacks on Cyber-physical Systems. , 2019, , .		1
36	Introduction to Prolog computation model and its implementation. Information and Software Technology, 1990, 32, 423-431.	4.4	0

#	ARTICLE	IF	CITATIONS
37	Logic-Based Formalization of System Requirements for Integrated Clinical Environments. Computational Biology, 2019, , 215-242.	0.2	0
38	Identify Potential Attacks from Simulated Log Analysis. , 2020, , .		0
39	Consistency of Replicated Datasets in Grid Computing. , 2009, , 49-58.		0
40	Towards Stochastic FMI Co-Simulations: Implementation of an FMU for a Stochastic Activity Networks Simulator. Lecture Notes in Computer Science, 2018, , 34-44.	1.3	0
41	Application of Model Checking to Fault Tolerance Analysis. Lecture Notes in Computer Science, 2019, , 531-547.	1.3	0