Elena Pastorelli

List of Publications by Year
in descending order

21 papers	144 citations	1478505 6 h-index	1281871 11 g-index
21 all docs	21 docs citations	21 times ranked	172 citing authors

#	Article	IF	Citations
1	Sleep-like slow oscillations improve visual classification through synaptic homeostasis and memory association in a thalamo-cortical model. Scientific Reports, 2019, 9, 8990.	3.3	28
2	Next generation of Exascale-class systems: ExaNeSt project and the status of its interconnect and storage development. Microprocessors and Microsystems, 2018, 61, 58-71.	2.8	17
3	NaNet: a configurable NIC bridging the gap between HPC and real-time HEP GPU computing. Journal of Instrumentation, 2015, 10, C04011-C04011.	1.2	14
4	NaNet-10: a 10GbE network interface card for the GPU-based low-level trigger of the NA62 RICH detector.. Journal of Instrumentation, 2016, 11, C03030-C03030.	1.2	12
5	mAgic-FPU and MADE: A customizable VLIW core and the modular VLIW processor architecture description environment. Computer Physics Communications, 2001, 139, 132-143.	7.5	10
6	Dynamic many-process applications on many-tile embedded systems and HPC clusters: The EURETILE programming environment and execution platforms. Journal of Systems Architecture, 2016, 69, 29-53.	4.3	10
7	The Next Generation of Exascale-Class Systems: The ExaNeSt Project. , 2017, , .		8
8	Thalamo-cortical spiking model of incremental learning combining perception, context and NREM-sleep. PLoS Computational Biology, 2021, 17, e1009045.	3.2	8
9	Scaling of a Large-Scale Simulation of Synchronous Slow-Wave and Asynchronous Awake-Like Activity of a Cortical Model With Long-Range Interconnections. Frontiers in Systems Neuroscience, 2019, 13, 33.	2.5	7
10	Fast Simulation of a Multi-Area Spiking Network Model of Macaque Cortex on an MPI-GPU Cluster. Frontiers in Neuroinformatics, 0, 16, .	2.5	7
11	Power-Efficient Computing: Experiences from the COSA Project. Scientific Programming, 2017, 2017, 1-14.	0.7	6
12	Gaussian and Exponential Lateral Connectivity on Distributed Spiking Neural Network Simulation. , 2018, , .		6
13	Architectural improvements and technological enhancements for the APEnet+ interconnect system. Journal of Instrumentation, 2015, 10, C02005-C02005.	1.2	5
14	A multi-port 10GbE PCIe NIC featuring UDP offload and GPUDirect capabilities.. Journal of Physics: Conference Series, 2015, 664, 092002.	0.4	3
15	Real-time track-less Cherenkov ring fitting trigger system based on Graphics Processing Units. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2017, 876, 115-118.	1.6	1
16	Development of Network Interface Cards for TRIDAQ systems with the NaNet framework. Journal of Instrumentation, 2017, 12, C03037-C03037.	1.2	1
17	Real-Time Cortical Simulations: Energy and Interconnect Scaling on Distributed Systems. , 2019, , .		1
18	Graphics Processors in HEP Low-Level Trigger Systems. EPJ Web of Conferences, 2016, 127, 00011.	0.3	0

#	Article	IF	Citations
19	GPU-based low-level trigger system for the standalone reconstruction of the ring-shaped hit patterns in the RICH Cherenkov detector of NA62 experiment. Journal of Instrumentation, 2017, 12, C03005-C03005.	1.2	0
20	Graphical processors for HEP trigger systems. Nuclear Instruments and Methods in Physics Research, Section A: Accelerators, Spectrometers, Detectors and Associated Equipment, 2017, 845, 612-615.	1.6	0
21	Latest generation interconnect technologies in APEnet+ networking infrastructure. Journal of Physics: Conference Series, 2017, 898, 082035.	0.4	0