

Omeed Momeni

List of Publications by Year in descending order

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766
citing authors

#	ARTICLE	IF	CITATIONS
1	High Power Terahertz and Millimeter-Wave Oscillator Design: A Systematic Approach. IEEE Journal of Solid-State Circuits, 2011, 46, 583-597.	3.5	322
2	A Novel CMOS High-Power Terahertz VCO Based on Coupled Oscillators: Theory and Implementation. IEEE Journal of Solid-State Circuits, 2012, 47, 3032-3042.	3.5	157
3	A High-Gain mm-Wave Amplifier Design: An Analytical Approach to Power Gain Boosting. IEEE Journal of Solid-State Circuits, 2017, 52, 357-370.	3.5	76
4	A Broadband mm-Wave and Terahertz Traveling-Wave Frequency Multiplier on CMOS. IEEE Journal of Solid-State Circuits, 2011, 46, 2966-2976.	3.5	69
5	A Silicon-Based 0.3 THz Frequency Synthesizer With Wide Locking Range. IEEE Journal of Solid-State Circuits, 2014, 49, 2951-2963.	3.5	66
6	High-Power and High-Efficiency Millimeter-Wave Harmonic Oscillator Design, Exploiting Harmonic Positive Feedback in CMOS. IEEE Transactions on Microwave Theory and Techniques, 2017, 65, 3922-3936.	2.9	56
7	A 200-GHz Inductively Tuned VCO With -7 -dBm Output Power in 130-nm SiGe BiCMOS. IEEE Transactions on Microwave Theory and Techniques, 2013, 61, 3666-3673.	2.9	54
8	A 10-Gb/s Inductorless Transimpedance Amplifier. IEEE Transactions on Circuits and Systems II: Express Briefs, 2010, 57, 926-930.	2.2	52
9	A 0.46-THz 25-Element Scalable and Wideband Radiator Array With Optimized Lens Integration in 65-nm CMOS. IEEE Journal of Solid-State Circuits, 2020, 55, 2387-2400.	3.5	44
10	A 190-GHz VCO With 20.7% Tuning Range Employing an Active Mode Switching Block in a 130 nm SiGe BiCMOS. IEEE Journal of Solid-State Circuits, 2017, 52, 2094-2104.	3.5	41
11	A 0.34-THz Wideband Wide-Angle 2-D Steering Phased Array in 0.13- μ m SiGe BiCMOS. IEEE Journal of Solid-State Circuits, 2019, 54, 2449-2461.	3.5	41
12	Second-Harmonic Power Generation Limits in Harmonic Oscillators. IEEE Journal of Solid-State Circuits, 2018, 53, 3217-3231.	3.5	31
13	Low-Power and Low-Noise Millimeter-Wave SSPLL With Subsampling Lock Detector for Automatic Dividerless Frequency Acquisition. IEEE Transactions on Microwave Theory and Techniques, 2021, 69, 469-481.	2.9	26
14	A Standing-Wave Architecture for Scalable and Wideband Millimeter-Wave and Terahertz Coherent Radiator Arrays. IEEE Transactions on Microwave Theory and Techniques, 2018, 66, 1597-1609.	2.9	25
15	An mm-Wave Scalable PLL-Coupled Array for Phased-Array Applications in 65-nm CMOS. IEEE Transactions on Microwave Theory and Techniques, 2021, 69, 1439-1452.	2.9	19
16	Electrical Prism: A High Quality Factor Filter for Millimeter-Wave and Terahertz Frequencies. IEEE Transactions on Microwave Theory and Techniques, 2009, 57, 2790-2799.	2.9	17
17	A 9.6 mW Low-Noise Millimeter-Wave Sub-Sampling PLL with a Divider-less Sub-Sampling Lock Detector in 65 nm CMOS. , 2019, , .		12
18	A 200-GHz Power Amplifier With a Wideband Balanced Slot Power Combiner and 9.4-dBm P_{sat} in 65-nm CMOS: Embedded Power Amplification. IEEE Journal of Solid-State Circuits, 2021, 56, 3318-3330.	3.5	12

#	ARTICLE	IF	CITATIONS
19	A Nonlinear Lattice for High-Amplitude Picosecond Pulse Generation in CMOS. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 370-380.	2.9	11
20	A CMOS V-Band PLL With a Harmonic Positive Feedback VCO Leveraging Operation in Triode Region for Phase-Noise Improvement. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 1818-1830.	3.5	11
21	A Charge Pump Current Mismatch Compensation Design for Sub-Sampling PLL. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 1852-1856.	2.2	11
22	Gain Boosting in Distributed Amplifiers for Close-to- ω_{max} Operation in Silicon. IEEE Transactions on Microwave Theory and Techniques, 2019, 67, 1039-1049.	2.9	10
23	Electrical Prism: a high quality factor filter for mm wave and terahertz frequencies. , 2008, , .		2
24	A 219-to-238-GHz Coupled Standing-Wave VCO with 3.4-dBm Peak Output Power in 65nm CMOS. , 2019, , .		1