

Yiming Yu

List of Publications by Year in Descending Order

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

81
papers

660
citations

14
h-index

22
g-index

94
ext. papers

922
ext. citations

2.6
avg, IF

4.15
L-index

#	Paper	IF	Citations
81	A Ka-Band CMOS Phase-Invariant and Ultralow Gain Error Variable Gain Amplifier With Active Cross-Coupling Neutralization and Asymmetric Capacitor Techniques. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2022 , 70, 85-100	4.1	0
80	A Wide-Band Divide-By-2 Injection-Locked Frequency Divider Based on Distributed Dual-Resonance Tank. <i>Electronics (Switzerland)</i> , 2022 , 11, 506	2.6	
79	A 3-GHz Inverse-Coupled Current-Reuse VCO Implemented by 1:1 Transformer. <i>IEEE Microwave and Wireless Components Letters</i> , 2022 , 1-3	2.6	
78	A Ku-Band Eight-Element Phased-Array Transmitter With Built-in Self-Test Capability in 180-nm CMOS Technology. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2022 , 1-12	2.6	
77	Hexahedron-Based Control Volume Finite Element Method for Fully Coupled Nonlinear Drift-Diffusion Transport Equations in Semiconductor Devices. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2022 , 1-1	4.1	1
76	Temperature-Dependent Threshold Voltage Extraction of FinFETs Using Noise Measurements. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2022 , 1-1	4.1	
75	A 33-1-GHz SiGe-BiCMOS Digital Step Attenuator With Minimized Unit Impedance Variation. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2021 , 29, 568-579	2.6	1
74	A SiGe Power Amplifier With Double Gain Peaks Based on the Control of Stationary Points of Impedance Transformation. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2021 , 69, 2279-2290	4.1	1
73	A 60-GHz Variable Gain Phase Shifter With 14.8-dB Gain Tuning Range and 6-Bit Phase Resolution Across 0.5 °C-10 °C. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2021 , 69, 2371-2385	4.1	8
72	An Improved Large-Signal Equivalent Circuit Model for Partially Depleted Silicon-on-Insulator MOSFET. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2021 , 69, 2972-2980	4.1	1
71	A 21-to-41-GHz High-Gain Low Noise Amplifier With Triple-Coupled Technique for Multiband Wireless Applications. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2021 , 68, 1857-1861	3.5	6
70	A 44 To 64 GHz Broadband 90° Hybrid Doherty PA With Quasi Non-Foster Tuner in 0.13 μm SiGe. <i>IEEE Microwave and Wireless Components Letters</i> , 2021 , 31, 760-763	2.6	3
69	Dual-beam and dual-mode circularly polarized antenna based on substrate integrated waveguide technology. <i>Microwave and Optical Technology Letters</i> , 2021 , 63, 2882-2887	1.2	
68	A Wideband CMOS Frequency Quadrupler With Transformer-Based Tail Feedback Loop. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2021 , 68, 1153-1157	3.5	2
67	An improved 220-GHz RF CMOS compact equivalent circuit model considering magnetic coupling effect. <i>Microwave and Optical Technology Letters</i> , 2021 , 63, 1048-1053	1.2	1
66	A 8.5 to 11.6 GHz digitally controlled variable gain amplifier with 15-dB gain range and 1-dB step. <i>Microwave and Optical Technology Letters</i> , 2021 , 63, 411-416	1.2	1
65	A 21.5 ~ 25 GHz 7-bit phase shifter with passive vector-sum topology. <i>Microwave and Optical Technology Letters</i> , 2021 , 63, 1652-1656	1.2	0

64	. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2021 , 69, 3989-4000	4.1	3
63	. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2021 , 69, 756-773	4.1	7
62	Analysis and Design of High-Harmonic-Rejection Multi-Ratio mm-Wave Frequency Multipliers. <i>IEEE Journal of Solid-State Circuits</i> , 2021 , 1-1	5.5	2
61	An Improved Surface-Potential-Based Model for MOSFETs Considering the Carrier Gaussian Distribution. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2020 , 68, 4082-4090	4.1	1
60	Analysis and design of transformer-based CMOS ultra-wideband millimeter-wave circuits for wireless applications: a review. <i>Frontiers of Information Technology and Electronic Engineering</i> , 2020 , 21, 97-115	2.2	3
59	A K-Band Frequency Tripler Using Transformer-Based Self-Mixing Topology With Peaking Inductor. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2020 , 68, 1688-1696	4.1	5
58	A 3740-GHz Low-Phase-Imbalance CMOS Attenuator With Tail-Capacitor Compensation Technique. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2020 , 67, 3400-3409	3.9	6
57	. <i>IEEE Access</i> , 2020 , 8, 29311-29318	3.5	6
56	A Ka-Band CMOS Variable Gain Amplifier with High Gain Resolution and Low Phase Variation 2020 ,		2
55	A Harmonic-Tuned VCO With an Intrinsic-High-Q F23 Inductor in 65-nm CMOS. <i>IEEE Microwave and Wireless Components Letters</i> , 2020 , 30, 981-984	2.6	0
54	A 68.5~90 GHz High-Gain Power Amplifier With Capacitive Stability Enhancement Technique in 0.13 μm SiGe BiCMOS. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2020 , 1-1	4.1	2
53	A 27.5-43.5 GHz 65-nm CMOS up-conversion mixer with 0.42 dBm OP1dB for 5G applications. <i>International Journal of Numerical Modelling: Electronic Networks, Devices and Fields</i> , 2020 , 33, e2550	1	
52	A 10-mW 3.9-dB NF transformer-based V-band low-noise amplifier in 65-nm CMOS. <i>International Journal of Numerical Modelling: Electronic Networks, Devices and Fields</i> , 2020 , 33, e2576	1	2
51	An improved open-short equivalent circuit model for CMOS transistors de-embedding. <i>International Journal of Numerical Modelling: Electronic Networks, Devices and Fields</i> , 2020 , 33, e2589	1	2
50	Millimeter wave balun design and optimization based on compensation matching capacitors and active S parameter. <i>International Journal of Numerical Modelling: Electronic Networks, Devices and Fields</i> , 2020 , 33, e2644	1	1
49	An improved wideband equivalent circuit model for integrated spiral inductors in CMOS technology. <i>International Journal of Numerical Modelling: Electronic Networks, Devices and Fields</i> , 2020 , 33, e2640	1	2
48	A millimeter-wave scalable small signal model of RF CMOS transistor against number of fingers. <i>International Journal of Numerical Modelling: Electronic Networks, Devices and Fields</i> , 2020 , 33, e2608	1	1
47	A 51.5 - 64.5 GHz Active Phase Shifter Using Linear Phase Control Technique With 1.4 π Phase resolution in 65-nm CMOS 2019 ,		4

46	An Injection-Current-Boosting Locking-Range Enhancement Technique for Ultra-Wideband mm-Wave Injection-Locked Frequency Triplers. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2019 , 67, 3174-3186	4.1	21
45	A 6285-GHz High Linearity Upconversion Mixer With 18-GHz IF Bandwidth. <i>IEEE Microwave and Wireless Components Letters</i> , 2019 , 29, 219-221	2.6	4
44	Fully Coupled Electrothermal Simulation of Large RRAM Arrays in the Thermal-House. <i>IEEE Access</i> , 2019 , 7, 3897-3908	3.5	5
43	An Ultralow Phase Noise Eight-Core Fundamental 62-to-67-GHz VCO in 65-nm CMOS. <i>IEEE Microwave and Wireless Components Letters</i> , 2019 , 29, 125-127	2.6	11
42	An Improved Small Signal Equivalent Circuit Modeling Based On 65nm CMOS Technology 2019 ,		1
41	Differential low-loss T/R switch for phase array application in 0.18- μ m CMOS technology. <i>IET Microwaves, Antennas and Propagation</i> , 2019 , 13, 813-818	1.6	1
40	An Improved Surface Potential-Based High-Order Channel Length Modulation Model 2019 ,		3
39	A 15-27 GHz Low Conversion Loss and High Isolation Resistive Ring Mixer for Direct Conversion Receiver 2019 ,		2
38	A High Linearity Low Noise Amplifier for 5G Front-End Modules 2019 ,		1
37	Multi-bias Small Signal Circuit Model for FinFET Transistors 2019 ,		3
36	A 19.5% Efficiency 5173-GHz High-Output Power Frequency Doubler in 65-nm CMOS. <i>IEEE Microwave and Wireless Components Letters</i> , 2019 , 29, 818-821	2.6	8
35	. <i>IEEE Access</i> , 2018 , 6, 10131-10138	3.5	12
34	An Improved Ultrawideband Open-Short De-Embedding Method Applied up to 220 GHz. <i>IEEE Transactions on Components, Packaging and Manufacturing Technology</i> , 2018 , 8, 269-276	1.7	11
33	An Improved RF MOSFET Model Accounting Substrate Coupling Among Terminals. <i>IEEE Microwave and Wireless Components Letters</i> , 2018 , 28, 138-140	2.6	9
32	Analysis and Design of Ultra-Wideband mm-Wave Injection-Locked Frequency Dividers Using Transformer-Based High-Order Resonators. <i>IEEE Journal of Solid-State Circuits</i> , 2018 , 53, 2177-2189	5.5	37
31	A 62-90 GHz High Linearity and Low Noise CMOS Mixer Using Transformer-Coupling Cascode Topology. <i>IEEE Access</i> , 2018 , 6, 19338-19344	3.5	17
30	A 24 GHz enhanced neutralized cascode LNA with 4.7 dB NF and 19.8 dB gain. <i>IEICE Electronics Express</i> , 2018 , 15, 20180464-20180464	0.5	3
29	An Improved Small-Signal Equivalent Circuit Model Considering Channel Current Magnetic Effect. <i>IEEE Microwave and Wireless Components Letters</i> , 2018 , 28, 804-806	2.6	4

28	A 256-QAM 39 GHz Dual-Channel Transceiver Chipset with LTCC Package for 5G Communication in 65 nm CMOS 2018 ,		16
27	66 GHz bias-dependent equivalent circuit model for CMOS transistor based on 90 nanometers CMOS technology. <i>Microwave and Optical Technology Letters</i> , 2018 , 60, 1808-1812	1.2	1
26	A package-level wideband driver amplifier with 134% fractional bandwidth. <i>IEICE Electronics Express</i> , 2018 , 15, 20180179-20180179	0.5	1
25	An Empirical Nonlinear Capacitance Model for SOI Transistor 2018 ,		1
24	RF CMOS Transistor Equivalent Circuit Model up to 66 GHz 2018 ,		3
23	A CMOS Ku-band receiver chain for phased array system. <i>IEICE Electronics Express</i> , 2018 , 15, 20180888-20180888		9
22	A 5-Gb/s 66 dB CMOS Variable-Gain Amplifier With Reconfigurable DC-Offset Cancellation for Multi-Standard Applications. <i>IEEE Access</i> , 2018 , 6, 54139-54146	3.5	4
21	Blind Nonlinear Self-Interference Cancellation for Wireless Full-Duplex Transceivers. <i>IEEE Access</i> , 2018 , 6, 37725-37737	3.5	13
20	Analysis and Design of Inductorless Wideband Low-Noise Amplifier With Noise Cancellation Technique. <i>IEEE Access</i> , 2017 , 5, 9389-9397	3.5	24
19	A 220-GHz Compact Equivalent Circuit Model of CMOS Transistors. <i>IEEE Microwave and Wireless Components Letters</i> , 2017 , 27, 651-653	2.6	9
18	Analysis and Design of CMOS Doherty Power Amplifier Based on Voltage Combining Method. <i>IEEE Access</i> , 2017 , 5, 5001-5012	3.5	13
17	A Wideband Model for On-Chip Interconnects With Different Shielding Structures. <i>IEEE Transactions on Components, Packaging and Manufacturing Technology</i> , 2017 , 7, 1702-1712	1.7	7
16	. <i>IEEE Journal of Solid-State Circuits</i> , 2017 , 52, 2892-2904	5.5	56
15	High-Isolation CMOS T/R Switch Design Using a Two-Stage Equivalent Transmission Line Structure. <i>IEEE Access</i> , 2017 , 5, 22704-22712	3.5	1
14	2017 ,		4
13	Fully Coupled Multiphysics Simulation of Crosstalk Effect in Bipolar Resistive Random Access Memory. <i>IEEE Transactions on Electron Devices</i> , 2017 , 64, 3647-3653	2.9	19
12	CMOS 90 nm multi-bias transistor model Up to 66 GHz 2017 ,		4
11	A CMOS K-Band 6-bit Attenuator With Low Phase Imbalance for Phased Array Applications. <i>IEEE Access</i> , 2017 , 5, 19657-19661	3.5	17

10	A 60-GHz 19.8-mW Current-Reuse Active Phase Shifter With Tunable Current-Splitting Technique in 90-nm CMOS. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2016 , 64, 1572-1584	4.1	34
9	A Broadband and Equivalent-Circuit Model for Millimeter-Wave On-Chip M:N Six-Port Transformers and Baluns. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2015 , 63, 3109-3121	4.1	22
8	An Equivalent Circuit Model With Current Return Path Effects for ON-Chip Interconnect up to 80 GHz. <i>IEEE Transactions on Components, Packaging and Manufacturing Technology</i> , 2015 , 5, 1320-1330	1.7	5
7	A 60-GHz vector summing phase shifter with digital tunable current-splitting and current-reuse techniques in 90 nm CMOS 2015 ,		1
6	Analysis and Equivalent-Circuit Model for CMOS On-Chip Multiple Coupled Inductors in the Millimeter-Wave Region. <i>IEEE Transactions on Electron Devices</i> , 2015 , 62, 3957-3964	2.9	29
5	A Q-band CMOS LNA exploiting transformer feedback and noise-cancelling. <i>Science China Information Sciences</i> , 2015 , 58, 1-10	3.4	0
4	Characterization and Modeling of Multiple Coupled Inductors Based on On-Chip Four-Port Measurement. <i>IEEE Transactions on Components, Packaging and Manufacturing Technology</i> , 2014 , 4, 1696-1704	1.7	4
3	A 60-GHz OOK Receiver With an On-Chip Antenna in 90 nm CMOS. <i>IEEE Journal of Solid-State Circuits</i> , 2010 , 45, 1720-1731	5.5	87
2	A New Six-Port Transformer Modeling Methodology Applied to 10-dBm 60-GHz CMOS ASK Modulator Designs. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2010 , 58, 297-309	4.1	18
1	Scalable Transmission Line and Inductor Models for CMOS Millimeter-Wave Design. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2008 , 56, 2954-2962	4.1	37