

Wei Chun Chin

List of Publications by Year in descending order

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3311381

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3475538

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#	ARTICLE	IF	CITATIONS
1	Voltage-in-Current Latency Insertion Method for Diodes and MOSFETs. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2020, 10, 1708-1720.	2.5	4
2	Thin-Film Transistor Simulations With the Voltage-In-Current Latency Insertion Method. IEEE Access, 2021, 9, 159334-159348.	4.2	0