

# Abm Harun-Ur Rashid

## List of Publications by Year in descending order

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15  
papers

44  
citations

2682572

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2272923

4  
g-index

15  
all docs

15  
docs citations

15  
times ranked

37  
citing authors

#	ARTICLE	IF	CITATIONS
1	Design of a linearly increasing inrush current limit circuit for DC-DC boost regulators. , 2010, , .		10
2	Low power high speed Ternary Content Addressable Memory design using 8 MOSFETs and 4 memristors - hybrid structure. , 2014, , .		7
3	Low power high speed ternary content addressable memory design using MOSFET and memristors. , 2014, , .		6
4	Graphene Nanoribbon Quantum-Well Interband and Intersubband Photodetector. IEEE Transactions on Electron Devices, 2015, 62, 4082-4090.	3.0	6
5	Memristorâ€transistor hybrid ternary content addressable memory using ternary memristive memory cell. IET Circuits, Devices and Systems, 2021, 15, 619-629.	1.4	4
6	Design of a 3.3V 4.1 GHz Narrowband CMOS differential low noise amplifier. , 2015, , .		3
7	Associative memory algorithm for visual pattern recognition with memristor array and CMOS neuron. , 2016, , .		3
8	Memristor-CMOS Hybrid Implementation of Leaky Integrate and Fire Neuron Model. , 2019, , .		2
9	Design and implementation of a 0.8V input, 84% duty cycle, variable frequency step-up converter. Microelectronics Journal, 2011, 42, 648-660.	2.0	1
10	Adaptive beamforming with a Microphone Array. , 2015, , .		1
11	Design of a high performance AC-DC LED driver based on SEPIC topology. International Journal of Power Electronics and Drive Systems, 2021, 12, 870.	0.6	1
12	Fault characterisation of complementary pass-transistor logic circuits. , 0, , .		0
13	Testing complementary pass-transistor logic circuits. , 0, , .		0
14	Numerical study on graphene nanoribbon quantum well-in-well interband and intersubband photodetector. , 2015, , .		0
15	A novel low buffered optimized Solid State Drive controller. , 2016, , .		0