

Keshab K Parhi

List of Publications by Citations

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

208
papers

3,289
citations

29
h-index

49
g-index

234
ext. papers

4,370
ext. citations

3.1
avg, IF

6.14
L-index

#	Paper	IF	Citations
208	Seizure prediction with spectral power of EEG using cost-sensitive support vector machines. <i>Epilepsia</i> , 2011 , 52, 1761-70	6.4	268
207	Iterative Vessel Segmentation of Fundus Images. <i>IEEE Transactions on Biomedical Engineering</i> , 2015 , 62, 1738-49	5	170
206	Early Stopping Criteria for Energy-Efficient Low-Latency Belief-Propagation Polar Code Decoders. <i>IEEE Transactions on Signal Processing</i> , 2014 , 62, 6496-6506	4.8	129
205	Low-Energy Digit-Serial/Parallel Finite Field Multipliers. <i>Journal of Signal Processing Systems</i> , 1998 , 19, 149-166		120
204	Low-Complexity Seizure Prediction From iEEG/sEEG Using Spectral Power and Ratios of Spectral Power. <i>IEEE Transactions on Biomedical Circuits and Systems</i> , 2016 , 10, 693-706	5.1	112
203	Pipelined Parallel FFT Architectures via Folding Transformation. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2012 , 20, 1068-1081	2.6	97
202	A Pipelined FFT Architecture for Real-Valued Signals. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2009 , 56, 2634-2643	3.9	80
201	Low-Latency Successive-Cancellation Polar Decoder Architectures Using 2-Bit Decoding. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2014 , 61, 1241-1254	3.9	62
200	RETOUCH: The Retinal OCT Fluid Detection and Segmentation Benchmark and Challenge. <i>IEEE Transactions on Medical Imaging</i> , 2019 , 38, 1858-1874	11.7	60
199	Low-Latency Successive-Cancellation List Decoders for Polar Codes With Multibit Decision. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2015 , 23, 2268-2280	2.6	60
198	Architecture optimizations for BP polar decoders 2013 ,		59
197	Obfuscating DSP Circuits via High-Level Transformations. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2015 , 23, 819-830	2.6	52
196	Reduced-latency SC polar decoder architectures 2012 ,		47
195	Computation Error Analysis in Digital Signal Processing Systems With Overscaled Supply Voltage. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2010 , 18, 517-526	2.6	47
194	High-Speed VLSI Implementation of 2-D Discrete Wavelet Transform. <i>IEEE Transactions on Signal Processing</i> , 2008 , 56, 393-403	4.8	47
193	High-Throughput VLSI Architecture for FFT Computation. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2007 , 54, 863-867	3.5	47
192	Network analysis of functional brain connectivity in borderline personality disorder using resting-state fMRI. <i>NeuroImage: Clinical</i> , 2016 , 11, 302-315	5.3	46

191	Architectures for digital filters using stochastic computing 2013 ,		46
190	Low-Complexity Welch Power Spectral Density Computation. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2014 , 61, 172-182	3.9	44
189	Fully Automated Segmentation of Fluid/Cyst Regions in Optical Coherence Tomography Images With Diabetic Macular Edema Using Neutrosophic Sets and Graph Algorithms. <i>IEEE Transactions on Biomedical Engineering</i> , 2018 , 65, 989-1001	5	42
188	Discrete-time signal processing with DNA. <i>ACS Synthetic Biology</i> , 2013 , 2, 245-54	5.7	41
187	Min-Sum Decoder Architectures With Reduced Word Length for LDPC Codes. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2010 , 57, 105-115	3.9	40
186	High-Speed Parallel Architectures for Linear Feedback Shift Registers. <i>IEEE Transactions on Signal Processing</i> , 2011 , 59, 4459-4469	4.8	38
185	Low-Complexity Switch Network for Reconfigurable LDPC Decoders. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2010 , 18, 85-94	2.6	36
184	Low-Area/Power Parallel FIR Digital Filter Implementations. <i>Journal of Signal Processing Systems</i> , 1997 , 17, 75-92		36
183	FFT Architectures for Real-Valued Signals Based on Radix- 2^3 and Radix- 2^4 Algorithms. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2013 , 60, 2422-2430	3.9	34
182	An In-Place FFT Architecture for Real-Valued Signals. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2013 , 60, 652-656	3.5	33
181	Secure and Reliable XOR Arbiter PUF Design 2017 ,		32
180	. <i>IEEE Transactions on Signal Processing</i> , 2016 , 64, 3705-3718	4.8	31
179	. <i>IEEE Transactions on Emerging Topics in Computing</i> , 2019 , 7, 44-59	4.1	29
178	Function-specific and Enhanced Brain Structural Connectivity Mapping via Joint Modeling of Diffusion and Functional MRI. <i>Scientific Reports</i> , 2018 , 8, 4741	4.9	28
177	Pipelined Architectures for Real-Valued FFT and Hermitian-Symmetric IFFT With Real Datapaths. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2013 , 60, 507-511	3.5	28
176	Successive cancellation decoding of polar codes using stochastic computing 2015 ,		27
175	2014 ,		27
174	Chemical Reaction Networks for Computing Polynomials. <i>ACS Synthetic Biology</i> , 2017 , 6, 76-83	5.7	27

173	Reliable PUF-Based Local Authentication With Self-Correction. <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , 2017 , 36, 201-213	2.5	25
172	Latency Analysis and Architecture Design of Simplified SC Polar Decoders. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2014 , 61, 115-119	3.5	25
171	2013 ,		25
170	Classification Using Hyperdimensional Computing: A Review. <i>IEEE Circuits and Systems Magazine</i> , 2020 , 20, 30-47	3.2	23
169	Synchronous sequential computation with molecular reactions 2011 ,		23
168	Early Seizure Detection Using Neuronal Potential Similarity: A Generalized Low-Complexity and Robust Measure. <i>International Journal of Neural Systems</i> , 2015 , 25, 1550019	6.2	22
167	Homogeneous and Heterogeneous Feed-Forward XOR Physical Unclonable Functions. <i>IEEE Transactions on Information Forensics and Security</i> , 2020 , 15, 2485-2498	8	22
166	Fully-automated segmentation of fluid regions in exudative age-related macular degeneration subjects: Kernel graph cut in neutrosophic domain. <i>PLoS ONE</i> , 2017 , 12, e0186949	3.7	22
165	LLR-Based Successive-Cancellation List Decoder for Polar Codes With Multibit Decision. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2017 , 64, 21-25	3.5	21
164	Low complexity algorithm for seizure prediction using Adaboost. <i>Annual International Conference of the IEEE Engineering in Medicine and Biology Society IEEE Engineering in Medicine and Biology Society Annual International Conference</i> , 2012 , 2012, 1061-4	0.9	21
163	Digital Signal Processing With Molecular Reactions. <i>IEEE Design and Test of Computers</i> , 2012 , 29, 21-31		20
162	Automated localization of cysts in diabetic macular edema using optical coherence tomography images. <i>Annual International Conference of the IEEE Engineering in Medicine and Biology Society IEEE Engineering in Medicine and Biology Society Annual International Conference</i> , 2013 , 2013, 1426-9	0.9	20
161	Low- Cost Parallel FIR Filter Structures With 2-Stage Parallelism. <i>IEEE Transactions on Circuits and Systems Part 1: Regular Papers</i> , 2007 , 54, 280-290		20
160	Multidimensional analysis of the abnormal neural oscillations associated with lexical processing in schizophrenia. <i>Clinical EEG and Neuroscience</i> , 2013 , 44, 135-43	2.3	19
159	Key-Based Dynamic Functional Obfuscation of Integrated Circuits Using Sequentially Triggered Mode-Based Design. <i>IEEE Transactions on Information Forensics and Security</i> , 2018 , 13, 79-93	8	18
158	Lattice FIR digital filter architectures using stochastic computing 2015 ,		17
157	Computing Mathematical Functions using DNA via Fractional Coding. <i>Scientific Reports</i> , 2018 , 8, 8312	4.9	17
156	Seizure prediction using polynomial SVM classification. <i>Annual International Conference of the IEEE Engineering in Medicine and Biology Society IEEE Engineering in Medicine and Biology Society Annual International Conference</i> , 2015 , 2015, 5748-51	0.9	17

155	A Low-Complexity Hybrid LDPC Code Encoder for IEEE 802.3an (10GBase-T) Ethernet. <i>IEEE Transactions on Signal Processing</i> , 2009 , 57, 4085-4094	4.8	17
154	High-Speed Architecture Design of Tomlinson-Harashima Precoders. <i>IEEE Transactions on Circuits and Systems Part 1: Regular Papers</i> , 2007 , 54, 1929-1937		17
153	Classification of obsessive-compulsive disorder from resting-state fMRI. <i>Annual International Conference of the IEEE Engineering in Medicine and Biology Society IEEE Engineering in Medicine and Biology Society Annual International Conference</i> , 2016 , 2016, 3606-3609	0.9	15
152	Architectures for polar BP decoders using folding 2014 ,		15
151	Seizure detection using regression tree based feature selection and polynomial SVM classification. <i>Annual International Conference of the IEEE Engineering in Medicine and Biology Society IEEE Engineering in Medicine and Biology Society Annual International Conference</i> , 2015 , 2015, 6578-81	0.9	15
150	Successive cancellation list polar decoder using log-likelihood ratios 2014 ,		15
149	Reconfigurable architectures for silicon Physical Unclonable Functions 2011 ,		15
148	High-speed VLSI arithmetic processor architectures using hybrid number representation. <i>Journal of Signal Processing Systems</i> , 1992 , 4, 177-198		15
147	Belief propagation decoding of polar codes using stochastic computing 2016 ,		14
146	Molecular Sensing and Computing Systems. <i>IEEE Transactions on Molecular, Biological, and Multi-Scale Communications</i> , 2015 , 1, 249-264	2.3	13
145	Architecture Optimizations for the RSA Public Key Cryptosystem: A Tutorial. <i>IEEE Circuits and Systems Magazine</i> , 2011 , 11, 24-34	3.2	13
144	FPGA implementation and comparison of AES-GCM and Deoxys authenticated encryption schemes 2017 ,		12
143	Fast 2D Convolution Algorithms for Convolutional Neural Networks. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2020 , 67, 1678-1691	3.9	12
142	Optimized Joint Timing Synchronization and Channel Estimation for OFDM Systems. <i>IEEE Wireless Communications Letters</i> , 2012 , 1, 149-152	5.9	12
141	Efficient parallel VLSI architecture for linear feedback shift registers 2010 ,		11
140	Low-Cost Fast VLSI Algorithm for Discrete Fourier Transform. <i>IEEE Transactions on Circuits and Systems Part 1: Regular Papers</i> , 2007 , 54, 791-806		11
139	Computing Polynomials with Positive Coefficients using Stochastic Logic by Double-NAND Expansion 2017 ,		10
138	Ranking Regions, Edges and Classifying Tasks in Functional Brain Graphs by Sub-Graph Entropy. <i>Scientific Reports</i> , 2019 , 9, 7628	4.9	10

137	Seizure prediction with spectral power of time/space-differential EEG signals using cost-sensitive support vector machine 2010 ,		10
136	Development of TEO phase for speaker recognition 2010 ,		10
135	GPU accelerated elliptic curve cryptography in GF(2 ^m) 2010 ,		10
134	High-Throughput Radix-4 logMAP Turbo Decoder Architecture 2006 ,		10
133	Input compression and efficient VLSI architectures for rank order and stack filters. <i>Signal Processing</i> , 1994 , 38, 441-453	4.4	10
132	A Serial Commutator Fast Fourier Transform Architecture for Real-Valued Signals. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2018 , 65, 1693-1697	3.5	10
131	OCT Fluid Segmentation using Graph Shortest Path and Convolutional Neural Network. <i>Annual International Conference of the IEEE Engineering in Medicine and Biology Society IEEE Engineering in Medicine and Biology Society Annual International Conference</i> , 2018 , 2018, 3426-3429	0.9	10
130	Computing Polynomials Using Unipolar Stochastic Logic. <i>ACM Journal on Emerging Technologies in Computing Systems</i> , 2017 , 13, 1-30	1.7	9
129	Markov chain computations using molecular reactions 2015 ,		9
128	Molecular and DNA Artificial Neural Networks via Fractional Coding. <i>IEEE Transactions on Biomedical Circuits and Systems</i> , 2020 , 14, 490-503	5.1	9
127	Asynchronous discrete-time signal processing with molecular reactions 2014 ,		9
126	Verifying equivalence of digital signal processing circuits 2012 ,		9
125	Power Reduction in Frequency-Selective FIR Filters Under Voltage Overscaling. <i>IEEE Journal on Emerging and Selected Topics in Circuits and Systems</i> , 2011 , 1, 343-356	5.2	9
124	A synthesis flow for digital signal processing with biomolecular reactions 2010 ,		9
123	Hardware Efficient Low-Latency Architecture for High Throughput Rate Viterbi Decoders. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2008 , 55, 1254-1258	3.5	9
122	Low Complexity Implementations of Sum-Product Algorithm for Decoding Low-Density Parity-Check Codes 2006 ,		9
121	Sub-graph entropy based network approaches for classifying adolescent obsessive-compulsive disorder from resting-state functional MRI. <i>NeuroImage: Clinical</i> , 2020 , 26, 102208	5.3	8
120	Machine learning classifiers using stochastic logic 2016 ,		8

119	Incremental-Precision Based Feature Computation and Multi-Level Classification for Low-Energy Internet-of-Things. <i>IEEE Journal on Emerging and Selected Topics in Circuits and Systems</i> , 2018 , 8, 822-835 ^{5.2}	8
118	. <i>IEEE Transactions on Knowledge and Data Engineering</i> , 2019 , 31, 1750-1764	4.2 8
117	Discriminative Ratio of Spectral Power and Relative Power Features Derived via Frequency-Domain Model Ratio With Application to Seizure Prediction. <i>IEEE Transactions on Biomedical Circuits and Systems</i> , 2019 , 13, 645-657	5.1 8
116	Classification of borderline personality disorder based on spectral power of resting-state fMRI. <i>Annual International Conference of the IEEE Engineering in Medicine and Biology Society IEEE Engineering in Medicine and Biology Society Annual International Conference</i> , 2014 , 2014, 5036-9	0.9 8
115	Serial and interleaved architectures for computing real FFT 2015 ,	7
114	Hierarchical Folding and Synthesis of Iterative Data Flow Graphs. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2013 , 60, 597-601	3.5 7
113	2017 ,	7
112	Seizure detection using wavelet decomposition of the prediction error signal from a single channel of intra-cranial EEG. <i>Annual International Conference of the IEEE Engineering in Medicine and Biology Society IEEE Engineering in Medicine and Biology Society Annual International Conference</i> , 2014 , 2014, 4443-6	0.9 7
111	Novel Variable length Teager Energy Based features for person recognition from their hum 2010 ,	7
110	Probabilistic Spherical Detection and VLSI Implementation for Multiple-Antenna Systems. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2009 , 56, 685-698	3.9 7
109	Screening fundus images for diabetic retinopathy 2012 ,	7
108	Novel FEXT Cancellation and Equalization for High Speed Ethernet Transmission. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2009 , 56, 1272-1285	3.9 7
107	High speed VLSI architecture for general linear feedback shift register (LFSR) structures 2009 ,	7
106	Area efficient controller design of barrel shifters for reconfigurable LDPC decoders 2008 ,	7
105	Design of a Sample-Rate Converter From CD to DAT Using Fractional Delay Allpass Filter. <i>IEEE Transactions on Circuits and Systems Part 2: Express Briefs</i> , 2007 , 54, 19-23	7
104	Lower bounds on memory requirements for statically scheduled DSP programs. <i>Journal of Signal Processing Systems</i> , 1996 , 12, 247-264	7
103	Fault-tolerant ripple-carry binary adder using partial triple modular redundancy (PTMR) 2015 ,	6
102	Architectures for stochastic normalized and modified lattice IIR filters 2015 ,	6

101	Fast Reconfigurable Elliptic Curve Cryptography Acceleration for GF(2 ^m) on 32 bit Processors. <i>Journal of Signal Processing Systems</i> , 2010 , 60, 31-45	1.4	6
100	Optimally quantized offset min-sum algorithm for flexible LDPC decoder 2008 ,		6
99	Computing hyperbolic tangent and sigmoid functions using stochastic logic 2016 ,		6
98	Computing RBF Kernel for SVM Classification Using Stochastic Logic 2016 ,		6
97	Abnormal Neural Oscillations in Schizophrenia Assessed by Spectral Power Ratio of MEG During Word Processing. <i>IEEE Transactions on Neural Systems and Rehabilitation Engineering</i> , 2016 , 24, 1148-1158	4.8	6
96	Anatomical Biomarkers for Adolescent Major Depressive Disorder from Diffusion Weighted Imaging using SVM Classifier. <i>Annual International Conference of the IEEE Engineering in Medicine and Biology Society IEEE Engineering in Medicine and Biology Society Annual International Conference, 2019, 2019, 2710-2713</i>	0.9	6
95	Performance comparison of AES-GCM-SIV and AES-GCM algorithms for authenticated encryption on FPGA platforms 2017 ,		5
94	Linear-Phase Lattice FIR Digital Filter Architectures Using Stochastic Logic. <i>Journal of Signal Processing Systems</i> , 2018 , 90, 791-803	1.4	5
93	Computing Complex Functions using Factorization in Unipolar Stochastic Logic 2016 ,		5
92	Algorithm and architecture for hybrid decoding of polar codes 2014 ,		5
91	2014 ,		5
90	Robust and low complexity algorithms for seizure detection. <i>Annual International Conference of the IEEE Engineering in Medicine and Biology Society IEEE Engineering in Medicine and Biology Society Annual International Conference</i> , 2014 , 2014, 4447-50	0.9	5
89	A Network-Efficient Nonbinary QC-LDPC Decoder Architecture. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2012 , 59, 1359-1371	3.9	5
88	Low-energy architectures for Support Vector Machine computation 2013 ,		5
87	Low-power frequency selective filtering 2009 ,		5
86	Efficient Highly-Parallel Decoder Architecture for Quasi-Cyclic Low-Density Parity-Check Codes 2007 ,		5
85	Brain-Inspired Computing: Models and Architectures. <i>IEEE Open Journal of Circuits and Systems</i> , 2020 , 1, 185-204	1.7	5
84	Classification of Major Depressive Disorder from Resting-State fMRI. <i>Annual International Conference of the IEEE Engineering in Medicine and Biology Society IEEE Engineering in Medicine and Biology Society Annual International Conference</i> , 2019 , 2019, 3511-3514	0.9	5

83	Predicting Biological Gender and Intelligence From fMRI via Dynamic Functional Connectivity. <i>IEEE Transactions on Biomedical Engineering</i> , 2021 , 68, 815-825	5	5
82	Stochastic Logic Implementations of Polynomials With All Positive Coefficients by Expansion Methods. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2018 , 65, 1698-1702	3.5	5
81	Extraction of common task signals and spatial maps from group fMRI using a PARAFAC-based tensor decomposition technique 2017 ,		4
80	Protecting DSP circuits through obfuscation 2014 ,		4
79	Noise reduction for low-power broadband filtering 2009 ,		4
78	Low-Latency Low-Complexity Architectures for Viterbi Decoders. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2009 , 56, 2315-2324	3.9	4
77	Low-Complexity Echo and NEXT Cancellers for High-Speed Ethernet Transceivers. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2008 , 55, 2827-2840	3.9	4
76	Low Complexity List Updating Circuits for List Sphere Decoders. <i>Signal Processing Systems Design and Implementation (siPS)</i> , <i>IEEE Workshop on</i> , 2006 ,		4
75	Classification of Adolescent Major Depressive Disorder Via Static and Dynamic Connectivity. <i>IEEE Journal of Biomedical and Health Informatics</i> , 2021 , 25, 2604-2614	7.2	4
74	Computing Radial Basis Function Support Vector Machine using DNA via Fractional Coding 2019 ,		3
73	Architecture Optimization and Performance Comparison of Nonce-Misuse-Resistant Authenticated Encryption Algorithms. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2019 , 27, 1053-1066	2.6	3
72	An obfuscated radix-2 real FFT architecture 2015 ,		3
71	. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2020 , 67, 5429-5442	3.9	3
70	Parallel - pipelined radix-22 FFT architecture for real valued signals 2010 ,		3
69	Asynchronous computation with molecular reactions 2011 ,		3
68	A Generalized Technique for Register Counting and its Application to Cost-Optimal DSP Architecture Synthesis. <i>Journal of Signal Processing Systems</i> , 1997 , 16, 57-72		3
67	Heuristic Loop-Based Scheduling and Allocation for DSP Synthesis with Heterogeneous Functional Units. <i>Journal of Signal Processing Systems</i> , 1998 , 19, 243-256		3
66	Design of Parallel Tomlinson-Harashima Precoders. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2008 , 55, 447-451	3.5	3

65	Performance of Quantized Min-Sum Decoding Algorithms for Irregular LDPC Codes 2007 ,		3
64	Adaptive Tap Management in Multi-Gigabit Echo & Next Cancellers. <i>Signal Processing Systems Design and Implementation (siPS), IEEE Workshop on</i> , 2006 ,		3
63	Performance-Scalable Array Architectures for Modular Multiplication. <i>Journal of Signal Processing Systems</i> , 2002 , 31, 101-116		3
62	Finite Wordlength Analysis and Adaptive Decoding for Turbo/MAP Decoders. <i>Journal of Signal Processing Systems</i> , 2001 , 29, 209-221		3
61	Power Efficient Folding of Pipelined LMS Adaptive Filters with Applications to Wireline Digital Communications. <i>Journal of Signal Processing Systems</i> , 2000 , 25, 199-213		3
60	Energy Efficient Signaling in Deep-submicron Technology. <i>VLSI Design</i> , 2002 , 15, 563-586		3
59	Synthesis of correlated bit streams for stochastic computing 2016 ,		3
58	Joint brain connectivity estimation from diffusion and functional MRI data 2015 ,		2
57	Functional encryption of integrated circuits by key-based hybrid obfuscation 2017 ,		2
56	A Physical Unclonable Function based on Capacitor Mismatch in a Charge-Redistribution SAR-ADC 2018 ,		2
55	Improved BER Performance With Rotated Head Array and 2-D Detector in Two-Dimensional Magnetic Recording. <i>IEEE Transactions on Magnetics</i> , 2016 , 52, 1-6	2	2
54	Interleaved successive cancellation polar decoders 2014 ,		2
53	Classification of single-trial MEG during sentence processing for automated schizophrenia screening 2013 ,		2
52	Comments on "Low-energy CSMT carry generators and binary adders". <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2013 , 21, 791-791	2.6	2
51	Impulse Noise Correction in OFDM Systems. <i>Journal of Signal Processing Systems</i> , 2014 , 74, 245-262	1.4	2
50	Underdetermined blind source separation based on Continuous Density Hidden Markov Models 2010 ,		2
49	Sparseness-Controlled Adaptive Tap algorithms for partial update adaptive filters 2009 ,		2
48	Hardware Efficient Fast Computation of the Discrete Fourier Transform. <i>Journal of Signal Processing Systems</i> , 2006 , 42, 159-171		2

47	On the Performance and Implementation Issues of Interleaved Single Parity Check Turbo Product Codes. <i>Journal of Signal Processing Systems</i> , 2005 , 39, 35-47		2
46	A Novel Low-power Shared Division and Square-root Architecture Using the GST Algorithm. <i>VLSI Design</i> , 2001 , 12, 365-376		2
45	A low-power twiddle factor addressing architecture for split-radix FFT processor. <i>Microelectronics Journal</i> , 2021 , 117, 105276	1.8	2
44	ProTro: A Probabilistic Counter Based Hardware Trojan Attack on FPGA Based MACSec Enabled Ethernet Switch. <i>Lecture Notes in Computer Science</i> , 2019 , 159-175	0.9	2
43	Seizure prediction using long-term fragmented intracranial canine and human EEG recordings 2016 ,		2
42	Training DNA Perceptrons via Fractional Coding 2019 ,		2
41	M3U: Minimum Mean Minimum Uncertainty Feature Selection for Multiclass Classification. <i>Journal of Signal Processing Systems</i> , 2020 , 92, 9-22	1.4	2
40	Classifying Treated vs. Untreated MDD Adolescents from Anatomical Connectivity using Nonlinear SVM. <i>Annual International Conference of the IEEE Engineering in Medicine and Biology Society IEEE Engineering in Medicine and Biology Society Annual International Conference</i> , 2018 , 2018, 1-4	0.9	2
39	Probabilistic Hardware Trojan Attacks on Multiple Layers of Reconfigurable Network Infrastructure. <i>Journal of Hardware and Systems Security</i> , 2020 , 4, 343-360	1.6	1
38	Canonic Composite Length Real-Valued FFT. <i>Journal of Signal Processing Systems</i> , 2018 , 90, 1401-1414	1.4	1
37	Low-Energy Architectures of Linear Classifiers for IoT Applications using Incremental Precision and Multi-Level Classification 2018 ,		1
36	Predicting Male vs. Female from Task-fMRI Brain Connectivity. <i>Annual International Conference of the IEEE Engineering in Medicine and Biology Society IEEE Engineering in Medicine and Biology Society Annual International Conference</i> , 2019 , 2019, 4089-4092	0.9	1
35	Canonic FFT flow graphs for real-valued even/odd symmetric inputs. <i>Eurasip Journal on Advances in Signal Processing</i> , 2017 , 2017,	1.9	1
34	Canonic real-valued radix-2n FFT computations 2015 ,		1
33	Seizure prediction using cross-correlation and classification 2015 ,		1
32	Two-Dimensional Magnetic Recording Using a Rotated Head Array and LDPC Code Decoding. <i>IEEE Transactions on Magnetics</i> , 2014 , 50, 1-4	2	1
31	Static and dynamic information derived from source and system features for person recognition from humming. <i>International Journal of Speech Technology</i> , 2012 , 15, 393-406	1.3	1
30	Secure Variable Data Rate Transmission. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2011 , 58, 100-104	3.5	1

29	Digital signal processing with biomolecular reactions 2010 ,		1
28	Low Complexity Decoder Architecture for Low-Density Parity-Check Codes. <i>Journal of Signal Processing Systems</i> , 2009 , 56, 217-228	1.4	1
27	Multi-delay block frequency domain adaptive filters with sparse partial subblock update 2009 ,		1
26	Sparse severe error removal in OFDM demodulators for erasure channels 2009 ,		1
25	Further cost reduction of adaptive echo and next cancellers for high-speed Ethernet transceivers 2008 ,		1
24	Minimal complexity low-latency architectures for Viterbi decoders 2008 ,		1
23	High-speed implementation of Smith-Waterman algorithm for DNA sequence scanning in VLSI 2008 ,		1
22	Low complexity iterative joint detection, decoding, and channel estimation for wireless MIMO system. <i>Signal Processing Systems Design and Implementation (siPS), IEEE Workshop on</i> , 2006 ,		1
21	Models for Architectural Power and Power Grid Noise Analysis on Data Bus. <i>Journal of Signal Processing Systems</i> , 2006 , 44, 25-46		1
20	High speed algorithm and VLSI architecture design for decoding BCH product codes 2002 ,		1
19	Reduced-Complexity Modular Polynomial Multiplication for R-LWE Cryptosystems 2021 ,		1
18	Seizure Detection Using Power Spectral Density via Hyperdimensional Computing 2021 ,		1
17	Data-Canonic Real FFT Flow-Graphs for Composite Lengths 2016 ,		1
16	Region-Level Functional and Effective Network Analysis of Human Brain During Cognitive Task Engagement. <i>IEEE Transactions on Neural Systems and Rehabilitation Engineering</i> , 2021 , 29, 1651-1660	4.8	1
15	Biomarkers for Adolescent MDD from Anatomical Connectivity and Network Topology Using Diffusion MRI. <i>Annual International Conference of the IEEE Engineering in Medicine and Biology Society IEEE Engineering in Medicine and Biology Society Annual International Conference</i> , 2018 , 2018, 1152-1155	0.9	1
14	Biological Gender Classification from fMRI via Hyperdimensional Computing 2021 ,		1
13	Design and Optimization of Multiplierless FIR Filters Using Sub-Threshold Circuits. <i>Journal of Signal Processing Systems</i> , 2013 , 70, 259-274	1.4	0
12	Teaching Digital Signal Processing by Partial Flipping, Active Learning, and Visualization: Keeping Students Engaged With Blended Teaching. <i>IEEE Signal Processing Magazine</i> , 2021 , 38, 20-29	9.4	0

11	Applicability of Hyperdimensional Computing to Seizure Detection. <i>IEEE Open Journal of Circuits and Systems</i> , 2022 , 3, 59-71	1.7	o
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