

Keshab K Parhi

List of Publications by Year in descending order

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234
papers

5,155
citations

117453

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234
docs citations

234
times ranked

3621
citing authors

#	ARTICLE	IF	CITATIONS
1	Seizure prediction with spectral power of EEG using cost-sensitive support vector machines. <i>Epilepsia</i> , 2011, 52, 1761-1770.	2.6	341
2	Iterative Vessel Segmentation of Fundus Images. <i>IEEE Transactions on Biomedical Engineering</i> , 2015, 62, 1738-1749.	2.5	231
3	Early Stopping Criteria for Energy-Efficient Low-Latency Belief-Propagation Polar Code Decoders. <i>IEEE Transactions on Signal Processing</i> , 2014, 62, 6496-6506.	3.2	178
4	Low-Complexity Seizure Prediction From iEEG/sEEG Using Spectral Power and Ratios of Spectral Power. <i>IEEE Transactions on Biomedical Circuits and Systems</i> , 2016, 10, 693-706.	2.7	171
5	Low-Energy Digit-Serial/Parallel Finite Field Multipliers. <i>Journal of Signal Processing Systems</i> , 1998, 19, 149-166.	1.0	161
6	Pipelined Parallel FFT Architectures via Folding Transformation. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2012, 20, 1068-1081.	2.1	140
7	RETOUCH: The Retinal OCT Fluid Detection and Segmentation Benchmark and Challenge. <i>IEEE Transactions on Medical Imaging</i> , 2019, 38, 1858-1874.	5.4	139
8	A Pipelined FFT Architecture for Real-Valued Signals. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2009, 56, 2634-2643.	3.5	108
9	Classification Using Hyperdimensional Computing: A Review. <i>IEEE Circuits and Systems Magazine</i> , 2020, 20, 30-47.	2.6	99
10	Low-Latency Successive-Cancellation Polar Decoder Architectures Using 2-Bit Decoding. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2014, 61, 1241-1254.	3.5	98
11	Low-Latency Successive-Cancellation List Decoders for Polar Codes With Multibit Decision. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2015, 23, 2268-2280.	2.1	86
12	Architecture optimizations for BP polar decoders. , 2013, , .		85
13	Network analysis of functional brain connectivity in borderline personality disorder using resting-state fMRI. <i>NeuroImage: Clinical</i> , 2016, 11, 302-315.	1.4	79
14	Reduced-latency SC polar decoder architectures. , 2012, , .		78
15	Discrete-Time Signal Processing with DNA. <i>ACS Synthetic Biology</i> , 2013, 2, 245-254.	1.9	77
16	Low-Area/Power Parallel FIR Digital Filter Implementations. <i>Journal of Signal Processing Systems</i> , 1997, 17, 75-92.	1.0	76
17	High-Speed VLSI Implementation of 2-D Discrete Wavelet Transform. <i>IEEE Transactions on Signal Processing</i> , 2008, 56, 393-403.	3.2	76
18	Low-Complexity Welch Power Spectral Density Computation. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2014, 61, 172-182.	3.5	73

#	ARTICLE	IF	CITATIONS
19	Obfuscating DSP Circuits via High-Level Transformations. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, 23, 819-830.	2.1	71
20	Fully-Automated Segmentation of Fluid/Cyst Regions in Optical Coherence Tomography Images with Diabetic Macular Edema using Neutrosophic Sets and Graph Algorithms. IEEE Transactions on Biomedical Engineering, 2017, 65, 1-1.	2.5	64
21	High-Throughput VLSI Architecture for FFT Computation. IEEE Transactions on Circuits and Systems II: Express Briefs, 2007, 54, 863-867.	2.2	59
22	Computation Error Analysis in Digital Signal Processing Systems With Overscaled Supply Voltage. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2010, 18, 517-526.	2.1	59
23	Secure and Reliable XOR Arbiter PUF Design. , 2017, , .		57
24	Architectures for digital filters using stochastic computing. , 2013, , .		56
25	Min-Sum Decoder Architectures With Reduced Word Length for LDPC Codes. IEEE Transactions on Circuits and Systems I: Regular Papers, 2010, 57, 105-115.	3.5	55
26	High-Speed Parallel Architectures for Linear Feedback Shift Registers. IEEE Transactions on Signal Processing, 2011, 59, 4459-4469.	3.2	54
27	Computing Arithmetic Functions Using Stochastic Logic by Series Expansion. IEEE Transactions on Emerging Topics in Computing, 2019, 7, 44-59.	3.2	54
28	An In-Place FFT Architecture for Real-Valued Signals. IEEE Transactions on Circuits and Systems II: Express Briefs, 2013, 60, 652-656.	2.2	52
29	Architectures for Recursive Digital Filters Using Stochastic Computing. IEEE Transactions on Signal Processing, 2016, 64, 3705-3718.	3.2	50
30	Homogeneous and Heterogeneous Feed-Forward XOR Physical Unclonable Functions. IEEE Transactions on Information Forensics and Security, 2020, 15, 2485-2498.	4.5	50
31	Low-Complexity Switch Network for Reconfigurable LDPC Decoders. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2010, 18, 85-94.	2.1	47
32	FFT Architectures for Real-Valued Signals Based on Radix- 2^3 and Radix- 2^4 Algorithms. IEEE Transactions on Circuits and Systems I: Regular Papers, 2013, 60, 2422-2430.	3.5	41
33	Chemical Reaction Networks for Computing Polynomials. ACS Synthetic Biology, 2017, 6, 76-83.	1.9	41
34	Low- Cost Parallel FIR Filter Structures With 2-Stage Parallelism. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2007, 54, 280-290.	0.1	39
35	Pipelined Architectures for Real-Valued FFT and Hermitian-Symmetric IFFT With Real Datapaths. IEEE Transactions on Circuits and Systems II: Express Briefs, 2013, 60, 507-511.	2.2	39
36	Low-Cost Fast VLSI Algorithm for Discrete Fourier Transform. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2007, 54, 791-806.	0.1	37

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37	Key-Based Dynamic Functional Obfuscation of Integrated Circuits Using Sequentially Triggered Mode-Based Design. IEEE Transactions on Information Forensics and Security, 2018, 13, 79-93.	4.5	36
38	Fully-automated segmentation of fluid regions in exudative age-related macular degeneration subjects: Kernel graph cut in neutrosophic domain. PLoS ONE, 2017, 12, e0186949.	1.1	36
39	High-speed VLSI arithmetic processor architectures using hybrid number representation. Journal of Signal Processing Systems, 1992, 4, 177-198.	1.0	35
40	Synchronous sequential computation with molecular reactions. , 2011, , .		35
41	Latency Analysis and Architecture Design of Simplified SC Polar Decoders. IEEE Transactions on Circuits and Systems II: Express Briefs, 2014, 61, 115-119.	2.2	35
42	Reliable PUF-Based Local Authentication With Self-Correction. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2017, 36, 201-213.	1.9	35
43	Function-specific and Enhanced Brain Structural Connectivity Mapping via Joint Modeling of Diffusion and Functional MRI. Scientific Reports, 2018, 8, 4741.	1.6	35
44	Digital logic with molecular reactions. , 2013, , .		34
45	Architectures for IIR digital filters using stochastic computing. , 2014, , .		34
46	Computing Mathematical Functions using DNA via Fractional Coding. Scientific Reports, 2018, 8, 8312.	1.6	30
47	A Low-Complexity Hybrid LDPC Code Encoder for IEEE 802.3an (10GBase-T) Ethernet. IEEE Transactions on Signal Processing, 2009, 57, 4085-4094.	3.2	29
48	Successive cancellation decoding of polar codes using stochastic computing. , 2015, , .		29
49	LLR-Based Successive-Cancellation List Decoder for Polar Codes With Multibit Decision. IEEE Transactions on Circuits and Systems II: Express Briefs, 2017, 64, 21-25.	2.2	28
50	Automated localization of cysts in diabetic macular edema using optical coherence tomography images. , 2013, 2013, 1426-9.		27
51	Successive cancellation list polar decoder using log-likelihood ratios. , 2014, , .		27
52	Low complexity algorithm for seizure prediction using Adaboost. , 2012, 2012, 1061-4.		26
53	Architectures for polar BP decoders using folding. , 2014, , .		25
54	Seizure prediction using polynomial SVM classification. , 2015, 2015, 5748-51.		25

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55	Molecular Sensing and Computing Systems. IEEE Transactions on Molecular, Biological, and Multi-Scale Communications, 2015, 1, 249-264.	1.4	24
56	Lattice FIR digital filter architectures using stochastic computing. , 2015, , .		24
57	Digital Signal Processing With Molecular Reactions. IEEE Design and Test of Computers, 2012, 29, 21-31.	1.4	23
58	Multidimensional Analysis of the Abnormal Neural Oscillations Associated With Lexical Processing in Schizophrenia. Clinical EEG and Neuroscience, 2013, 44, 135-143.	0.9	23
59	Seizure detection using regression tree based feature selection and polynomial SVM classification. , 2015, 2015, 6578-81.		22
60	Early Seizure Detection Using Neuronal Potential Similarity: A Generalized Low-Complexity and Robust Measure. International Journal of Neural Systems, 2015, 25, 1550019.	3.2	22
61	Classification of obsessive-compulsive disorder from resting-state fMRI. , 2016, 2016, 3606-3609.		22
62	A Serial Commutator Fast Fourier Transform Architecture for Real-Valued Signals. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 1693-1697.	2.2	22
63	OCT Fluid Segmentation using Graph Shortest Path and Convolutional Neural Network. , 2018, 2018, 3426-3429.		21
64	Brain-Inspired Computing: Models and Architectures. IEEE Open Journal of Circuits and Systems, 2020, 1, 185-204.	1.4	21
65	Reconfigurable architectures for silicon Physical Unclonable Functions. , 2011, , .		20
66	Classification of Adolescent Major Depressive Disorder Via Static and Dynamic Connectivity. IEEE Journal of Biomedical and Health Informatics, 2021, 25, 2604-2614.	3.9	20
67	Input compression and efficient VLSI architectures for rank order and stack filters. Signal Processing, 1994, 38, 441-453.	2.1	19
68	Belief propagation decoding of polar codes using stochastic computing. , 2016, , .		19
69	FPGA implementation and comparison of AES-GCM and Deoxys authenticated encryption schemes. , 2017, , .		19
70	Molecular and DNA Artificial Neural Networks via Fractional Coding. IEEE Transactions on Biomedical Circuits and Systems, 2020, 14, 490-503.	2.7	19
71	High-Speed Architecture Design of Tomlinson–Harashima Precoders. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2007, 54, 1929-1937.	0.1	18
72	Optimized Joint Timing Synchronization and Channel Estimation for OFDM Systems. IEEE Wireless Communications Letters, 2012, 1, 149-152.	3.2	18

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73	Computing Polynomials Using Unipolar Stochastic Logic. ACM Journal on Emerging Technologies in Computing Systems, 2017, 13, 1-30.	1.8	18
74	Fast 2D Convolution Algorithms for Convolutional Neural Networks. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 1678-1691.	3.5	18
75	Predicting Biological Gender and Intelligence From fMRI via Dynamic Functional Connectivity. IEEE Transactions on Biomedical Engineering, 2021, 68, 815-825.	2.5	18
76	Markov chain computations using molecular reactions. , 2015, , .		17
77	Seizure prediction with spectral power of time/space-differential EEG signals using cost-sensitive support vector machine. , 2010, , .		16
78	Architecture Optimizations for the RSA Public Key Cryptosystem: A Tutorial. IEEE Circuits and Systems Magazine, 2011, 11, 24-34.	2.6	16
79	Classification of Major Depressive Disorder from Resting-State fMRI. , 2019, 2019, 3511-3514.		16
80	Fast Reconfigurable Elliptic Curve Cryptography Acceleration for GF(2 ^m) on 32 bit Processors. Journal of Signal Processing Systems, 2010, 60, 31-45.	1.4	15
81	Screening fundus images for diabetic retinopathy. , 2012, , .		15
82	Asynchronous discrete-time signal processing with molecular reactions. , 2014, , .		15
83	Computing RBF Kernel for SVM Classification Using Stochastic Logic. , 2016, , .		15
84	Sub-graph entropy based network approaches for classifying adolescent obsessive-compulsive disorder from resting-state functional MRI. NeuroImage: Clinical, 2020, 26, 102208.	1.4	15
85	Novel Variable length Teager Energy Based features for person recognition from their hum. , 2010, , .		14
86	Efficient parallel VLSI architecture for linear feedback shift registers. , 2010, , .		14
87	Computing Polynomials with Positive Coefficients using Stochastic Logic by Double-NAND Expansion. , 2017, , .		14
88	Analysis of stochastic logic circuits in unipolar, bipolar and hybrid formats. , 2017, , .		14
89	High-Throughput Radix-4 logMAP Turbo Decoder Architecture. , 2006, , .		13
90	Design of a Sample-Rate Converter From CD to DAT Using Fractional Delay Allpass Filter. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2007, 54, 19-23.	2.3	13

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91	Hardware Efficient Low-Latency Architecture for High Throughput Rate Viterbi Decoders. IEEE Transactions on Circuits and Systems II: Express Briefs, 2008, 55, 1254-1258.	2.2	13
92	Novel FEXT Cancellation and Equalization for High Speed Ethernet Transmission. IEEE Transactions on Circuits and Systems I: Regular Papers, 2009, 56, 1272-1285.	3.5	13
93	Development of TEO phase for speaker recognition. , 2010, , .		13
94	Hierarchical Folding and Synthesis of Iterative Data Flow Graphs. IEEE Transactions on Circuits and Systems II: Express Briefs, 2013, 60, 597-601.	2.2	13
95	Serial and interleaved architectures for computing real FFT. , 2015, , .		13
96	Computing hyperbolic tangent and sigmoid functions using stochastic logic. , 2016, , .		13
97	Abnormal Neural Oscillations in Schizophrenia Assessed by Spectral Power Ratio of MEG During Word Processing. IEEE Transactions on Neural Systems and Rehabilitation Engineering, 2016, 24, 1148-1158.	2.7	13
98	Ranking Regions, Edges and Classifying Tasks in Functional Brain Graphs by Sub-Graph Entropy. Scientific Reports, 2019, 9, 7628.	1.6	13
99	Low Complexity Implementations of Sum-Product Algorithm for Decoding Low-Density Parity-Check Codes. , 2006, , .		12
100	Efficient Highly-Parallel Decoder Architecture for Quasi-Cyclic Low-Density Parity-Check Codes. , 2007, , .		12
101	A synthesis flow for digital signal processing with biomolecular reactions. , 2010, , .		12
102	GPU accelerated elliptic curve cryptography in $GF(2^m)$. , 2010, , .		12
103	Extraction of common task signals and spatial maps from group fMRI using a PARAFAC-based tensor decomposition technique. , 2017, , .		12
104	Incremental-Precision Based Feature Computation and Multi-Level Classification for Low-Energy Internet-of-Things. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2018, 8, 822-835.	2.7	12
105	Reservoir Computing Using DNA Oscillators. ACS Synthetic Biology, 2022, 11, 780-787.	1.9	12
106	Seizure detection using wavelet decomposition of the prediction error signal from a single channel of intra-cranial EEG. , 2014, 2014, 4443-6.		11
107	Algorithm and architecture for hybrid decoding of polar codes. , 2014, , .		11
108	Classification of borderline personality disorder based on spectral power of resting-state fMRI. , 2014, 2014, 5036-9.		11

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109	Fault-tolerant ripple-carry binary adder using partial triple modular redundancy (PTMR)., 2015, , .		11
110	MUSE: Minimum Uncertainty and Sample Elimination Based Binary Feature Selection. IEEE Transactions on Knowledge and Data Engineering, 2019, 31, 1750-1764.	4.0	11
111	Discriminative Ratio of Spectral Power and Relative Power Features Derived via Frequency-Domain Model Ratio With Application to Seizure Prediction. IEEE Transactions on Biomedical Circuits and Systems, 2019, 13, 645-657.	2.7	11
112	Performance of Quantized Min-Sum Decoding Algorithms for Irregular LDPC Codes. , 2007, , .		10
113	High speed VLSI architecture for general linear feedback shift register (LFSR) structures. , 2009, , .		10
114	Power Reduction in Frequency-Selective FIR Filters Under Voltage Overscaling. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2011, 1, 343-356.	2.7	10
115	Verifying equivalence of digital signal processing circuits. , 2012, , .		10
116	Anatomical Biomarkers for Adolescent Major Depressive Disorder from Diffusion Weighted Imaging using SVM Classifier. , 2018, 2018, 2740-2743.		10
117	Architecture Optimization and Performance Comparison of Nonce-Misuse-Resistant Authenticated Encryption Algorithms. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 1053-1066.	2.1	10
118	Area efficient controller design of barrel shifters for reconfigurable LDPC decoders. , 2008, , .		9
119	Optimally quantized offset min-sum algorithm for flexible LDPC decoder. , 2008, , .		9
120	Probabilistic Spherical Detection and VLSI Implementation for Multiple-Antenna Systems. IEEE Transactions on Circuits and Systems I: Regular Papers, 2009, 56, 685-698.	3.5	9
121	A Network-Efficient Nonbinary QC-LDPC Decoder Architecture. IEEE Transactions on Circuits and Systems I: Regular Papers, 2012, 59, 1359-1371.	3.5	9
122	Performance comparison of AES-GCM-SIV and AES-GCM algorithms for authenticated encryption on FPGA platforms. , 2017, , .		9
123	Lower bounds on memory requirements for statically scheduled DSP programs. Journal of Signal Processing Systems, 1996, 12, 247-264.	1.0	8
124	Low Complexity List Updating Circuits for List Sphere Decoders. Signal Processing Systems Design and Implementation (siPS), IEEE Workshop on, 2006, , .	0.0	8
125	Low-Latency Low-Complexity Architectures for Viterbi Decoders. IEEE Transactions on Circuits and Systems I: Regular Papers, 2009, 56, 2315-2324.	3.5	8
126	Parallel - pipelined radix-2²/sup> FFT architecture for real valued signals. , 2010, , .		8

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127	Protecting DSP circuits through obfuscation. , 2014, , .		8
128	Machine learning classifiers using stochastic logic. , 2016, , .		8
129	A Physical Unclonable Function based on Capacitor Mismatch in a Charge-Redistribution SAR-ADC. , 2018, , .		8
130	Linear-Phase Lattice FIR Digital Filter Architectures Using Stochastic Logic. Journal of Signal Processing Systems, 2018, 90, 791-803.	1.4	8
131	Stochastic Logic Implementations of Polynomials With All Positive Coefficients by Expansion Methods. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 1698-1702.	2.2	8
132	Seizure Onset Zone Identification From iEEG: A Review. IEEE Access, 2022, 10, 62535-62547.	2.6	8
133	Hierarchical Functional Obfuscation of Integrated Circuits using a Mode-Based Approach. , 2017, , .		7
134	Region-Level Functional and Effective Network Analysis of Human Brain During Cognitive Task Engagement. IEEE Transactions on Neural Systems and Rehabilitation Engineering, 2021, 29, 1651-1660.	2.7	7
135	Low-energy architectures for Support Vector Machine computation. , 2013, , .		6
136	Robust and low complexity algorithms for seizure detection. , 2014, 2014, 4447-50.		6
137	Architectures for stochastic normalized and modified lattice IIR filters. , 2015, , .		6
138	An obfuscated radix-2 real FFT architecture. , 2015, , .		6
139	Synthesis of correlated bit streams for stochastic computing. , 2016, , .		6
140	Seizure prediction using long-term fragmented intracranial canine and human EEG recordings. , 2016, , .		6
141	Computing Complex Functions using Factorization in Unipolar Stochastic Logic. , 2016, , .		6
142	Biomarkers for Adolescent MDD from Anatomical Connectivity and Network Topology Using Diffusion MRI. , 2018, 2018, 1152-1155.		6
143	Effect of SSRIs on Resting-State Functional Brain Networks in Adolescents with Major Depressive Disorder. Journal of Clinical Medicine, 2021, 10, 4322.	1.0	6
144	Estimating Delay Differences of Arbiter PUFs Using Silicon Data. , 2016, , .		6

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145	Finite Wordlength Analysis and Adaptive Decoding for Turbo/MAP Decoders. Journal of Signal Processing Systems, 2001, 29, 209-221.	1.0	5
146	Low-Complexity Echo and NEXT Cancellers for High-Speed Ethernet Transceivers. IEEE Transactions on Circuits and Systems I: Regular Papers, 2008, 55, 2827-2840.	3.5	5
147	Noise reduction for low-power broadband filtering. , 2009, , .		5
148	Low-power frequency selective filtering. , 2009, , .		5
149	Canonic real-valued FFT structures. , 2014, , .		5
150	Interleaved successive cancellation polar decoders. , 2014, , .		5
151	Effect of Finite Word-Length on SQNR, Area and Power for Real-Valued Serial FFT. , 2019, , .		5
152	A High-Performance Stochastic LDPC Decoder Architecture Designed via Correlation Analysis. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 5429-5442.	3.5	5
153	Applicability of Hyperdimensional Computing to Seizure Detection. IEEE Open Journal of Circuits and Systems, 2022, 3, 59-71.	1.4	5
154	Biological Gender Classification from fMRI via Hyperdimensional Computing. , 2021, , .		5
155	DNA Memristors and Their Application to Reservoir Computing. ACS Synthetic Biology, 2022, 11, 2202-2213.	1.9	5
156	Title is missing!. Journal of Signal Processing Systems, 1997, 16, 57-72.	1.0	4
157	Title is missing!. Journal of Signal Processing Systems, 1998, 19, 243-256.	1.0	4
158	Title is missing!. Journal of Signal Processing Systems, 2000, 25, 199-213.	1.0	4
159	Hardware Efficient Fast Computation of the Discrete Fourier Transform. Journal of Signal Processing Systems, 2006, 42, 159-171.	1.0	4
160	Sparseness-Controlled Adaptive Tap algorithms for partial update adaptive filters. , 2009, , .		4
161	Asynchronous computation with molecular reactions. , 2011, , .		4
162	Impulse Noise Correction in OFDM Systems. Journal of Signal Processing Systems, 2014, 74, 245-262.	1.4	4

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163	Seizure prediction using cross-correlation and classification. , 2015, , .		4
164	Constrained Tensor Decomposition Optimization With Applications To Fmri Data Analysis. , 2018, , .		4
165	Predicting Male vs. Female from Task-fMRI Brain Connectivity. , 2019, 2019, 4089-4092.		4
166	Computing Radial Basis Function Support Vector Machine using DNA via Fractional Coding. , 2019, , .		4
167	Probabilistic Hardware Trojan Attacks on Multiple Layers of Reconfigurable Network Infrastructure. Journal of Hardware and Systems Security, 2020, 4, 343-360.	0.8	4
168	Reduced-Complexity Modular Polynomial Multiplication for R-LWE Cryptosystems. , 2021, , .		4
169	Performance-Scalable Array Architectures for Modular Multiplication. Journal of Signal Processing Systems, 2002, 31, 101-116.	1.0	3
170	On the Performance and Implementation Issues of Interleaved Single Parity Check Turbo Product Codes. Journal of Signal Processing Systems, 2005, 39, 35-47.	1.0	3
171	Adaptive Tap Management in Multi-Gigabit Echo&Next Cancellers. Signal Processing Systems Design and Implementation (siPS), IEEE Workshop on, 2006, , .	0.0	3
172	Minimal complexity low-latency architectures for Viterbi decoders. , 2008, , .		3
173	Design of Parallel Tomlinson&Harashima Precoders. IEEE Transactions on Circuits and Systems II: Express Briefs, 2008, 55, 447-451.	2.2	3
174	Digital signal processing with biomolecular reactions. , 2010, , .		3
175	Secure Variable Data Rate Transmission. IEEE Transactions on Circuits and Systems II: Express Briefs, 2011, 58, 100-104.	2.2	3
176	Classification of single-trial MEG during sentence processing for automated schizophrenia screening. , 2013, , .		3
177	Improved BER Performance With Rotated Head Array and 2-D Detector in Two-Dimensional Magnetic Recording. IEEE Transactions on Magnetics, 2016, 52, 1-6.	1.2	3
178	Canonic FFT flow graphs for real-valued even/odd symmetric inputs. Eurasip Journal on Advances in Signal Processing, 2017, 2017, .	1.0	3
179	Functional encryption of integrated circuits by key-based hybrid obfuscation. , 2017, , .		3
180	Classifying Treated vs. Untreated MDD Adolescents from Anatomical Connectivity using Nonlinear SVM. , 2018, 2018, 1-4.		3

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181	M3U: Minimum Mean Minimum Uncertainty Feature Selection for Multiclass Classification. Journal of Signal Processing Systems, 2020, 92, 9-22.	1.4	3
182	Seizure Detection Using Power Spectral Density via Hyperdimensional Computing. , 2021, , .		3
183	ProTro: A Probabilistic Counter Based Hardware Trojan Attack on FPGA Based MACSec Enabled Ethernet Switch. Lecture Notes in Computer Science, 2019, , 159-175.	1.0	3
184	Energy Efficient Signaling in Deep-submicron Technology. VLSI Design, 2002, 15, 563-586.	0.5	3
185	LayerPipe: Accelerating Deep Neural Network Training by Intra-Layer and Inter-Layer Gradient Pipelining and Multiprocessor Scheduling. , 2021, , .		3
186	A Novel Low-power Shared Division and Square-root Architecture Using the GST Algorithm. VLSI Design, 2001, 12, 365-376.	0.5	2
187	A very low complexity soft decoding of space-time block codes. , 2002, , .		2
188	Low Complexity Radius Reduction Method for List Sphere Decoders. , 2006, , .		2
189	Multi-delay block frequency domain adaptive filters with sparse partial subblock update. , 2009, , .		2
190	Sparse severe error removal in OFDM demodulators for erasure channels. , 2009, , .		2
191	Underdetermined blind source separation based on Continuous Density Hidden Markov Models. , 2010, , .		2
192	Static and dynamic information derived from source and system features for person recognition from humming. International Journal of Speech Technology, 2012, 15, 393-406.	1.4	2
193	Comments on "Low-energy CSMT carry generators and binary adders". IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2013, 21, 791-791.	2.1	2
194	Canonic real-valued radix-2n FFT computations. , 2015, , .		2
195	Joint brain connectivity estimation from diffusion and functional MRI data. , 2015, , .		2
196	Data-Canonic Real FFT Flow-Graphs for Composite Lengths. , 2016, , .		2
197	Not All Feed-Forward MUX PUFs Generate Unique Signatures. , 2019, , .		2
198	Training DNA Perceptrons via Fractional Coding. , 2019, , .		2

#	ARTICLE	IF	CITATIONS
199	A Reconfigurable 74-140Mbps LDPC Decoding System for CCSDS Standard. IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, 2021, E104.A, 1509-1515.	0.2	2
200	Teaching Digital Signal Processing by Partial Flipping, Active Learning, and Visualization: Keeping Students Engaged With Blended Teaching. IEEE Signal Processing Magazine, 2021, 38, 20-29.	4.6	2
201	Graph-Theoretic Properties of Sub-Graph Entropy. IEEE Signal Processing Letters, 2021, 28, 135-139.	2.1	2
202	A low-power twiddle factor addressing architecture for split-radix FFT processor. Microelectronics Journal, 2021, 117, 105276.	1.1	2
203	High speed algorithm and VLSI architecture design for decoding BCH product codes. , 2002, , .		1
204	Low complexity iterative joint detection, decoding, and channel estimation for wireless MIMO system. Signal Processing Systems Design and Implementation (siPS), IEEE Workshop on, 2006, , .	0.0	1
205	Models for Architectural Power and Power Grid Noise Analysis on Data Bus. Journal of Signal Processing Systems, 2006, 44, 25-46.	1.0	1
206	A New Side Channel Resistant Scalar Point Multiplication Method for Binary Elliptic Curves. , 2006, , .		1
207	Further cost reduction of adaptive echo and next cancellers for high-speed Ethernet transceivers. , 2008, , .		1
208	High-speed implementation of Smith-Waterman algorithm for DNA sequence scanning in VLSI. , 2008, , .		1
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