

Mehdi Nasrollahpour

List of Publications by Year in descending order

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Version: 2024-02-01

19
papers

256
citations

1478505

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docs citations

20
times ranked

206
citing authors

#	ARTICLE	IF	CITATIONS
1	Analysis and Design Methodology of RF Energy Harvesting Rectifier Circuit for Ultra-Low Power Applications. IEEE Open Journal of Circuits and Systems, 2022, 3, 82-96.	1.9	11
2	A 30% Efficient High-Output Voltage Fully Integrated Self-Biased Gate RF Rectifier Topology for Neural Implants. IEEE Journal of Solid-State Circuits, 2022, 57, 3324-3335.	5.4	4
3	Ultra-compact dual-band smart NEMS magnetolectric antennas for simultaneous wireless energy harvesting and magnetic field sensing. Nature Communications, 2021, 12, 3141.	12.8	95
4	Simulation and Experimental Evaluation of Energy Harvesting Circuits with Magnetolectric Antennas. , 2021, , .		3
5	Magnetolectric Antenna for Miniaturized Acoustic Noise Dosimetry Applications. , 2021, 5, 1-4.		7
6	Integration of a novel CMOS-compatible magnetolectric antenna with a low-noise amplifier and a tunable input matching. Analog Integrated Circuits and Signal Processing, 2020, 105, 407-415.	1.4	12
7	A Radio Frequency Magnetolectric Antenna Prototyping Platform for Neural Activity Monitoring Devices with Sensing and Energy Harvesting Capabilities. Electronics (Switzerland), 2020, 9, 2123.	3.1	11
8	A Sub-Threshold Differential CMOS Schmitt Trigger with Adjustable Hysteresis Based on Body Bias Technique. Electronics (Switzerland), 2020, 9, 806.	3.1	6
9	NanoNeuroRFID: A Wireless Implantable Device Based on Magnetolectric Antennas. IEEE Journal of Electromagnetics, RF and Microwaves in Medicine and Biology, 2019, 3, 206-215.	3.4	66
10	Design and Analysis of 66GHz Voltage Controlled Oscillators for FMCW Radar Applications with Phase Noise Impact Consideration. , 2018, , .		1
11	Extra Bit Generation for High-Speed Time-Based Flash ADCs in 65nm CMOS. , 2018, , .		1
12	A Compact and Efficient Implementation of Modified MMF2 Encryption on FPGA. Canadian Journal of Electrical and Computer Engineering, 2018, 41, 3-7.	2.0	2
13	Cascode Stage Based LNA for Bluetooth Applications in 45 nm CMOS Technology. , 2017, , .		7
14	Low-power and high-frequency ring oscillator design in 65nm CMOS technology. , 2017, , .		7
15	A low phase noise CMOS oscillator with tail current-shaping technique in wireless implantable SoC applications. , 2017, , .		1
16	High speed, low offset, low power differential comparator with constant common mode voltage. , 2017, , .		5
17	A low-power, high-resolution, 1 GHz differential comparator with low-offset and low-kickback. , 2017, , .		5
18	A high-speed, low-offset and low-power differential comparator for analog to digital converters. , 2017, , .		7

#	ARTICLE	IF	CITATIONS
19	Fast transient response and high PSRR low drop-out voltage regulator. , 2016, , .		4