

Asmarashid Bin Ponniran

List of Publications by Year in descending order

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31
papers

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1937685

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31
all docs

31
docs citations

31
times ranked

146
citing authors

#	ARTICLE	IF	CITATIONS
1	DC-DC converter with 50 kHz-500 kHz range of switching frequency for passive component volume reduction. International Journal of Electrical and Computer Engineering, 2021, 11, 1114.	0.7	0
2	Frequency based signal processing technique for pulse modulation ground penetrating radar system. International Journal of Electrical and Computer Engineering, 2021, 11, 4104.	0.7	0
3	Evaluation of high power density achievement of optimum 4-level capacitor-clamped DC-DC boost converter with passive lossless snubber circuit by using Pareto-Front method. IET Power Electronics, 2020, 13, 40-49.	2.1	2
4	Decrystallization with high current pulses technique for capacity restoration of industrial nickel-cadmium battery. International Journal of Power Electronics and Drive Systems, 2020, 11, 1603.	0.6	0
5	Parameters observation of restoration capacity of industrial lead acid battery using high current pulses. International Journal of Power Electronics and Drive Systems, 2020, 11, 1596.	0.6	3
6	Design and development of PWM switching for 5-level multiphase interleaved DC/DC boost converter using FPGA. Indonesian Journal of Electrical Engineering and Computer Science, 2020, 17, 131.	0.8	0
7	An Improved Two-Switch Bridgeless PFC SEPIC Structure for Total Harmonic Distortion Reduction and Circulating Current Minimization. , 2019, , .		1
8	Modified single-switch bridgeless PFC SEPIC structure by eliminating circulating current and power quality improvement. IET Power Electronics, 2019, 12, 3792-3801.	2.1	7
9	Modeling of FPGA- and DSP-Based Pulse Width Modulation for Multi-Input Interleaved DC/DC Converter. International Review of Electrical Engineering, 2019, 14, 79.	0.2	2
10	4-level capacitor-clamped boost converter with hard-switching and soft-switching implementations. International Journal of Power Electronics and Drive Systems, 2019, 10, 288.	0.6	0
11	Characteristics of High Voltage Gain of Non-Isolated Inductor-Less DC-DC Converter. International Journal of Integrated Engineering, 2019, 11, .	0.4	0
12	Multilevel inverter with MPWM-LFT switching strategy for voltage THD minimization. International Journal of Power Electronics and Drive Systems, 2019, 10, 1461.	0.6	1
13	A Study of 4-level DC-DC Boost Inverter with Passive Component Reduction Consideration. Journal of Physics: Conference Series, 2018, 995, 012062.	0.4	4
14	A Single Phase 7-Level Cascade Inverter Topology with Reduced Number of Switches on Resistive Load by Using PWM. Journal of Physics: Conference Series, 2018, 995, 012061.	0.4	2
15	Symmetrical and Asymmetrical Multilevel Inverter Structures with Reduced Number of Switching Devices. Indonesian Journal of Electrical Engineering and Computer Science, 2018, 11, 144.	0.8	3
16	Harmonic Current Reduction Using Soft-Switching Active Power Filter. Advanced Science Letters, 2018, 24, 9203-9208.	0.2	0
17	Parameters design evaluation in 3-level flying capacitor boost converter. , 2017, , .		7
18	A Study on 3-phase Interleaved DC-DC Boost Converter Structure and Operation for Input Current Stress Reduction. International Journal of Power Electronics and Drive Systems, 2017, 8, 1948.	0.6	2

#	ARTICLE	IF	CITATIONS
19	Minimization of Passive Components in Multi-level Flying Capacitor DC-DC Converter. IEEJ Journal of Industry Applications, 2016, 5, 10-11.	1.1	11
20	Minimum Flying Capacitor for <i>N</i> -Level Capacitor DC/DC Boost Converter. IEEE Transactions on Industry Applications, 2016, 52, 3255-3266.	4.9	37
21	Volume reduction consideration in multilevel DC-DC boost converter. , 2016, , .		4
22	Purpose-built test rig for gas insulation breakdown tests under lightning impulse. , 2016, , .		3
23	Fundamental Operation of Marx Topology for High Boost Ratio DC-DC Converter. IEEJ Journal of Industry Applications, 2016, 5, 329-338.	1.1	4
24	Modular multi-stage Marx topology for high boost ratio DC/DC converter in HVDC. , 2015, , .		2
25	Interleaved high boost ratio Marx topology DC-DC converter. , 2015, , .		3
26	Minimum flying capacitor for N-level capacitor DC/DC boost converter. , 2015, , .		10
27	Size Reduction of DC-DC Converter using Flying Capacitor Topology with Small Capacitance. IEEJ Journal of Industry Applications, 2014, 3, 446-454.	1.1	18
28	A design of single axis sun tracking system. , 2011, , .		33
29	Data transfer rate, Central Processing Unit usage and Read Access Memory usage in Networked Control System via Industrial Ethernet. , 2010, , .		1
30	Study on the performance of underground XLPE cables in service based on tan delta and capacitance measurements. , 2008, , .		19
31	Performance of multi-column MOVs for C class protection of AC power circuits. , 2008, , .		2