

# Abbas Vafaei

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/734457/publications.pdf>

Version: 2024-02-01

20  
papers

258  
citations

1307543

7  
h-index

996954

15  
g-index

20  
all docs

20  
docs citations

20  
times ranked

215  
citing authors

#	ARTICLE	IF	CITATIONS
1	Novel RAM cell designs based on inherent capabilities of quantum-dot cellular automata. <i>Microelectronics Journal</i> , 2011, 42, 701-708.	2.0	77
2	Abnormal event detection in crowded scenes using one-class SVM. <i>Signal, Image and Video Processing</i> , 2018, 12, 1115-1123.	2.7	54
3	Anomaly detection and localization in crowded scenes using connected component analysis. <i>Multimedia Tools and Applications</i> , 2018, 77, 14767-14782.	3.9	30
4	Design of a novel reversible multiplier circuit using modified full adder. , 2010, , .		17
5	Reversible multipliers: Decreasing the depth of the circuit. , 2008, , .		16
6	Analysis and Evaluation of a New Algorithm Based Fault Tolerance for Computing Systems. <i>International Journal of Grid and High Performance Computing</i> , 2012, 4, 37-51.	0.9	9
7	Vision-based entrance detection in outdoor scenes. <i>Multimedia Tools and Applications</i> , 2018, 77, 26219-26238.	3.9	9
8	Registration of fluorescein angiography and optical coherence tomography images of curved retina via scanning laser ophthalmoscopy photographs. <i>Biomedical Optics Express</i> , 2020, 11, 3455.	2.9	9
9	ANALYSIS AND DESIGN OF AN ABFT AND PARITY-CHECKING TECHNIQUE IN HIGH PERFORMANCE COMPUTING SYSTEMS. <i>Journal of Circuits, Systems and Computers</i> , 2012, 21, 1250017.	1.5	7
10	A local image descriptor based on radial and angular gradient intensity histogram for blurred image matching. <i>Visual Computer</i> , 2019, 35, 1373-1391.	3.5	7
11	Designing QCA Delay-Insensitive Serial Adder. , 2008, , .		5
12	A framework for ABFT techniques in the design of fault-tolerant computing systems. <i>Eurasip Journal on Advances in Signal Processing</i> , 2011, 2011, .	1.7	4
13	A comparative study between single and multi-frame anomaly detection and localization in recorded video streams. <i>Journal of Visual Communication and Image Representation</i> , 2021, 79, 103232.	2.8	4
14	Nonparametric scene parsing in the images of buildings. <i>Computers and Electrical Engineering</i> , 2018, 70, 777-788.	4.8	3
15	A Fast Architecture for H.264/AVC Deblocking Filter Using a Clock Cycles Saving Process. <i>Journal of Signal Processing Systems</i> , 2012, 69, 189-196.	2.1	2
16	Energy minimization in the STT-RAM-based high-capacity last-level caches. <i>Journal of Supercomputing</i> , 2019, 75, 6831-6854.	3.6	2
17	Integer linear programming model for allocation and migration of data blocks in the STT-RAM-based hybrid caches. <i>IET Computers and Digital Techniques</i> , 2020, 14, 97-106.	1.2	2
18	2DIGH: a polar invariant local image descriptor based on joint histogram. <i>Visual Computer</i> , 2018, 34, 1579-1595.	3.5	1

#	ARTICLE	IF	CITATIONS
19	The Design of Reversible BCD Digit Adders: Decreasing the Depth of Circuit. , 2008, , .		0
20	Separating conflict misses to reduce miss-rate of caches through an unified replacement policy. , 2010, , .		0