Alon Amid

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/7328527/publications.pdf

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		2258059	2272923	
15	367	3	4	
papers	citations	h-index	g-index	
15	15	15	196	
all docs	docs citations	times ranked	citing authors	

#	Article	IF	Citations
1	COBRA: A Framework for Evaluating Compositions of Hardware Branch Predictors. , 2021, , .		O
2	FireMarshal: Making HW/SW Co-Design Reproducible and Reliable. , 2021, , .		3
3	Vertically Integrated Computing Labs Using Open-Source Hardware Generators and Cloud-Hosted FPGAs., 2021,,.		3
4	Accessible, FPGA Resource-Optimized Simulation of Multiclock Systems in FireSim. IEEE Micro, 2021, 41, 58-66.	1.8	4
5	A 16mm ² 106.1 GOPS/W Heterogeneous RISC-V Multi-Core Multi-Accelerator SoC in Low-Power 22nm FinFET., 2021, , .		6
6	Gemmini: Enabling Systematic Deep-Learning Architecture Evaluation via Full-Stack Integration. , 2021, , .		77
7	Programmable Fine-Grained Power Management and System Analysis of RISC-V Vector Processors in 28-nm FD-SOI. IEEE Solid-State Circuits Letters, 2020, 3, 210-213.	2.0	3
8	Invited: Chipyard - An Integrated SoC Research and Implementation Environment. , 2020, , .		4
9	Chipyard: Integrated Design, Simulation, and Implementation Framework for Custom SoCs. IEEE Micro, 2020, 40, 10-21.	1.8	114
10	FirePerf. , 2020, , .		12
11	FireSim: FPGA-Accelerated Cycle-Exact Scale-Out System Simulation in the Public Cloud. IEEE Micro, 2019, 39, 56-65.	1.8	4
12	Invited: Co-Design of Deep Neural Nets and Neural Net Accelerators for Embedded Vision Applications. , 2018, , .		10
13	FireSim: FPGA-Accelerated Cycle-Exact Scale-Out System Simulation in the Public Cloud. , 2018, , .		115
14	Co-design of deep neural nets and neural net accelerators for embedded vision applications. , 2018, , .		6
15	Policy based Role Centric Attribute Based Access Control model Policy RC-ABAC., 2015,,.		6