

Sattar Mirzakuchaki

List of Publications by Year in descending order

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Version: 2024-02-01

117
papers

1,923
citations

394421

19
h-index

302126

39
g-index

118
all docs

118
docs citations

118
times ranked

1426
citing authors

#	ARTICLE	IF	CITATIONS
1	Classification of Low and High Schizotypy Levels via Evaluation of Brain Connectivity. International Journal of Neural Systems, 2022, 32, 2250013.	5.2	14
2	New MGDI-based full adder cells for energy-efficient applications. International Journal of Electronics, 2021, 108, 462-477.	1.4	1
3	A low-power delay stage ring VCO based on wrap-gate CNTFET technology for X-band satellite communication applications. International Journal of Circuit Theory and Applications, 2021, 49, 142-158.	2.0	7
4	An ultra-miniature broadband operational transconductance amplifier utilizing 10 nm wrap-gate CNTFET technology. Analog Integrated Circuits and Signal Processing, 2021, 107, 423-434.	1.4	5
5	Design of InSb-based quantum-well laser with emission line at 5.6 μ m. European Physical Journal Plus, 2021, 136, 1.	2.6	0
6	Design and performance analysis of wrap-gate CNTFET-based ring oscillators for IoT applications. The Integration VLSI Journal, 2020, 70, 116-125.	2.1	12
7	A low power and energy efficient 4:2 precise compressor based on novel 14T hybrid full adders in 10Ånm wrap gate CNTFET technology. Microelectronics Journal, 2020, 104, 104892.	2.0	5
8	A digital watermarking method based on NSCT transform and hybrid evolutionary algorithms with neural networks. SN Applied Sciences, 2020, 2, 1.	2.9	11
9	A robust and energy-efficient near-threshold SRAM cell utilizing ballistic carbon nanotube wrap-gate transistors. AEU - International Journal of Electronics and Communications, 2019, 110, 152874.	2.9	13
10	A Machine Learning Approach to Brain Tumors Segmentation Using Adaptive Random Forest Algorithm. , 2019, , .		19
11	Design and Implementation of a Low-Power, Embedded CNN Accelerator on a Low-end FPGA. , 2019, , .		15
12	Development of a Robust Carrier Synchronizer for High Dynamic and Low Signal to Noise Ratio Signals in GPS Receivers. Wireless Personal Communications, 2019, 108, 1243-1259.	2.7	0
13	GAA CNT TFETs Structural Engineering: A Higher ON Current, Lower Ambipolarity. IEEE Transactions on Electron Devices, 2019, 66, 2822-2830.	3.0	20
14	List Scheduling for Heterogeneous Computing Systems Introducing a Performance-Effective Definition for Critical Path. , 2019, , .		3
15	Design and analysis of a novel low-power and energy-efficient 18T hybrid full adder. Microelectronics Journal, 2018, 74, 49-59.	2.0	51
16	Semi-analytical modeling of high performance nano-scale complementary logic gates utilizing ballistic carbon nanotube transistors. Physica E: Low-Dimensional Systems and Nanostructures, 2018, 104, 286-296.	2.7	29
17	Breaking a novel image encryption scheme based on an improper fractional order chaotic system. Multimedia Tools and Applications, 2017, 76, 1817-1826.	3.9	34
18	Modular neuron comprises of memristor-based synapse. Neural Computing and Applications, 2017, 28, 1-11.	5.6	23

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19	Breaking an image encryption technique based on neural chaotic generator. <i>Optik</i> , 2017, 140, 946-952.	2.9	11
20	IR small target detection based on human visual attention using pulsed discrete cosine transform. <i>IET Image Processing</i> , 2017, 11, 397-405.	2.5	10
21	An innovative smart package for liquid flow meters. , 2017, , .		1
22	Modelling and analysis of crosstalk induced noise effects in bundle SWCNT interconnects and its impact on signal stability. <i>Journal of Computational Electronics</i> , 2017, 16, 845-855.	2.5	10
23	PUF-based solutions for secure communications in Advanced Metering Infrastructure (AMI). <i>International Journal of Communication Systems</i> , 2017, 30, e3195.	2.5	23
24	An image encryption algorithm based on DNA sequence operations and cellular neural network. <i>Multimedia Tools and Applications</i> , 2017, 76, 13681-13701.	3.9	53
25	Design of Multiple Modulated Frequency Lock-In Amplifier for Tapping-Mode Atomic Force Microscopy Systems. <i>IEEE Transactions on Instrumentation and Measurement</i> , 2016, 65, 2284-2292.	4.7	25
26	Design and implementation of high throughput FPGA-based DVB-T system. <i>Computers and Electrical Engineering</i> , 2016, 51, 43-57.	4.8	1
27	Breaking an image encryption algorithm based on the new substitution stage with chaotic functions. <i>Optik</i> , 2016, 127, 5695-5701.	2.9	26
28	Infrared dim small target detection with high reliability using saliency map fusion. <i>IET Image Processing</i> , 2016, 10, 524-533.	2.5	20
29	Temperature Dependence of I-V Characteristics for CNT Based p-i-n TFET and n-i-n MOSFET. <i>ECS Journal of Solid State Science and Technology</i> , 2016, 5, M44-M50.	1.8	11
30	A novel FPGA design of a high dynamic human tracking system using particle filter. , 2016, , .		0
31	Two new energy-efficient full adder designs. , 2016, , .		5
32	A novel fast, low-power and high-performance XOR-XNOR cell. , 2016, , .		21
33	Design and Implementation of High Throughput, Robust, Parallel M-QAM Demodulator in Digital Communication Receivers. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2016, 63, 1295-1304.	5.4	8
34	Design of new digital blind feedforward timing recovery algorithm for linear modulations in digital communication receivers. <i>IET Communications</i> , 2016, 10, 1736-1743.	2.2	3
35	A novel image encryption algorithm based on discrete wavelet transform using two dimensional logistic map. , 2016, , .		4
36	A Ring Oscillator-Based PUF With Enhanced Challenge-Response Pairs. <i>Canadian Journal of Electrical and Computer Engineering</i> , 2016, 39, 174-180.	2.0	20

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37	A very low complexity IR-UWB transmitter with BPSK modulation for balanced antenna. IEEJ Transactions on Electrical and Electronic Engineering, 2015, 10, 144-148.	1.4	2
38	New Programmable CMOS Fuzzifier and C2V Circuits Applicable in FLC Chip for Signal Processing of MEMS Glucose Sensors. Applied Sciences (Switzerland), 2015, 5, 402-414.	2.5	3
39	Crosstalk bandwidth and stability analysis in graphene nanoribbon interconnects. Microelectronics Reliability, 2015, 55, 1262-1268.	1.7	13
40	Hyperbolic tangent passive resistive-type neuron. , 2015, , .		10
41	Optimization of the low-cost INS/GPS navigation system using ANFIS for high speed vehicle application. , 2015, , .		10
42	A novel color image encryption algorithm based on spatial permutation and quantum chaotic map. Nonlinear Dynamics, 2015, 81, 511-529.	5.2	130
43	Vertex angle image watermarking with optimal detector. Multimedia Tools and Applications, 2015, 74, 3077-3098.	3.9	4
44	A novel image encryption based on row-column, masking and main diffusion processes with hyper chaos. Multimedia Tools and Applications, 2015, 74, 781-811.	3.9	99
45	Design and Analysis of a High Force, Low Voltage and High Flow Rate Electro-Thermal Micropump. Micromachines, 2014, 5, 1323-1341.	2.9	4
46	Low-power and high-performance 5:2 compressors. , 2014, , .		3
47	A simple, sensitive and secure image encryption algorithm based on hyper-chaotic system with only one round diffusion process. Multimedia Tools and Applications, 2014, 71, 1469-1497.	3.9	176
48	A novel image encryption based on hash function with only two-round diffusion process. Multimedia Systems, 2014, 20, 45-64.	4.7	144
49	Geometric modelling of the wavelet coefficients for image watermarking using optimum detector. IET Image Processing, 2014, 8, 162-172.	2.5	29
50	A band structure study on inter-wall conductance of double-walled carbon nanotubes. Materials Science in Semiconductor Processing, 2014, 17, 222-227.	4.0	6
51	RGB color image encryption based on Choquet fuzzy integral. Journal of Systems and Software, 2014, 97, 128-139.	4.5	17
52	Using Local-Based Harris-PHOG Features in a Combination Framework for Human Action Recognition. Arabian Journal for Science and Engineering, 2014, 39, 903-912.	1.1	3
53	A fast color image encryption algorithm based on hyper-chaotic systems. Nonlinear Dynamics, 2014, 78, 995-1015.	5.2	129
54	On DPA-Resistive Implementation of FSR-based Stream Ciphers using SABL Logic Styles. International Journal of Computers, Communications and Control, 2014, 3, 324.	1.8	9

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55	High on/off current ratio in ballistic CNTFETs based on tuning the gate insulator parameters for different ambient temperatures. Applied Physics A: Materials Science and Processing, 2013, 113, 447-457.	2.3	20
56	Robust image watermarking using dihedral angle based on maximum likelihood detector. IET Image Processing, 2013, 7, 451-463.	2.5	12
57	Robust image watermarking in 3-D space using wavelet coefficients. , 2013, , .		0
58	Geometric modeling of the wavelet coefficients for image watermarking. , 2013, , .		1
59	Performance dependency on doping level of carbon nanotube for ballistic CNTFETs. Europhysics Letters, 2013, 103, 68009.	2.0	9
60	Homo- and hetero-epitaxial growth of InSb and Al _x In _{1-x} Sb layers by molecular beam epitaxy. , 2013, , .		0
61	A new high capacity and EMD-based image steganography scheme in spatial domain. , 2013, , .		6
62	A recognition approach using multilayer perceptron and keyboard dynamics patterns. , 2013, , .		3
63	Modification of photonic crystals for obtaining common band gaps for TE and TM waves. Canadian Journal of Physics, 2012, 90, 175-180.	1.1	5
64	Cell Number Optimization for Quantum Cellular Automata Based on AND-OR-INVERTER. Journal of Computational and Theoretical Nanoscience, 2012, 9, 627-630.	0.4	3
65	Heteroepitaxial growth of InSb directly on (001) GaAs without any buffer layer using growth rate ramping by MBE. , 2012, , .		2
66	A fast color image encryption algorithm based on coupled two-dimensional piecewise chaotic map. Signal Processing, 2012, 92, 1202-1215.	3.7	274
67	Image encryption scheme based on Choquet fuzzy integral with pseudo-random keystream generator. , 2011, , .		4
68	The impact of gate insulator dielectric constant on performance of CNTFETs at different ambient temperatures. , 2011, , .		1
69	Single electron fault modelling in quantum binary wire. Micro and Nano Letters, 2011, 6, 75-77.	1.3	6
70	Single Electron Fault Modeling in Basic Quantum Devices. Japanese Journal of Applied Physics, 2011, 50, 094401.	1.5	2
71	Study of Carbon Nanotube Field Effect Transistors Performance Based on Changes in Gate Parameters. Journal of Nanoscience and Nanotechnology, 2011, 11, 10424-10428.	0.9	4
72	Dependence of carbon nanotube field effect transistors performance on doping level of channel at different diameters: On/off current ratio. Applied Physics Letters, 2011, 99, .	3.3	13

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73	Hardware Realization of a Novel LSB Steganographic Algorithm. , 2011, , 1129-1133.		0
74	Single Electron Fault Modeling in Basic Quantum Devices. Japanese Journal of Applied Physics, 2011, 50, 094401.	1.5	0
75	Optimized Local Relocation for VLSI Circuit Modification Using Mean-Field Annealing. ETRI Journal, 2010, 32, 932-939.	2.0	1
76	Using Symlet Decomposition Method, Fuzzy Integral and Fisherface Algorithm for Face Recognition. , 2010, , .		1
77	Using subclass discriminant analysis, fuzzy integral and symlet decomposition for face recognition. , 2010, , .		0
78	Study of carbon nanotube field effect transistor performance based on changes in gate parameters. , 2010, , .		0
79	A novel image encryption algorithm based on hash function. , 2010, , .		30
80	A second-order Gm-C continuous time filter in mobile radio receiver architecture. , 2010, , .		1
81	Logic-based QCA realization of a 4. , 2010, , .		3
82	Design of A 100MHz – 1.66GHz, 0.13µm CMOS phase locked loop. , 2010, , .		2
83	A novel 3D wavelet-based method for blind digital video watermarking. , 2010, , .		1
84	On design of differential voltage controlled oscillators with single ended control line. , 2010, , .		0
85	Space radiation effects in QCA binary wire. , 2010, , .		0
86	Design and simulation of a CMOS DLL-based frequency multiplier. , 2010, , .		5
87	LUT-based QCA implementation of a 4×4 S-Box. , 2009, , .		0
88	Design of new CMOS current mode min and max circuits for FLC chip applications. , 2009, , .		6
89	Low cost implementation of Pomaranch S-Box. , 2009, , .		1
90	Three phase power line balancing based on smart energy meters. , 2009, , .		13

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91	Single Electron Fault in QCA Inverter Gate. , 2009, , .		2
92	Alamout: A new synchronous stream cipher with authentication. , 2009, , .		1
93	Power Amplifiers Linearization Based on Digital Predistortion with Memory Effects. , 2009, , .		0
94	QCA Implementation of A5/1 Stream Cipher. , 2009, , .		5
95	Single Electron Fault in QCA Binary Wire. , 2009, , .		1
96	Logic-based QCA implementation of a 4. , 2009, , .		4
97	QCA Implementation of Serpent Block Cipher. , 2009, , .		8
98	LUT-Based QCA Implementation of a 4#x0D7;4 S-Box. , 2009, , .		1
99	SEU Effects on QCA Circuits. , 2009, , .		9
100	A Gm-C anti-aliasing filter for digital radio receivers. , 2008, , .		4
101	QCA implementation of a MUX-Based FPGA CLB. , 2008, , .		33
102	Public key cryptography using semigroup actions and semirings. Journal of Discrete Mathematical Sciences and Cryptography, 2008, 11, 437-445.	0.8	2
103	Design and implementation of a 118 MHz 2D DCT processor. , 2008, , .		8
104	Design and Implementation of an Image CoProcessor. Lecture Notes in Computer Science, 2008, , 498-507.	1.3	0
105	Behavioral Modeling and Simulation of Semiconductor Devices and Circuits Using VHDL-AMS. , 2007, , .		1
106	Modeling the Effects of Deep Level Traps and Carrier Transport on the L-I characteristic, Transient Response and Wavelength Chirp of SCH-QW Lasers. , 2007, , .		0
107	High CMRR and low THD current-mode instrumentation amplifier using current inversion technique. , 2007, , .		9
108	Design and Implementation of a 50MHZ DXT CoProcessor. , 2007, , .		1

#	ARTICLE	IF	CITATIONS
109	A Solution to Remote Detecting of Illegal Electricity Usage Based on Smart Metering. , 2007, , .		47
110	Test access to deeply embedded analog terminals within an A/MS SoC. Journal of Zhejiang University: Science A, 2007, 8, 1543-1552.	2.4	1
111	Room-Temperature Infrared Radiation Detection Using n -Type PtSi/Porous Si. Sensor Letters, 2007, 5, 392-394.	0.4	1
112	Room-temperature gas-sensing ability of PtSi/porous Si Schottky junctions. IEEE Sensors Journal, 2006, 6, 146-150.	4.7	4
113	High CMRR, Low Power and Wideband Current-Mode Instrumentation Amplifier. , 2006, , .		11
114	Simulation of substrate coupling in mixed-signal IC's using an efficient and real-time macromodel. IEICE Electronics Express, 2006, 3, 509-516.	0.8	0
115	Selective area deposition of diamond thin films on patterns of porous silicon by hot-filament chemical vapor deposition. Applied Physics Letters, 1995, 67, 3557-3559.	3.3	15
116	A Low Power, High SFDR, ROM-Less Direct Digital Frequency Synthesizer. , 0, , .		5
117	A Low Power, High SFDR, ROM-Less Direct Digital Frequency Synthesizer. , 0, , .		16