

# Jaehyeok Yang

## List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	Time-Divided Spread-Spectrum Code-Based 400 fW-Detectable Multichannel fNIRS IC for Portable Functional Brain Imaging. IEEE Journal of Solid-State Circuits, 2016, 51, 484-495.	5.4	45
2	Analysis of a Frequency Acquisition Technique With a Stochastic Reference Clock Generator. IEEE Transactions on Circuits and Systems II: Express Briefs, 2012, 59, 336-340.	3.0	20
3	Phase-Rotator-Based All-Digital Phase-Locked Loop for a Spread-Spectrum Clock Generator. IEEE Transactions on Circuits and Systems II: Express Briefs, 2014, 61, 880-884.	3.0	7
4	A 20Gb/s transceiver with framed-pulsewidth modulation in 40nm CMOS. , 2018, , .		6
5	A 4 x 10-Gb/s Referenceless-and-Masterless Phase Rotator-Based Parallel Transceiver in 90-nm CMOS. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, , 1-11.	3.1	3
6	Reference-Less Time-Division Duplex Transceiver IC for a Renal Denervation System. IEEE Journal of Solid-State Circuits, 2019, 54, 1657-1668.	5.4	2
7	A Framed-Pulsewidth Modulation Transceiver for High-Speed Broadband Communication Links. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 2825-2835.	5.4	1
8	A 2.048 Mb/s Full-Duplex Free-Space Optical Transceiver IC for a Real-Time <i>In Vivo</i> Brain-Computer Interface Mouse Experiment Under Social Interaction. IEEE Journal of Solid-State Circuits, 2017, 52, 1007-1020.	5.4	0