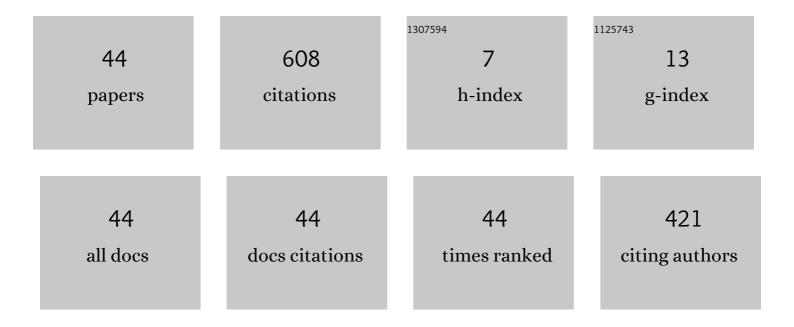
## Salvatore Monteleone

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/7256439/publications.pdf Version: 2024-02-01



#	Article	IF	CITATIONS
1	DNN Model Compression for IoT Domain-Specific Hardware Accelerators. IEEE Internet of Things Journal, 2022, 9, 6650-6662.	8.7	13
2	Exploiting the Approximate Computing Paradigm with DNN Hardware Accelerators. , 2022, , .		1
3	LAMBDA: An Open Framework for Deep Neural Network Accelerators Simulation. , 2021, , .		7
4	An Introduction to Patterns for the Internet of Robotic Things in the Ambient Assisted Living Scenario. Robotics, 2021, 10, 56.	3.5	5
5	Improving Inference Latency and Energy of Network-on-Chip based Convolutional Neural Networks through Weights Compression. , 2020, , .		4
6	DNNZip: Selective Layers Compression Technique in Deep Neural Network Accelerators. , 2020, , .		5
7	Framework for Design Exploration and Performance Analysis of RF-NoC Manycore Architecture. Journal of Low Power Electronics and Applications, 2020, 10, 37.	2.0	4
8	Implementing On-Chip Wireless Communication in Multi-stage Interconnection NoCs. Advances in Intelligent Systems and Computing, 2020, , 533-546.	0.6	1
9	Delta Multi-Stage Interconnection Networks for Scalable Wireless On-Chip Communication. Electronics (Switzerland), 2020, 9, 913.	3.1	7
10	Impact of Users' Beliefs in Text-Based Linguistic Interaction. IEEE Access, 2020, 8, 46861-46867.	4.2	0
11	Improving Inference Latency and Energy of DNNs through Wireless Enabled Multi-Chip-Module-based Architectures and Model Parameters Compression. , 2020, , .		12
12	Exploiting Data Resilience in Wireless Network-on-chip Architectures. ACM Journal on Emerging Technologies in Computing Systems, 2020, 16, 1-27.	2.3	10
13	Efficient Compression Technique for NoC-based Deep Neural Network Accelerators. , 2020, , .		0
14	NoCArc 2020 Content Announcement Page. , 2020, , .		0
15	Analyzing networks-on-chip based deep neural networks. , 2019, , .		6
16	GOOSE: goal oriented orchestration for smart environments. International Journal of Ad Hoc and Ubiquitous Computing, 2019, 32, 159.	0.5	2
17	Networks-on-Chip based Deep Neural Networks Accelerators for IoT Edge Devices. , 2019, , .		5
18	GOOSE: goal oriented orchestration for smart environments. International Journal of Ad Hoc and Ubiquitous Computing, 2019, 32, 159.	0.5	0

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#	Article	IF	CITATIONS
19	Improving Energy Efficiency in Wireless Network-on-Chip Architectures. ACM Journal on Emerging Technologies in Computing Systems, 2018, 14, 1-24.	2.3	22
20	Approximate Wireless Networks-on-Chip. , 2018, , .		5
21	Improving energy consumption of NoC based architectures through approximate communication. , 2018, , .		7
22	Packetization of Shared-Memory Traces for Message Passing Oriented NoC Simulation. Lecture Notes in Computer Science, 2018, , 311-325.	1.3	0
23	smARTworks: A Multi-sided Context-aware Platform for the Smart Museum. , 2018, , .		Ο
24	Cycle-Accurate Network on Chip Simulation with Noxim. ACM Transactions on Modeling and Computer Simulation, 2017, 27, 1-25.	0.8	162
25	Bus as a sensor: A mobile sensor nodes network for the air quality monitoring. , 2017, , .		12
26	User-Generated Services Composition in Smart Multi-User Environments. Journal of Sensor and Actuator Networks, 2017, 6, 20.	3.9	1
27	A Context-Aware Smart Parking System. , 2016, , .		9
28	A Context-Aware Solution to Improve Web Service Discovery and User-Service Interaction. , 2016, , .		6
29	Improving the energy efficiency of wireless Network on Chip architectures through online selective buffers and receivers shutdown. , 2016, , .		8
30	Performance analysis of visualmarkers for indoor navigation systems. Frontiers of Information Technology and Electronic Engineering, 2016, 17, 730-740.	2.6	18
31	Making Android Apps Data-Leak-Safe by Data Flow Analysis and Code Injection. , 2016, , .		8
32	Energy Efficient Transceiver in Wireless Network on Chip Architectures. , 2016, , .		10
33	Smart EDIFICE $\hat{a} \in \hat{~}$ Smart EveryDay interoperating future devICEs. , 2015, , .		2
34	User-Generated services: Policy Management and access control in a cross-domain environment. , 2015, , .		1
35	Computer Vision Based Indoor Navigation: A Visual Markers Evaluation. Advances in Intelligent Systems and Computing, 2015, , 165-173.	0.6	12
36	Noxim: An open, extensible and cycle-accurate network on chip simulator. , 2015, , .		187

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#	Article	IF	CITATIONS
37	A Low-resource and Scalable Strategy for Segment Partitioning of Many-core Nano Networks. , 2014, , .		2
38	Distributed topology discovery in self-assembled nano network-on-chip. Computers and Electrical Engineering, 2014, 40, 292-306.	4.8	7
39	Topology Discovery in Deadlock Free Self-assembled DNA Networks. Advances in Intelligent Systems and Computing, 2014, , 301-311.	0.6	Ο
40	SecureDroid: An Android security framework extension for context-aware policy enforcement. , 2013, , .		8
41	A first effort for a distributed segment-based approach on self-assembled nano networks. , 2013, , .		1
42	A Novel Approach to Web of Things: M2M and Enhanced Javascript Technologies. , 2012, , .		5
43	Cross-Platform Access Control for Mobile Web Applications. , 2012, , .		19
44	Evaluating impact of security on OPC UA performance. , 2010, , .		14