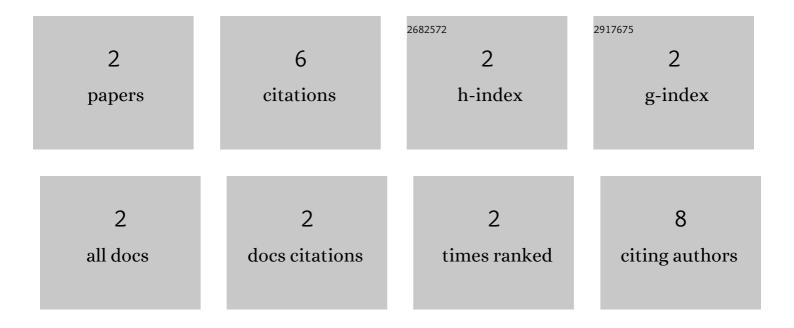
Sakthivel Palaniappan

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/7236886/publications.pdf Version: 2024-02-01



#	Article	IF	CITATIONS
1	Low power sleepy keeper technique based VLSI architecture of Viterbi decoder in WLANs. Australian Journal of Electrical and Electronics Engineering, 2020, 17, 263-268.	1.2	3
2	An Asynchronous Low Power and High Performance VLSI Architecture for Viterbi Decoder	2.1	3

An Asynchronous Low Power and High Performance VLSI Architecture for Viterbi Decoder Implemented with Quasi Delay Insensitive Templates. Scientific World Journal, The, 2015, 2015, 1-13. 2