Sakthivel Palaniappan

List of Publications by Year
in descending order

2	6	2682572	2917675
papers	citations	2 h-index	2 g-index

2	2	2	8
all docs	docs citations	times ranked	citing authors

#	Article	IF	Citations
1	Low power sleepy keeper technique based VLSI architecture of Viterbi decoder in WLANs. Australian Journal of Electrical and Electronics Engineering, 2020, 17, 263-268.	1.2	3
2	An Asynchronous Low Power and High Performance VLSI Architecture for Viterbi Decoder Implemented with Quasi Delay Insensitive Templates. Scientific World Journal, The, 2015, 2015, 1-13.	2.1	3