

Sakthivel Palaniappan

List of Publications by Year in descending order

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2
papers

6
citations

2682572

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2917675

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docs citations

2
times ranked

8
citing authors

#	ARTICLE	IF	CITATIONS
1	An Asynchronous Low Power and High Performance VLSI Architecture for Viterbi Decoder Implemented with Quasi Delay Insensitive Templates. Scientific World Journal, The, 2015, 2015, 1-13.	2.1	3
2	Low power sleepy keeper technique based VLSI architecture of Viterbi decoder in WLANs. Australian Journal of Electrical and Electronics Engineering, 2020, 17, 263-268.	1.2	3