

# Chester Liu

## List of Publications by Year in descending order

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7  
papers

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2682572  
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87  
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#	ARTICLE	IF	CITATIONS
1	SNAP: An Efficient Sparse Neural Acceleration Processor for Unstructured Sparse Deep Neural Network Inference. IEEE Journal of Solid-State Circuits, 2021, 56, 636-647.	5.4	34
2	A 135-mW 1.70TOPS Sparse Video Sequence Inference SoC for Action Classification. IEEE Journal of Solid-State Circuits, 2019, 54, 2081-2090.	5.4	0
3	SNAP: A 1.67 $\mu$ m <sup>2</sup> 21.55TOPS/W Sparse Neural Acceleration Processor for Unstructured Sparse Deep Neural Network Inference in 16nm CMOS. , 2019, , .		34
4	Inference and Learning Hardware Architecture for Neuro- Inspired Sparse Coding Algorithm. , 2018, , .		0
5	A 2.56-mm <sup>2</sup> 718GOPS Configurable Spiking Convolutional Sparse Coding Accelerator in 40-nm CMOS. IEEE Journal of Solid-State Circuits, 2018, 53, 2818-2827.	5.4	3
6	High-performance spiking neural net accelerators for embedded computer vision applications. , 2017, , .		0
7	A 1.40mm <sup>2</sup> 141mW 898GOPS sparse neuromorphic processor in 40nm CMOS. , 2016, , .		4