Xueyan Wang

List of Publications by Year
in descending order

11 papers	108 citations	1684188 5 h-index	1588992 8 g-index
12 all docs	12 docs citations	12 times ranked	84 citing authors

#	Article	IF	Citations
1	Exploiting Spin-Orbit Torque Devices As Reconfigurable Logic for Circuit Obfuscation. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2019, 38, 57-69.	2.7	21
2	Secure and Low-Overhead Circuit Obfuscation Technique with Multiplexers. , 2016, , .		20
3	Efficient Computation Reduction in Bayesian Neural Networks Through Feature Decomposition and Memorization. IEEE Transactions on Neural Networks and Learning Systems, 2021, 32, 1703-1712.	11.3	17
4	Reconfigurable and Dynamically Transformable In-Cache-MPUF System With True Randomness Based on the SOT-MRAM. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 2694-2706.	5.4	9
5	Is the Secure IC camouflaging really secure?. , 2016, , .		8
6	Triangle Counting Accelerations: From Algorithm to In-Memory Computing Architecture. IEEE Transactions on Computers, 2022, 71, 2462-2472.	3.4	8
7	Hardware Security in Spin-based Computing-in-memory. ACM Journal on Emerging Technologies in Computing Systems, 2020, 16, 1-18.	2.3	5
8	Accelerating Graph-Connected Component Computation With Emerging Processing-In-Memory Architecture. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2022, 41, 5333-5342.	2.7	5
9	Toward a Formal and Quantitative Evaluation Framework for Circuit Obfuscation Methods. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2019, 38, 1844-1857.	2.7	4
10	Parallelizing SAT-based de-camouflaging attacks by circuit partitioning and conflict avoiding. The Integration VLSI Journal, 2019, 67, 108-120.	2.1	4
11	An Empirical Study on Gate Camouflaging Methods Against Circuit Partition Attack. , 2017, , .		2