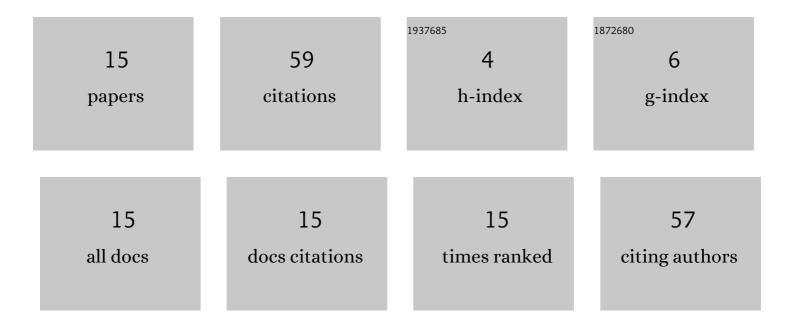
Kyung-Sik Choi

List of Publications by Year in descending order

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| # | Article | IF | CITATIONS |
|----|--|-----|-----------|
| 1 | A 0.3-to-1-GHz IoT Transmitter Employing Pseudo-Randomized Phase Switching Modulator and Single-Supply Class-G Harmonic Rejection PA. IEEE Journal of Solid-State Circuits, 2022, 57, 892-905. | 5.4 | 3 |
| 2 | A Fully Integrated 490-GHz CMOS Receiver Adopting Dual-Locking Receiver-Based FLL. IEEE Journal of Solid-State Circuits, 2022, 57, 2626-2639. | 5.4 | 0 |
| 3 | An LPWAN Radio with a Reconfigurable Data/Duty-Cycled-Wake-Up Receiver. , 2022, , . | | 7 |
| 4 | A 915 MHz IoT Transmitter Employing Frequency Tripler and Digitally Controlled Duty-Cycle/Phase Calibration. IEEE Journal of Solid-State Circuits, 2022, 57, 3336-3347. | 5.4 | 2 |
| 5 | Analysis and Design of Inductorless Transimpedance Amplifier Employing Nested Feedforward Noise-Canceling Amplifiers. IEEE Transactions on Microwave Theory and Techniques, 2022, 70, 3923-3932. | 4.6 | 5 |
| 6 | CMOS Fractional-N Frequency Synthesizer for UHF RFID Reader Applications With Transformer-Based ISF Manipulation VCO. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 4083-4087. | 3.0 | 0 |
| 7 | A Sub-nW Single-Supply 32-kHz Sub-Harmonic Pulse Injection Crystal Oscillator. IEEE Journal of Solid-State Circuits, 2021, 56, 1849-1858. | 5.4 | 6 |
| 8 | An Active Leakage Canceller Adopting Switched-Capacitor Digital Power Amplifier for UHF-RFID Transceiver. IEEE Microwave and Wireless Components Letters, 2021, 31, 604-607. | 3.2 | 3 |
| 9 | A Low-Noise and Fast-Settling UHF RFID Receiver With Digitally Controlled Leakage Cancellation. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 2810-2814. | 3.0 | 3 |
| 10 | A –123-dBm Sensitivity Split-Channel BFSK Reconfigurable Data/Wake-Up Receiver for Low-Power Wide-Area Networks. IEEE Journal of Solid-State Circuits, 2021, 56, 2656-2667. | 5.4 | 5 |
| 11 | 500 GHz CMOS heterodyne imager adopting fourth subharmonic passive mixer. Microwave and Optical Technology Letters, 2020, 62, 683-687. | 1.4 | 3 |
| 12 | A 5.5-dBm, 31.9% Efficiency 915-MHz Transmitter Employing Frequency Tripler and 207-\$mu\$ W Synthesizer. IEEE Microwave and Wireless Components Letters, 2020, 30, 90-93. | 3.2 | 6 |
| 13 | A 5 dBm 30.6% Efficiency 915 MHz Transmitter with \$210 mu mathrm{W}\$ ULP PLL Employing Frequency Tripler and Digitally Controlled Duty/Phase Calibration Buffer. , 2020, , . | | 2 |
| 14 | A Fully Integrated 490-GHz CMOS Heterodyne Imager Adopting Second Subharmonic Resistive Mixer Structure. IEEE Microwave and Wireless Components Letters, 2019, 29, 673-676. | 3.2 | 8 |
| 15 | A 915 MHz, 499 μW, –99 dBm, and 100 kbps BFSK Direct Conversion Receiver. , 2019, , . | | 6 |