

Ka Nang Alex Leung

List of Publications by Year in Descending Order

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

107
papers

2,909
citations

27
h-index

53
g-index

133
ext. papers

3,712
ext. citations

3.1
avg, IF

5.37
L-index

#	Paper	IF	Citations
107	A Hybrid Low-Dropout Regulator With Load Regulation Correction. <i>IEEE Access</i> , 2022 , 10, 25106-25113	3.5	1
106	An Analog-Assisted Digital LDO With Dynamic-Biasing Asynchronous Comparator. <i>IEEE Access</i> , 2022 , 1-1	3.5	0
105	Full-Wave Sense-FET-Based Inductor-Current Sensor With Wide Dynamic Range for Buck Converters. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2021 , 1-1	3.5	
104	Optimization of Charge Pump Based on Piecewise Modeling of Output-Voltage Ripple. <i>Energies</i> , 2021 , 14, 4809	3.1	0
103	Adaptive-biased sense-FETBased inductor-current sensor for 10-MHz buck converter. <i>International Journal of Circuit Theory and Applications</i> , 2020 , 48, 953-964	2	1
102	A flipped-voltage-follower-based low-dropout regulator with signal- and transient-current boosting. <i>Analog Integrated Circuits and Signal Processing</i> , 2020 , 105, 471-476	1.2	2
101	An Output-Capacitorless Low-Dropout Regulator with High Slew Rate and Unity-Gain Bandwidth 2020 ,		1
100	A 40nm CMOS Hysteretic Buck DC-DC Converter With Digital-Controlled Power-Driving-Tracked-Duration Current Pump. <i>IEEE Access</i> , 2020 , 8, 177374-177384	3.5	0
99	Bandwidth and Slew Rate Enhanced OTA With Sustainable Dynamic Bias. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2020 , 67, 635-639	3.5	3
98	A Single-Inductor Multiple-Output Buck/Boost DCDC Converter With Duty-Cycle and Control-Current Predictor. <i>IEEE Transactions on Power Electronics</i> , 2020 , 35, 12022-12039	7.2	16
97	Single-inductor multiple-output DCDC converter with duty-cycle-constrained comparator control. <i>Electronics Letters</i> , 2019 , 55, 617-619	1.1	3
96	Low-dropout regulator with dual cross-coupled current mirrors. <i>International Journal of Circuit Theory and Applications</i> , 2019 , 47, 1869-1876	2	5
95	A 3.3-MHz fast-response load-dependent-on/off-time buck-boost DC-DC converter with low-noise hybrid full-wave current sensor. <i>Microelectronics Journal</i> , 2018 , 74, 1-12	1.8	5
94	Fast-response full-wave inductor current sensor for 10MHz buck converter. <i>Electronics Letters</i> , 2018 , 54, 379-381	1.1	2
93	A 200-ps-Response-Time Output-Capacitorless Low-Dropout Regulator With Unity-Gain Bandwidth >100 MHz in 130-nm CMOS. <i>IEEE Transactions on Power Electronics</i> , 2018 , 33, 3232-3246	7.2	41
92	A Fully Integrated Low-Dropout Regulator With Differentiator-Based Active Zero Compensation. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2018 , 65, 3578-3591	3.9	19
91	A Double Gain-Boosted Amplifier with Widened Output Swing Based on Signal-and Transient-Current Boosting Technique in CMOS 130-nm Technology 2018 ,		1

90	Enhanced active-feedback frequency compensation with on-chip-capacitor reduction feature for amplifiers with large capacitive load. <i>International Journal of Circuit Theory and Applications</i> , 2017 , 45, 2119-2133	2	8
89	Limit-cycle oscillation reduction in high-efficiency wireless power receiver. <i>Electronics Letters</i> , 2017 , 53, 1152-1154	1.1	0
88	A full-load hybrid compensated ldo with output capacitance range of 0 to 1 2017 ,		1
87	A Single-Inductor Multiple-Output Auto-Buck-Boost DCDC Converter With Autophase Allocation. <i>IEEE Transactions on Power Electronics</i> , 2016 , 31, 2296-2313	7.2	28
86	A Two-Stage Large-Capacitive-Load Amplifier With Multiple Cross-Coupled Small-Gain Stages. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2016 , 24, 2580-2592	2.6	7
85	. <i>IEEE Transactions on Power Electronics</i> , 2016 , 31, 6362-6371	7.2	44
84	. <i>IEEE Transactions on Power Electronics</i> , 2016 , 31, 7857-7875	7.2	27
83	Digitally-assisted constant-on-time dynamic-biasing technique for bandwidth and slew-rate enhancement in ultra-low-power low-dropout regulator. <i>International Journal of Circuit Theory and Applications</i> , 2016 , 44, 504-513	2	4
82	Power-area-efficient transient-improved capacitor-free FVF-LDO with digital detecting technique. <i>Electronics Letters</i> , 2015 , 51, 94-96	1.1	6
81	A robust cross-regulation-suppressed single-inductor multiple-output dc-dc converter with duty-regulated comparator control 2015 ,		1
80	. <i>IEEE Journal of Solid-State Circuits</i> , 2015 , 50, 2750-2757	5.5	27
79	Design considerations of STCB OTA in CMOS 65nm with large capacitive loads 2015 ,		2
78	Gain and slew rate enhancement for amplifiers through current starving and feeding 2015 ,		4
77	A fixed-frequency auto-buck-boost SIMO DC-DC converter with duty-cycle redistribution and duty-predicted current control 2015 ,		2
76	A 5.4-mW 180-cm Transmission Distance 2.5-Mb/s Advanced Techniques-Based Novel Intrabody Communication Receiver Analog Front End. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2015 , 23, 2829-2841	2.6	10
75	A Signal- and Transient-Current Boosting Amplifier for Large Capacitive Load Applications. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2014 , 61, 2777-2785	3.9	25
74	An Area-Efficient 96.5%-Peak-Efficiency Cross-Coupled Voltage Doubler With Minimum Supply of 0.8 V. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2014 , 61, 656-660	3.5	10
73	Load regulation cancellation based on adaptive body bias for STC-LDOs 2014 ,		1

72	Analysis of CMOS low-dropout regulator [Power-supply rejection ratio 2014 ,		3
71	A 124-dB double-gain-boosted cascode amplifier with 92% rail-to-rail output swing 2014 ,		1
70	A CMOS voltage buffer with slew-rate enhancement. <i>International Journal of Electronics</i> , 2014 , 101, 820-830		1
69	23 μ W 8.9-effective number of bit 1.1MS/s successive approximation register analog-to-digital converter with an energy-efficient digital-to-analog converter switching scheme. <i>Journal of Engineering</i> , 2014 , 2014, 420-425		0.7
68	A transient-improved low-dropout regulator with nested flipped voltage follower structure. <i>International Journal of Circuit Theory and Applications</i> , 2013 , 41, 1016-1026	2	9
67	A class-E power amplifier for wireless biomedical systems. <i>Analog Integrated Circuits and Signal Processing</i> , 2013 , 75, 525-529	1.2	3
66	A 25mA CMOS LDO with 85dB PSRR at 2.5MHz 2013 ,		6
65	Sub-mW Ψ LC Ψ Dual-Input Injection-Locked Oscillator for Autonomous WBSNs. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2013 , 21, 546-553	2.6	5
64	Improved active-diode circuit used in voltage doubler. <i>International Journal of Circuit Theory and Applications</i> , 2012 , 40, 165-173	2	2
63	A CMOS voltage regulator for passive RFID tag ICs. <i>International Journal of Circuit Theory and Applications</i> , 2012 , 40, 329-340	2	11
62	A Fast-Response Pseudo-PWM Buck Converter With PLL-Based Hysteresis Control. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2012 , 20, 1167-1174	2.6	37
61	A fully integrated CMOS direct-conversion transmitter front-end for WiMAX. <i>International Journal of Electronics</i> , 2012 , 99, 255-266	1.2	1
60	Digital-Control Single-Inductor Triple-Output DCDC Converter With Pre-Sub-Period Inductor-Current Control. <i>IEEE Transactions on Power Electronics</i> , 2012 , 27, 2028-2042	7.2	13
59	A low-power MICS fractional-N frequency synthesizer for implantable biomedical systems 2012 ,		1
58	Development of energy-efficient fast-transient CMOS low-dropout regulators for SoC applications 2011 ,		2
57	Dynamic Bias-Current Boosting Technique for Ultralow-Power Low-Dropout Regulator in Biomedical Applications. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2011 , 58, 174-178	3.5	52
56	A gain-optimising regulated charge pump. <i>International Journal of Electronics</i> , 2011 , 98, 197-205	1.2	3
55	A 0.5-Hz High-Pass Cutoff Dual-Loop Transimpedance Amplifier for Wearable NIR Sensing Device. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2010 , 57, 531-535	3.5	14

54	High slew-rate voltage follower based on double-sided dynamic biasing. <i>Electronics Letters</i> , 2010 , 46, 824	1.1	1
53	25 mA LDO with 83 dB PSRR at 30 MHz for WiMAX. <i>Electronics Letters</i> , 2010 , 46, 1080	1.1	9
52	A Low-Power Fast-Transient 90-nm Low-Dropout Regulator With Multiple Small-Gain Stages. <i>IEEE Journal of Solid-State Circuits</i> , 2010 ,	5.5	58
51	A Chip-Area Efficient Voltage Regulator for VLSI Systems. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2010 , 18, 1757-1762	2.6	2
50	Power-on-reset circuit with power-off auto-discharging path for passive RFID tag ICs 2010 ,		5
49	A Fast-Transient Low-Dropout Regulator With Load-Tracking Impedance Adjustment and Loop-Gain Boosting Technique. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2010 , 57, 757-761	3.5	28
48	. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2010 , 57, 2312-2319	3.9	55
47	An Output-Capacitorless Low-Dropout Regulator With Direct Voltage-Spike Detection. <i>IEEE Journal of Solid-State Circuits</i> , 2010 , 45, 458-466	5.5	171
46	A Low-Power Continuously-Calibrated Clock Recovery Circuit for UHF RFID EPC Class-1 Generation-2 Transponders. <i>IEEE Journal of Solid-State Circuits</i> , 2010 , 45, 587-599	5.5	17
45	A 6- μ W Chip-Area-Efficient Output-Capacitorless LDO in 90-nm CMOS Technology. <i>IEEE Journal of Solid-State Circuits</i> , 2010 , 45, 1896-1905	5.5	195
44	A 90nm RFID tag baseband processor with novel PIE decoder and uplink clock generator 2010 ,		5
43	A fast-transient LDO based on buffered flipped voltage follower 2010 ,		6
42	Analysis of the behaviours of phase and amplitude mismatch compensators to achieve 82.5 dB image rejection ratio. <i>International Journal of Electronics</i> , 2010 , 97, 553-568	1.2	
41	Slew-rate enhancement circuit of CMOS current-mirror amplifier by edge-detecting technique 2010 ,		1
40	A sub-1 μ A improved-transient CMOS low-dropout regulator without minimal ESR requirement 2009 ,		1
39	A fold-back current-limit circuit with load-insensitive quiescent current for CMOS low dropout regulator 2009 ,		1
38	A Low-power signal processing front-end and decoder for UHF passive RFID transponders 2009 ,		3
37	Robust and low complexity packet detector design for MB-OFDM UWB 2009 ,		3

36	Low power injection locked oscillators for MICS standard 2009 ,		6
35	Compensation-Capacitor Free Pseudo Three-Stage Amplifier with Large Capacitive Loads 2008 ,		2
34	A slew-rate enhancement technique based on current comparator and capacitive-coupled push-pull output stage for CMOS amplifiers 2008 ,		1
33	Design of passive UHF RFID tag in 130nm CMOS technology 2008 ,		6
32	A Fully Differential Band-Selective Low-Noise Amplifier for MB-OFDM UWB Receivers. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2008 , 55, 653-657	3.5	15
31	Development of Single-Transistor-Control LDO Based on Flipped Voltage Follower for SoC. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2008 , 55, 1392-1401	3.9	111
30	A Low-Power CMOS Front-End for Photoplethysmographic Signal Acquisition With Robust DC Photocurrent Rejection. <i>IEEE Transactions on Biomedical Circuits and Systems</i> , 2008 , 2, 280-8	5.1	42
29	Design and challenges of passive UHF RFID tag in 90nm CMOS technology 2008 ,		1
28	Design of a Low-Voltage CMOS Charge Pump 2008 ,		3
27	Temperature-compensated CMOS ring oscillator for power-management circuits. <i>Electronics Letters</i> , 2007 , 43, 786	1.1	10
26	An Adaptive Current-Boosting Voltage Buffer for Low-Power Low Dropout Regulators 2007 ,		7
25	A Low-Dropout Regulator for SoC With $\zeta Q\zeta$ -Reduction. <i>IEEE Journal of Solid-State Circuits</i> , 2007 , 42, 658-664	5.4	136
24	Integrated ramp generator with auto-set hysteretic comparator for PWM voltage regulators. <i>Electronics Letters</i> , 2007 , 43, 1384	1.1	6
23	A low power CMOS front-end for photoplethysmographic signal acquisition with robust DC Photocurrent Rejection 2007 ,		2
22	A voltage-mode PWM buck regulator with end-point prediction. <i>IEEE Transactions on Circuits and Systems Part 2: Express Briefs</i> , 2006 , 53, 294-298		45
21	A NIR CMOS preamplifier with DC photocurrent rejection for pulsed light source 2006 ,		2
20	A 1-V integrated current-mode boost converter in standard 3.3/5-V CMOS technologies. <i>IEEE Journal of Solid-State Circuits</i> , 2005 , 40, 2265-2274	5.5	82
19	An integrated CMOS current-sensing circuit for low-voltage current-mode buck regulator. <i>IEEE Transactions on Circuits and Systems Part 2: Express Briefs</i> , 2005 , 52, 394-397		75

18	Design of low-power analog drivers based on slew-rate enhancement circuits for CMOS low-dropout regulators. <i>IEEE Transactions on Circuits and Systems Part 2: Express Briefs</i> , 2005 , 52, 563-567		61
17	A 2-V 23- μ A 5.3-ppm/ $^{\circ}$ C curvature-compensated CMOS bandgap voltage reference. <i>IEEE Journal of Solid-State Circuits</i> , 2003 , 38, 561-564	5.5	132
16	A CMOS voltage reference based on weighted ∇ /sub GS/ for CMOS low-dropout linear regulators. <i>IEEE Journal of Solid-State Circuits</i> , 2003 , 38, 146-150	5.5	117
15	A capacitor-free CMOS low-dropout regulator with damping-factor-control frequency compensation. <i>IEEE Journal of Solid-State Circuits</i> , 2003 , 38, 1691-1702	5.5	280
14	A dual-path bandwidth extension amplifier topology with dual-loop parallel compensation. <i>IEEE Journal of Solid-State Circuits</i> , 2003 , 38, 1739-1744	5.5	54
13	A sub-1-V 15-ppm/ $^{\circ}$ C CMOS bandgap voltage reference without requiring low threshold voltage device. <i>IEEE Journal of Solid-State Circuits</i> , 2002 , 37, 526-530	5.5	203
12	Analysis of multistage amplifier-frequency compensation. <i>IEEE Transactions on Circuits and Systems Part 1: Regular Papers</i> , 2001 , 48, 1041-1056		191
11	Nested Miller compensation in low-power CMOS design. <i>IEEE Transactions on Circuits and Systems Part 2: Express Briefs</i> , 2001 , 48, 388-394		67
10	Three-stage large capacitive load amplifier with damping-factor-control frequency compensation. <i>IEEE Journal of Solid-State Circuits</i> , 2000 , 35, 221-230	5.5	170
9	Low-Voltage Analog Circuit Techniques Using Bias-Current Re-Utilization, Self-Biasing and Signal Superposition		
8	Design considerations of recent advanced low-voltage low-temperature-coefficient CMOS bandgap voltage reference		26
7	A low-voltage CMOS low-dropout regulator with enhanced loop response		4
6	Analysis of low-dropout regulator topologies for low-voltage regulation		1
5	Design of a 1.5-V high-order curvature-compensated CMOS bandgap reference		2
4	A 2-V 23- μ A 5.3-ppm/ $^{\circ}$ C 4th-order curvature-compensated CMOS bandgap reference		5
3	Optimum nested Miller compensation for low-voltage low-power CMOS amplifier design		1
2	Right-half-plane zero removal technique for low-voltage low-power nested Miller compensation CMOS amplifier		4
1	A novel frequency compensation technique for low-voltage low-dropout regulator		3

