Huei Wang

List of Publications by Year
in descending order

341 papers	6,003 citations	66315 42 h-index	143943 57 g-index
342 all docs	342 docs citations	342 times ranked	2443 citing authors

#	Article	IF	Citations
1	60-GHz Four-Element Phased-Array Transmit/Receive System-in-Package Using Phase Compensation Techniques in 65-nm Flip-Chip CMOS Process. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 743-756.	2.9	177
2	Millimeter-Wave Low Power and Miniature CMOS Multicascode Low-Noise Amplifiers with Noise Reduction Topology. IEEE Transactions on Microwave Theory and Techniques, 2009, 57, 3049-3059.	2.9	102
3	Millimeter-Wave CMOS Power Amplifiers With High Output Power and Wideband Performances. IEEE Transactions on Microwave Theory and Techniques, 2013, 61, 4520-4533.	2.9	96
4	Design and Analysis of a 55â€“71-GHz Compact and Broadband Distributed Active Transformer Power Amplifier in 90-nm CMOS Process. IEEE Transactions on Microwave Theory and Techniques, 2009, 57, 1637-1646.	2.9	89
5	A 5-GHz low phase noise differential colpitts CMOS VCO. IEEE Microwave and Wireless Components Letters, 2005, 15, 327-329.	2.0	88
6	A 24-GHz 3.9-dB NF low-noise amplifier using 0.18 /spl mu/m CMOS technology. IEEE Microwave and Wireless Components Letters, 2005, 15, 448-450.	2.0	85
7	A 50 to 94-GHz CMOS SPDT Switch Using Traveling-Wave Concept. IEEE Microwave and Wireless Components Letters, 2007, 17, 130-132.	2.0	83
8	A High-Range-Accuracy and High-Sensitivity Harmonic Radar Using Pulse Pseudorandom Code for Bee Searching. IEEE Transactions on Microwave Theory and Techniques, 2013, 61, 666-675.	2.9	83
9	Millimeter-Wave MMIC Passive HEMT Switches Using Traveling-Wave Concept. IEEE Transactions on Microwave Theory and Techniques, 2004, 52, 1798-1808.	2.9	82
10	Analysis of Multiconductor Coupled-Line Marchand Baluns for Miniature MMIC Design. IEEE Transactions on Microwave Theory and Techniques, 2007, 55, 1190-1199.	2.9	80
11	Broadband Balanced Frequency Doublers With Fundamental Rejection Enhancement Using a Novel Compensated Marchand Balun. IEEE Transactions on Microwave Theory and Techniques, 2013, 61, 1913-1923.	2.9	79
12	Electronically Switchable Bandpass Filters Using Loaded Stepped-Impedance Resonators. IEEE Transactions on Microwave Theory and Techniques, 2006, 54, 4193-4201.	2.9	77
13	A 0.6-22-GHz broadband CMOS distributed amplifier. , 0, , .		73
14	A Modified Wilkinson Power Divider With Isolation Bandwidth Improvement. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 2768-2780.	2.9	72
15	A DC-11.5 GHz Low-Power, Wideband Amplifier Using Splitting-Load Inductive Peaking Technique. IEEE Microwave and Wireless Components Letters, 2008, 18, 482-484.	2.0	68
16	A 0.3-25-GHz ultra-wideband mixer using commercial 0.18-/spl mu/m CMOS technology. IEEE Microwave and Wireless Components Letters, 2004, 14, 522-524.	2.0	67
17	An analysis of miniaturized dual-mode bandpass filter structure using shunt-capacitance perturbation. IEEE Transactions on Microwave Theory and Techniques, 2005, 53, 861-867.	2.9	63
18	Analysis and Design of Millimeter-Wave Low-Voltage CMOS Cascode LNA With Magnetic Coupled Technique. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 4066-4079.	2.9	63

#	Article	IF	Citations
19	Design and analysis of DC-to-14-GHz and 22-GHz CMOS cascode. IEEE Journal of Solid-State Circuits, 2004, 39, 1370-1374.	3.5	61
20	A 30â€“100 GHz Wideband Sub-Harmonic Active Mixer in 90 nm CMOS Technology. IEEE Microwave and Wireless Components Letters, 2008, 18, 554-556.	2.0	60
21	Design and Analysis of 24-GHz Active Isolator and Quasi-Circulator. IEEE Transactions on Microwave Theory and Techniques, 2015, 63, 2638-2649.	2.9	60
22	Power-amplifier modules covering 70-113 GHz using MMICs. IEEE Transactions on Microwave Theory and Techniques, 2001, 49, 9-16.	2.9	59
23	Compact and broad-band millimeter-wave monolithic transformer balanced mixers. IEEE Transactions on Microwave Theory and Techniques, 2005, 53, 3106-3114.	2.9	56
24	A 77-GHz 2T6R Transceiver With Injection-Lock Frequency Sextupler Using 65-nm CMOS for Automotive Radar System Application. IEEE Transactions on Microwave Theory and Techniques, 2016, 64, 3031-3048.	2.9	55
25	A miniature broad-band pHEMT MMIC balanced distributed doubler. IEEE Transactions on Microwave Theory and Techniques, 2003, 51, 1257-1261.	2.9	54
26	A Novel Distributed Amplifier With High Gain, Low Noise, and High Output Power in ${hbox{0.18-}} mu{hbox {m}}$ CMOS Technology. IEEE Transactions on Microwave Theory and Techniques, 2013, 61, 1533-1542.	2.9	54
27	38-GHz Phased Array Transmitter and Receiver Based on Scalable Phased Array Modules With Endfire Antenna Arrays for 5G MMW Data Links. IEEE Transactions on Microwave Theory and Techniques, 2021, 69, 980-999.	2.9	53
28	Design of a V -Band 20-dBm Wideband Power Amplifier Using Transformer-Based Radial Power Combining in 90-nm CMOS. IEEE Transactions on Microwave Theory and Techniques, 2016, 64, 4545-4560.	2.9	51
29	Triple-push oscillator approach: theory and experiments. IEEE Journal of Solid-State Circuits, 2001, 36, 1472-1479.	3.5	50
30	Design and Analysis for a 60-GHz Low-Noise Amplifier With RF ESD Protection. IEEE Transactions on Microwave Theory and Techniques, 2009, 57, 298-305.	2.9	50
31	1024-QAM High Image Rejection E-Band Sub-Harmonic IQ Modulator and Transmitter in 65-nm CMOS Process. IEEE Transactions on Microwave Theory and Techniques, 2013, 61, 3974-3985.	2.9	50
32	Forward-looking automotive radar using a W-band single-chip transceiver. IEEE Transactions on Microwave Theory and Techniques, 1995, 43, 1659-1668.	2.9	48
33	A W-Band Medium Power Amplifier in 90 nm CMOS. IEEE Microwave and Wireless Components Letters, 2008, 18, 818-820.	2.0	48
34	A miniature Q-band low noise amplifier using 0.13-/spl mu/m CMOS technology. IEEE Microwave and Wireless Components Letters, 2006, 16, 327-329.	2.0	46
35	A Low-Power 114-GHz Pushâ€“Push CMOS VCO UsingLCSource Degeneration. IEEE Journal of Solid-State Circuits, 2007, 42, 1230-1239.	3.5	46
36	A 21 GHz Complementary Transformer Coupled CMOS VCO. IEEE Microwave and Wireless Components Letters, 2008, 18, 278-280.	2.0	46

#	Article	IF	Citations
37	Design and Analysis of CMOS Frequency Dividers With Wide Input Locking Ranges. IEEE Transactions on Microwave Theory and Techniques, 2009, 57, 3060-3069.	2.9	46
38	Design and analysis of a 44-GHz MMIC low-loss built-in linearizer for high-linearity medium power amplifiers. IEEE Transactions on Microwave Theory and Techniques, 2006, 54, 2487-2496.	2.9	45
39	A Low Power Folded Mixer for UWB System Applications in 0.18-mum CMOS Technology. IEEE Microwave and Wireless Components Letters, 2007, 17, 367-369.	2.0	45
40	Novel Miniature and Broadband Millimeter-Wave Monolithic Star Mixers. IEEE Transactions on Microwave Theory and Techniques, 2008, 56, 793-802.	2.9	44
41	A 86 to 108 GHz Amplifier in 90 nm CMOS. IEEE Microwave and Wireless Components Letters, 2008, 18, 124-126.	2.0	44
42	A 2â€“40 GHz Active Balun Using 0.13 $mu{m m}$ CMOS Process. IEEE Microwave and Wireless Components Letters, 2009, 19, 164-166.	2.0	44
43	A 60 GHz Low Phase Variation Variable Gain Amplifier in 65 nm CMOS. IEEE Microwave and Wireless Components Letters, 2014, 24, 457-459.	2.0	44
44	Design and Analysis of a 0.8–77.5-GHz Ultra-Broadband Distributed Drain Mixer Using 0.13-<formula formulatype="inline"><tex Notation="TeX">mu</tex></formula>m CMOS Technology. IEEE Transactions on Microwave Theory and Techniques, 2009, 57, 562-572.	2.9	43
45	A 1.5â€“9.6 GHz Monolithic Active Quasi-Circulator in 0.18 $mu{m m}$ CMOS Technology. IEEE Microwave and Wireless Components Letters, 2008, 18, 797-799.	2.0	41
46	MMICs in the millimeter-wave regime. IEEE Microwave Magazine, 2009, 10, 99-117.	0.7	41
47	Flip-Chip-Assembled W-Band CMOS Chip Modules on Ceramic Integrated Passive Device With Transition Compensation for Millimeter-Wave System-in-Package Integration. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 766-777.	2.9	41
48	Design and analysis of novel high-gain and broad-band GaAs pHEMT MMIC distributed amplifiers with traveling-wave gain stages. IEEE Transactions on Microwave Theory and Techniques, 2003, 51, 2188-2196.	2.9	40
49	Broad-Band HBT BPSK and IQ Modulator MMICs and Millimeter-Wave Vector Signal Characterization. IEEE Transactions on Microwave Theory and Techniques, 2004, 52, 908-919.	2.9	40
50	A noise optimization formulation for CMOS low-noise amplifiers with on-chip low-Q inductors. IEEE Transactions on Microwave Theory and Techniques, 2006, 54, 1554-1560.	2.9	40
51	A miniature 25-GHz 9-dB CMOS cascaded single-stage distributed amplifier. IEEE Microwave and Wireless Components Letters, 2004, 14, 554-556.	2.0	39
52	Millimeter-wave MMIC single-pole-double-throw passive HEMT switches using impedance-transformation networks. IEEE Transactions on Microwave Theory and Techniques, 2003, 51, 1076-1085.	2.9	38
53	A 50 to 70 GHz Power Amplifier Using 90 nm CMOS Technology. IEEE Microwave and Wireless Components Letters, 2009, 19, 45-47.	2.0	38
54	A 60 GHz Broadband Low-Noise Amplifier With Variable-Gain Control in 65 nm CMOS. IEEE Microwave and Wireless Components Letters, 2011, 21, 610-612.	2.0	38

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55	A 1-17-GHz InGaP-GaAs HBT MMIC analog multiplier and mixer with broad-band input-matching networks. IEEE Transactions on Microwave Theory and Techniques, 2002, 50, 2564-2568.	2.9	37
56	A 77-GHz MMIC power amplifier for automotive radar applications. IEEE Microwave and Wireless Components Letters, 2003, 13, 143-145.	2.0	37
57	A 17â€“35 GHz Broadband, High Efficiency PHEMT Power Amplifier Using Synthesized Transformer Matching Technique. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 112-119.	2.9	37
58	Analysis and Design of Bandpass Single-Poleâ€“Double-Throw FET Filter-Integrated Switches. IEEE Transactions on Microwave Theory and Techniques, 2007, 55, 1601-1610.	2.9	36
59	Analysis and Design of Millimeter-Wave Low-Power CMOS LNA With Transformer-Multicascode Topology. IEEE Transactions on Microwave Theory and Techniques, 2011, 59, 3441-3454.	2.9	36
60	Design of a 60-GHz High-Output Power Stacked- FET Power Amplifier Using Transformer-Based Voltage-Type Power Combining in 65-nm CMOS. IEEE Transactions on Microwave Theory and Techniques, 2018, 66, 4595-4607.	2.9	36
61	A W-band subharmonically pumped monolithic GaAs-based HEMT gate mixer. IEEE Microwave and Wireless Components Letters, 2004, 14, 313-315.	2.0	35
62	A 131 GHz push-push VCO in 90-nm CMOS technology. , 0, , .		35
63	A 60â€“110 GHz Transmission-Line Integrated SPDT Switch in 90 nm CMOS Technology. IEEE Microwave and Wireless Components Letters, 2010, 20, 85-87.	2.0	34
64	A 4â€“17 GHz Darlington Cascode Broadband Medium Power Amplifier in 0.18-mum CMOS Technology. IEEE Microwave and Wireless Components Letters, 2010, 20, 43-45.	2.0	33
65	Design and Analysis of Stacked Power Amplifier in Series-Input and Series-Output Configuration. IEEE Transactions on Microwave Theory and Techniques, 2007, 55, 2802-2812.	2.9	32
66	A 78-114 GHz monolithic subharmonically pumped GaAs-based HEMT diode mixer. IEEE Microwave and Wireless Components Letters, 2002, 12, 209-211.	2.0	31
67	A 57â€“78 GHz Frequency Tripler MMIC in 65-nm CMOS. IEEE Microwave and Wireless Components Letters, 2016, 26, 723-725.	2.0	31
68	A 0.5-14-GHz 10.6-dB CMOS cascode distributed amplifier. , 0, , .		30
69	Phase-Noise Reduction of X-Band Pushâ€“Push Oscillator With Second-Harmonic Self-Injection Techniques. IEEE Transactions on Microwave Theory and Techniques, 2007, 55, 66-77.	2.9	30
70	A V-Band On-Wafer Near-Field Antenna Measurement System Using an IC Probe Station. IEEE Transactions on Antennas and Propagation, 2013, 61, 2058-2067.	3.1	30
71	A <italic>Ka</italic>-Band Transformer-Based Doherty Power Amplifier for Multi-Gb/s Application in 90-nm CMOS. IEEE Microwave and Wireless Components Letters, 2018, 28, 1134-1136.	2.0	30
72	A 50 GHz Divide-by-4 Injection Lock Frequency Divider Using Matching Method. IEEE Microwave and Wireless Components Letters, 2008, 18, 344-346.	2.0	29

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73	A Fundamental 90-GHz CMOS VCO Using New Ring-Coupled Quad. IEEE Microwave and Wireless Components Letters, 2007, 17, 226-228.	2.0	27
74	A 60-GHz Frequency Tripler With Gain and Dynamic-Range Enhancement. IEEE Transactions on Microwave Theory and Techniques, 2011, 59, 660-671.	2.9	27
75	Bidirectional Diode-Triggered Silicon-Controlled Rectifiers for Low-Voltage ESD Protection. IEEE Electron Device Letters, 2012, 33, 1360-1362.	2.2	27
76	A High-Efficiency, Broadband CMOS Power Amplifier for Cognitive Radio Applications. IEEE Transactions on Microwave Theory and Techniques, 2010, , .	2.9	26
77	A 22â€“31 GHz Distributed Amplifier Based on High-Pass Transmission Lines Using 0.18 <formula formulatype="inline"><tex Notation="TeX">$mu{m m}$</tex></formula> CMOS Technology. IEEE Microwave and Wireless Components Letters, 2011, 21, 160-162.	2.0	26
78	A Fully SiP Integrated V-Band Butler Matrix End-Fire Beam-Switching Transmitter Using Flip-Chip Assembled CMOS Chips on LTCC. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 1424-1436.	2.9	26
79	<tex>K</tex>-Band HBT and HEMT Monolithic Active Phase Shifters Using Vector Sum Method. IEEE Transactions on Microwave Theory and Techniques, 2004, 52, 1414-1424.	2.9	25
80	FET-integrated CPW and the application in filter synthesis design method on traveling-wave switch above 100 GHz. IEEE Transactions on Microwave Theory and Techniques, 2006, 54, 2090-2097.	2.9	25
81	Ring-Based Triple-Push VCOs With Wide Continuous Tuning Ranges. IEEE Transactions on Microwave Theory and Techniques, 2009, 57, 2173-2183.	2.9	25
82	A low-voltage and variable-gain distributed amplifier for 3.1-10.6 GHz UWB systems. IEEE Microwave and Wireless Components Letters, 2006, 16, 179-181.	2.0	24
83	Analysis and Design of Reduced-Size Marchand Rat-Race Hybrid for Millimeter-Wave Compact Balanced Mixers in 130-nm CMOS Process. IEEE Transactions on Microwave Theory and Techniques, 2009, 57, 1966-1977.	2.9	24
84	A High Linearity 24-GHz Down-Conversion Mixer Using Distributed Derivative Superposition Technique in 0.18- $mu ext{m}$ CMOS Process. IEEE Microwave and Wireless Components Letters, 2018, 28, 49-51.	2.0	24
85	A K-Band Power Amplifier with 26-dBm Output Power and 34% PAE with Novel Inductance-based Neutralization in 90-nm CMOS. , 2018, , .		24
86	A broadband PHEMT MMIC distributed doubler using high-pass drain line topology. IEEE Microwave and Wireless Components Letters, 2004, 14, 201-203.	2.0	24
87	A 22-GHz push-push CMOS oscillator using micromachined inductors. IEEE Microwave and Wireless Components Letters, 2005, 15, 859-861.	2.0	23
88	K-band MMIC active band-pass filters. IEEE Microwave and Wireless Components Letters, 2005, 15, 19-21.	2.0	23
89	Design and Analysis of Down-Conversion Gate/Base-Pumped Harmonic Mixers Using Novel Reduced-Size 180^circ Hybrid With Different Input Frequencies. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 2473-2485.	2.9	23
90	A harmonic injection-locked frequency divider in 0.18-/spl mu/m SiGe BiCMOS. IEEE Microwave and Wireless Components Letters, 2006, 16, 561-563.	2.0	22

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91	A Compact 60 GHz Integrated Up-Converter Using Miniature Transformer Couplers With 5 dB Conversion Gain. IEEE Microwave and Wireless Components Letters, 2008, 18, 641-643.	2.0	22
92	A 15-50 GHz broadband resistive FET ring mixer using 0.18-µm CMOS technology. , 2010, , .		22
93	A Wide Gain Control Range V-Band CMOS Variable-Gain Amplifier With Built-In Linearizer. IEEE Transactions on Microwave Theory and Techniques, 2013, 61, 902-913.	2.9	22
94	A 180-GHz monolithic sub-harmonic InP-based HEMT diode mixer. , 1999, 9, 529-531.		21
95	A 46-GHz Direct Wide Modulation Bandwidth ASK Modulator in 0.13-mum CMOS Technology. IEEE Microwave and Wireless Components Letters, 2007, 17, 691-693.	2.0	21
96	A Ka-Band Stacked Power Amplifier with 24.8-dBm Output Power and 24.3% PAE in 65-nm CMOS Technology. , 2019, , .		21
97	A Submilliwatt K-Band Low-Noise Amplifier for Next Generation Radio Astronomical Receivers in 65-nm CMOS Process. IEEE Microwave and Wireless Components Letters, 2020, 30, 669-672.	2.0	21
98	40-GHz MMIC SPDT and Multiple-Port Bandpass Filter-Integrated Switches. IEEE Transactions on Microwave Theory and Techniques, 2007, 55, 2691-2699.	2.9	20
99	Low Insertion-Loss Single-Poleâ€“Double-Throw Reduced-Size Quarter-Wavelength HEMT Bandpass Filter Integrated Switches. IEEE Transactions on Microwave Theory and Techniques, 2008, 56, 3028-3038.	2.9	20
100	A 24 GHz CMOS power amplifier using reversed body bias technique to improve linearity and power added efficiency. , 2012, , .		20
101	A W-band power amplifier in 65-nm CMOS with 27GHz bandwidth and 14.8dBm saturated output power. , 2012, , .		19
102	A V-Band Power Amplifier With 23.7-dBm Output Power, 22.1% PAE, and 29.7-dB Gain in 65-nm CMOS Technology. IEEE Transactions on Microwave Theory and Techniques, 2019, 67, 4418-4426.	2.9	19
103	A High Gain, High Power K-Band Frequency Doubler in 0.18 <formula formulatype="inline"><tex Notation="TeX">$mu{m m}$</tex></formula> CMOS Process. IEEE Microwave and Wireless Components Letters, 2010, 20, 522-524.	2.0	18
104	Signal processing for harmonic pulse radar based on spread spectrum technology. IET Radar, Sonar and Navigation, 2014, 8, 242-250.	0.9	18
105	A <formula formulatype="inline"><tex Notation="TeX">W</tex> </formula>-band High LO-to-RF Isolation Triple Cascode Mixer With Wide IF Bandwidth. IEEE Transactions on Microwave Theory and Techniques, 2014, 62, 1506-1514.	2.9	18
106	A Q-band LNA with 55.7% bandwidth for radio astronomy applications in 0.15-Î¼m GaAs pHEMT process. , 2016, , .		18
107	Highly Selective Microstrip Bandpass Filters in Ka-Band. , 2002, , .		17
108	A 66–72 GHz divide-by-3 injection-locked frequency divider in 0.13-μm CMOS technology. , 2007, , .		17

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109	A Novel Reduced-Size Rat-Race Broadside Coupler and Its Application for CMOS Distributed Sub-Harmonic Mixer. IEEE Microwave and Wireless Components Letters, 2008, 18, 194-196.	2.0	17
110	V-Band High Data-Rate I/Q Modulator and Demodulator With a Power-Locked Loop LO Source in 0.15-/spl mu/m GaAs pHEMT Technology. IEEE Transactions on Microwave Theory and Techniques, 2013, 61, 2670-2684.	2.9	17
111	A 3-33 GHz PHEMT MMIC distributed drain mixer. , 0, , .		16
112	A V-band quasi-optical GaAs HEMT monolithic integrated antenna and receiver front end. IEEE Transactions on Microwave Theory and Techniques, 2003, 51, 2461-2468.	2.9	16
113	18-26 GHz low-noise amplifiers using 130- and 90-nm bulk CMOS technologies. , 0, , .		16
114	A Ku-band CMOS low-noise amplifier. , 2005, , .		16
115	A 71â€“76 GHz CMOS variable gain amplifier using current steering technique. , 2008, , .		16
116	A 24 GHz low power VCO with transformer feedback. , 2009, , .		16
117	A 98/196 GHz Low Phase Noise Voltage Controlled Oscillator With a Mode Selector Using a 90 nm CMOS Process. IEEE Microwave and Wireless Components Letters, 2009, 19, 170-172.	2.0	16
118	A Novel 30â€“90-GHz Singly Balanced Mixer With Broadband LO/IF. IEEE Transactions on Microwave Theory and Techniques, 2016, 64, 4611-4623.	2.9	16
119	A K-band transformer based power amplifier with 24.4-dBm output power and 28% PAE in 90-nm CMOS technology. , 2017, , .		16
120	A Broadband Transformer-Based Power Amplifier Achieving 24.5-dBm Output Power Over 24â€“41 GHz in 65-nm CMOS Process. IEEE Microwave and Wireless Components Letters, 2021, 31, 308-311.	2.0	16
121	A 45-GHz Quadrature Voltage Controlled Oscillator with a Reflection-Type IQ Modulator in 0.13-Î¼m CMOS Technology. , 2006, , .		15
122	A 19.1-dBm Fully-Integrated 24 GHz Power Amplifier Using 0.18-Â¿m CMOS Technology. , 2008, , .		15
123	A 22-dBm 24-GHz power amplifier using 0.18-µm CMOS technology. , 2010, , .		15
124	A 22.5-dB gain, 20.1-dBm output power K-band power amplifier in 0.18-µm CMOS. , 2010, , .		15
125	A high gain broadband LNA in GaAs 0.15-Î¼m pHEMT process using inductive feedback gain compensation for radio astronomy applications. , 2015, , .		15
126	A high-gain low-noise distributed amplifier with low DC power in 0.18-µm CMOS for vital sign detection radar. , 2015, , .		15

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127	Design and Analysis of W-Band Injection-Locked Frequency Divider Using Split Transformer-Coupled Oscillator Technique. IEEE Transactions on Microwave Theory and Techniques, 2018, 66, 177-186.	2.9	15
128	An Inductive-Neutralized 26-dBm K -/K_{a} -Band Power Amplifier With 34% PAE in 90-nm CMOS. IEEE Transactions on Microwave Theory and Techniques, 2019, 67, 4427-4440.	2.9	15
129	A 3.7â€“43.7-GHz Low-Power Consumption Variable Gain Distributed Amplifier in 90-nm CMOS. IEEE Microwave and Wireless Components Letters, 2021, 31, 169-172.	2.0	15
130	A W-band GCPW MMIC Diode Tripler. , 2002, , .		14
131	A 71â€“80 GHz Amplifier Using 0.13-$mu{hbox{m}}$ CMOS Technology. IEEE Microwave and Wireless Components Letters, 2007, 17, 685-687.	2.0	14
132	A 14â¼23 GHz CMOS MMIC distributed doubler with a 22-dB fundamental rejection. , 2008, , .		14
133	Analysis of a New 33â€“58-GHz Doubly Balanced Drain Mixer in 90-nm CMOS Technology. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 1057-1068.	2.9	14
134	A 35.7â€“64.2 GHz low power Miller Divider with Weak Inversion Mixer in 65 nm CMOS. IEEE Microwave and Wireless Components Letters, 2016, 26, 948-950.	2.0	14
135	38-GHz CMOS Linearized Receiver With IM3 Suppression, P _{1 dB}/IP3/RR3 Enhancements, and Mitigation of QAM Constellation Diagram Distortion in 5G MMW Systems. IEEE Transactions on Microwave Theory and Techniques, 2020, 68, 2779-2795.	2.9	14
136	A miniature low-insertion-loss, high-power CMOS SPDT switch using floating-body technique for 2.4- and 5.8-GHz applications. , 0, , .		13
137	A Compact 35-65 GHz Up-Conversion Mixer with Integrated Broadband Transformers in 0.18-um SiGe BiCMOS Technology. , 0, , .		13
138	A 10.8-GHz CMOS Low-Noise Amplifier Using Parallel-Resonant Inductor. , 2007, , .		13
139	A 4â€“41 GHz Singly Balanced Distributed Mixer Using GaAs pHEMT Technology. IEEE Microwave and Wireless Components Letters, 2007, 17, 136-138.	2.0	13
140	60 GHz Double-Balanced Gate-Pumped Down-Conversion Mixers With a Combined Hybrid on 130 nm CMOS Processes. IEEE Microwave and Wireless Components Letters, 2010, 20, 160-162.	2.0	13
141	Design and Analysis of Novel Linearization Technique of Cascode Cell in a 60-GHz CMOS Demodulator. IEEE Transactions on Microwave Theory and Techniques, 2011, 59, 456-465.	2.9	13
142	Novel MMIC Power Amplifier Linearization Utilizing Input Reflected Nonlinearity. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 542-554.	2.9	13
143	Bee Searching Radar With High Transmitâ€“Receive Isolation Using Pulse Pseudorandom Code. IEEE Transactions on Microwave Theory and Techniques, 2016, 64, 4324-4335.	2.9	13
144	A 5.4-mW LNA using 0.35- Î¼m SiGe BiCMOS technology for 3.1-10.6-GHz UWB wireless receivers. , 0, , .		12

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145	A Band-Pass Filter-Integrated Switch Using Field-Effect Transistors and Its Power Analysis. , 2006, , .		12
146	A new feedback method for power amplifier with unilateralization and improved output return loss. IEEE Transactions on Microwave Theory and Techniques, 2006, 54, 1590-1597.	2.9	12
147	Topology Analysis and Design of Passive HEMT Millimeter-Wave Multiple-Port Switches. IEEE Transactions on Microwave Theory and Techniques, 2008, 56, 1545-1554.	2.9	12
148	A 60 GHz Sub-Harmonic Resistive FET Mixer Using 0.13 $mu{m m}$ CMOS Technology. IEEE Microwave and Wireless Components Letters, 2011, 21, 562-564.	2.0	12
149	K-band CMOS power amplifier with adaptive bias for enhancement in back-off efficiency. , 2011, , .		12
150	A 24-GHz low power and high isolation active quasi-circulator. , 2012, , .		12
151	A <inline-formula> <tex-math notation="LaTeX">${Ka}$ </tex-math> </inline-formula>-Band Dual-Mode Power Amplifier in 65-nm CMOS Technology. IEEE Microwave and Wireless Components Letters, 2018, 28, 708-710.	2.0	12
152	A 5.8-GHz two-stage high-linearity low-voltage low noise amplifier in a 0.35-Î¼m CMOS technology [WLANs]. , 0, , .		11
153	A 30-GHz Low-Phase-Noise 0.35-μm CMOS Push-Push Oscillator Using Micromachined Inductors. , 2006, , .		11
154	A 57-66 GHz Vector Sum Phase Shifter with Low Phase/Amplitude Error Using a Wilkinson Power Divider with LHTL/RHTL Elements. , 2011, , .		11
155	Design and Analysis of Digital-Assisted Bandwidth-Enhanced Miller Divider in $ {hbox {0.18-}}mu{hbox {m}}$ CMOS Process. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 3769-3777.	2.9	11
156	An ultra low-power Q-band LNA with 50% bandwidth in WIN GaAs 0.1-μm pHEMT process. , 2013, , .		11
157	A Compact 40-GHz Doherty Power Amplifier With 21% PAE at 6-dB Power Back Off in 0.1-mu m GaAs pHEMT Process. IEEE Microwave and Wireless Components Letters, 2019, 29, 545-547.	2.0	11
158	A 28-GHz High Linearity Up-Conversion Mixer Using Second-Harmonic Injection Technique in 28-nm CMOS Technology. IEEE Microwave and Wireless Components Letters, 2021, 31, 276-279.	2.0	11
159	Broad-band MMICs based on modified loss-compensation method using 0.35-/spl mu/m SiGe BiCMOS technology. IEEE Transactions on Microwave Theory and Techniques, 2005, 53, 496-505.	2.9	10
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