

# Jiannan Huang

## List of Publications by Year in descending order

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10  
papers

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citations

1307594

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#	ARTICLE	IF	CITATIONS
1	A Digitally Assisted Multiplexed Neural Recording System With Dynamic Electrode Offset Cancellation via an LMS Interference-Canceling Filter. IEEE Journal of Solid-State Circuits, 2022, 57, 953-964.	5.4	15
2	A 112-dB SFDR 89-dB SNDR VCO-Based Sensor Front-End Enabled by Background-Calibrated Differential Pulse Code Modulation. IEEE Journal of Solid-State Circuits, 2021, 56, 1046-1057.	5.4	23
3	A 178.9-dB FoM 128-dB SFDR VCO-Based AFE for ExG Readouts With a Calibration-Free Differential Pulse Code Modulation Technique. IEEE Journal of Solid-State Circuits, 2021, 56, 3236-3246.	5.4	13
4	A 94.2-dB SNDR 142.6- $\hat{1}$ / <sub>4</sub> W VCO-Based Audio ADC With a Split-ADC Differential Pulse Code Modulation Architecture. IEEE Solid-State Circuits Letters, 2021, 4, 121-124.	2.0	7
5	Neural recording and stimulation using wireless networks of microimplants. Nature Electronics, 2021, 4, 604-614.	26.0	81
6	A 174.7-dB FoM, 2 <sup>nd</sup> -Order VCO-Based ExG-to-Digital Front-End Using a Multi-Phase Gated-Inverted-Ring Oscillator Quantizer. IEEE Transactions on Biomedical Circuits and Systems, 2021, 15, 1283-1294.	4.0	13
7	A $\hat{\sim}$ 105dB THD 88dB-SNDR VCO-Based Sensor Front-End Enabled by Background-Calibrated Differential Pulse Code Modulation. , 2020, , .		4
8	A Scalable and Low Stress Post-CMOS Processing Technique for Implantable Microsensors. Micromachines, 2020, 11, 925.	2.9	16
9	An Implantable Wireless Network of Distributed Microscale Sensors for Neural Applications. , 2019, , .		39
10	A 0.01-mm <sup>2</sup> Mostly Digital Capacitor-Less AFE for Distributed Autonomous Neural Sensor Nodes. IEEE Solid-State Circuits Letters, 2018, 1, 162-165.	2.0	32