

Jiannan Huang

List of Publications by Year in descending order

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papers

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1307594

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11
times ranked

195
citing authors

#	ARTICLE	IF	CITATIONS
1	Neural recording and stimulation using wireless networks of microimplants. Nature Electronics, 2021, 4, 604-614.	26.0	81
2	An Implantable Wireless Network of Distributed Microscale Sensors for Neural Applications. , 2019, , .		39
3	A 0.01-mm ² Mostly Digital Capacitor-Less AFE for Distributed Autonomous Neural Sensor Nodes. IEEE Solid-State Circuits Letters, 2018, 1, 162-165.	2.0	32
4	A 112-dB SFDR 89-dB SNDR VCO-Based Sensor Front-End Enabled by Background-Calibrated Differential Pulse Code Modulation. IEEE Journal of Solid-State Circuits, 2021, 56, 1046-1057.	5.4	23
5	A Scalable and Low Stress Post-CMOS Processing Technique for Implantable Microsensors. Micromachines, 2020, 11, 925.	2.9	16
6	A Digitally Assisted Multiplexed Neural Recording System With Dynamic Electrode Offset Cancellation via an LMS Interference-Canceling Filter. IEEE Journal of Solid-State Circuits, 2022, 57, 953-964.	5.4	15
7	A 178.9-dB FoM 128-dB SFDR VCO-Based AFE for ExG Readouts With a Calibration-Free Differential Pulse Code Modulation Technique. IEEE Journal of Solid-State Circuits, 2021, 56, 3236-3246.	5.4	13
8	A 174.7-dB FoM, 2 nd -Order VCO-Based ExG-to-Digital Front-End Using a Multi-Phase Gated-Inverted-Ring Oscillator Quantizer. IEEE Transactions on Biomedical Circuits and Systems, 2021, 15, 1283-1294.	4.0	13
9	A 94.2-dB SNDR 142.6- μ W VCO-Based Audio ADC With a Split-ADC Differential Pulse Code Modulation Architecture. IEEE Solid-State Circuits Letters, 2021, 4, 121-124.	2.0	7
10	A \sim 105dB THD 88dB-SNDR VCO-Based Sensor Front-End Enabled by Background-Calibrated Differential Pulse Code Modulation. , 2020, , .		4